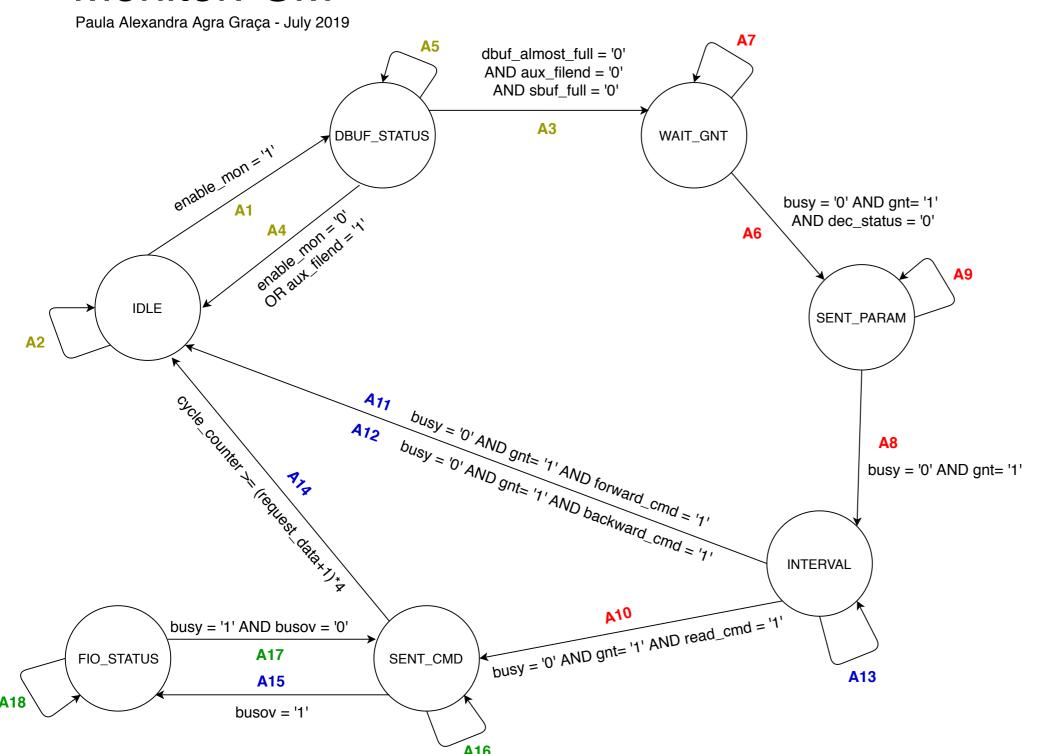
MonitorFSM



Outputs:

rog <-

req <= '0' busiv <= '0' dbuf_wr <= '0' forward_lcd <= '0' backward_lcd <= '0'

A1

A2

req <= '0'
busiv<= '0'
dbuf_wr<= '0'
aux_filend <= '0'
cycle_counter <= x"00000000" & '0'
forward_lcd <= '0';
backward_lcd<= '0'

req <= '1' busiv <= '0' dbuf_wr <= '0'

A3, A7, A8, A13

A4, A5, A16

req <= '0' busiv <= '0' dbuf_wr <= '0'

A6

req <= '1'
ctrl <= '0'
busiv <= '1'
busi <= request_data
dbuf_wr <= '0'

A9

req <= '1' busiv <= '0' dbuf_wr <= '0'

req <= '1' ctrl <= '1' busiv <= '1' busi <= x"02" dbuf_wr <= '0' read cmd <= '0'

A10

A11

forward_lcd <= '1'
req <= '1'
ctrl <= '1'
busiv <= '1'
busi <= x"04"
dbuf wr <= '0'

A12

backward_lcd <= '1'
req <= '1'
ctrl <= '1'
busiv <= '1'
busi <= x"05"
dbuf_wr <= '0'
backward_cmd <= '0'

A14

A15

busiv <= '0' dbuf_wr <= '1'

A17

busiv <= '0' dbuf_wr <= '0' cycle_counter <= cycle_counter + dword_size

A18

busiv <= '0'
dbuf_wr <= '1'
cycle_counter <=
cycle_counter + dword_size</pre>