



STM32F407ZGT6 V1.2

MODE	BOOT0	BOOT1
FLASH	0	X
ISP	1	0
SRAM	1	1

Title		
Sta	Number	Revision
E		
Date	19 Dec 2016	Sheet of 1
File	F:\STM32F407ZGT6_V1.2\PCB\STM32F407ZGT6_V1.2_PCB.dwg	Drawn by: J. J. J.