CSEE W4823x

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**HW5 Designing a Slave Controller for the Philips I2C Bus Protocol**

We assign code as following,



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MSB LSB

{ SDA\_in, SCL\_in, Byte\_done, addr\_match/bit\_send }

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MSB LSB

{ SDA\_out, SDA\_enable, data\_out, addr\_out }

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(i) The symbolic state diagram of the Moore FSM.