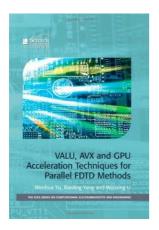
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SciTech Publishing Inc, United States, 2013. Hardback. Book Condition: New. New.. 254 x 178 mm. Language: English. Brand New Book. This book introduces a general hardware acceleration technique that can significantly speed up finite difference time domain (FDTD) simulations and their applications to engineering problems without requiring any additional hardware devices. It provides detailed code demonstration and analysis to help readers get the step-by-step guidelines using the vector arithmetic logic units (VALU) in the CPU and demonstrates the detailed...

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