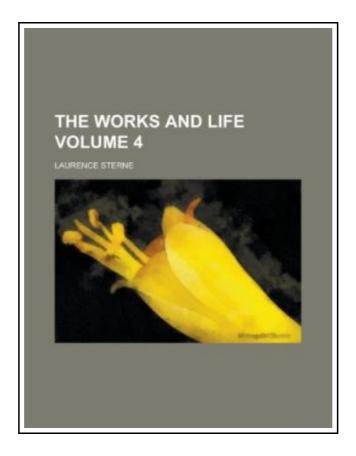
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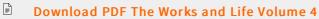
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RareBooksClub. Paperback. Book Condition: New. This item is printed on demand. Paperback. 26 pages. Original publisher: Adelphi, MD: Army Research Laboratory, 2009 OCLC Number: (OCoLC)424642972 Subject: Automatic pilot (Airplanes) Excerpt: . . . 9. The Microcontroller The same microcontroller was used to process data from the encoders, receive data from the computer, and drive the stepper motors to a specific position. Virtually any microcontroller with two or more RS-232 ports and three or more PWM generators can be used. An RS-232 interface at 115, 200 baud is connected to the U. S. Digital AD5 Quadrature Encoder device, and another RS-232 interface at 115, 200 baud is connected to the computer. Three PWM outputs are connected to the Geckodrive 201 drivers to control pitch, roll, and yaw stepper motors, respectively. Three general-purpose IO lines, which control motor direction, are connected to the Geckodrive 201 drivers as well. The frequency of the microcontrollers master clock and PWM generators determine the frequency range of the stepper motors as well as the resolution of stepper motor control. The microcontroller was configured to operate at 48, 054, 857 Hz. This is a function of the 18. 432-MHz crystal oscillator and the phase locked loop multiplier and divider hardware within the microcontroller. The divider register was set to 14, and the multiplier was set to 73. Finally, a postscale division of 2 was applied, i. e., Freq PLL OSC MClk. (3) Freq Postscale Divider The master clock frequency of the microcontroller dictates the range of frequencies that can be chosen for PWM generators. Each PWM generator has separate registers for the 16-bit period and duty values. Furthermore, there exists a register to divide the master clock for the PWM generators frequency. Presently, the master clock is divided by 256, slowing the...



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