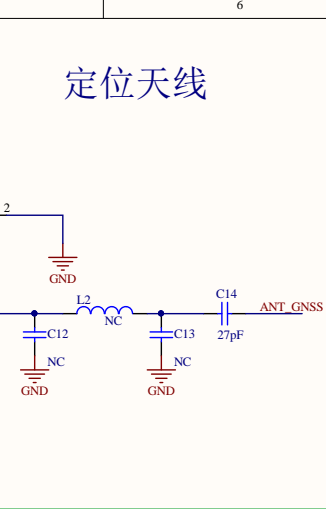


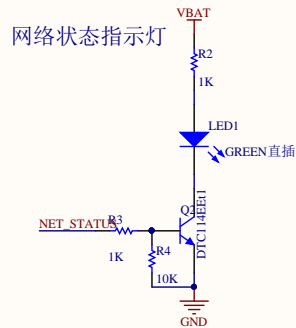
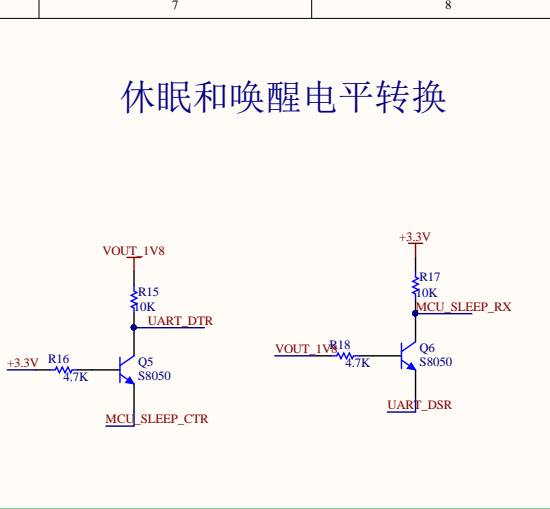


网络天线

The diagram illustrates a network antenna circuit. It starts with an RF input connected to a 2-pin connector. The signal path includes a series capacitor C9, a series inductor L1 (0R), and a series capacitor C10, all connected to ground (GND) via a network capacitor NC. The output is connected to a 27pF capacitor C11, which is connected to the ANT_MAIN line.



定位天线



UART 3.3-1.8V电平转换

The image displays two circuit diagrams for UART level conversion between 3.3V and 1.8V logic levels.

Left Diagram (RXD): This circuit converts a 3.3V signal to 1.8V. It features a PNP transistor (Q3, S8050) with its emitter connected to +3.3V. The base is pulled up to +3.3V by resistor R6 (4.7K). The collector is connected to the 1.8V signal line (C100_UART_RXD) through resistor R5 (10K). A 100nF capacitor (C100) is connected between the signal line and ground. The output is labeled VOUT_1V8.

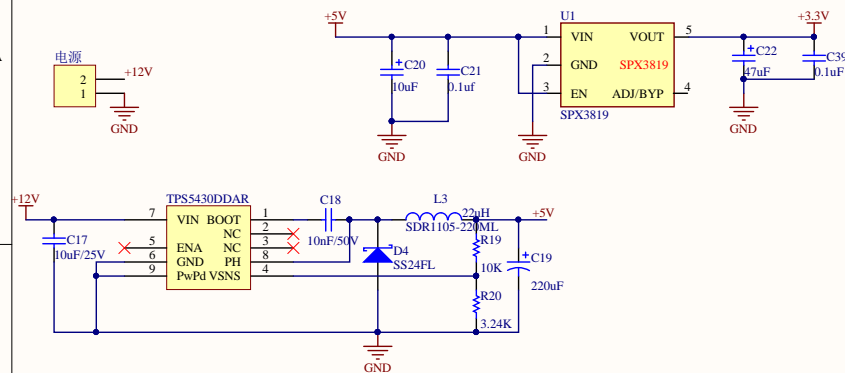
Right Diagram (TXD): This circuit converts a 3.3V signal to 1.8V. It features an NPN transistor (Q4, S8050) with its emitter connected to ground. The base is pulled up to +3.3V by resistor R7 (10K). The collector is connected to the 1.8V signal line (C100_UART_TXD) through resistor R8 (4.7K). A 100nF capacitor (C100) is connected between the signal line and ground. The output is labeled VOUT_1V8.



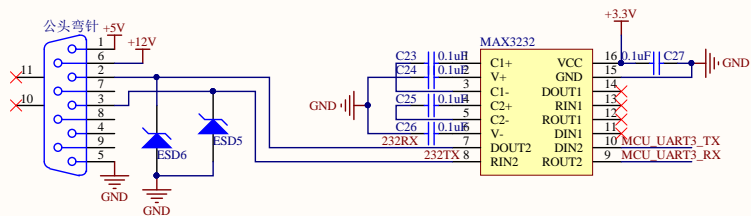
注：VBAT走线宽度2mm,天线匹配网络(L1,C9,C10)靠近天线连接器(J1)放置。
 射频线走线需要控制50欧姆阻抗。GND焊盘与天线焊盘的最近距离0.7mm

D

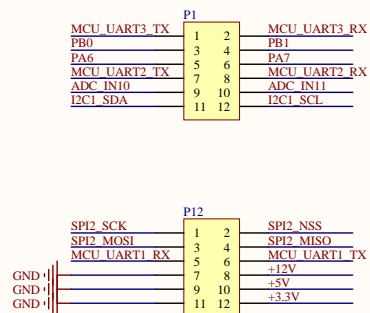
电源模块



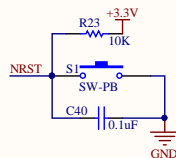
RS232转TTL



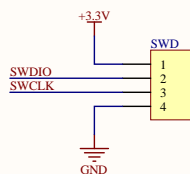
GPIO引脚



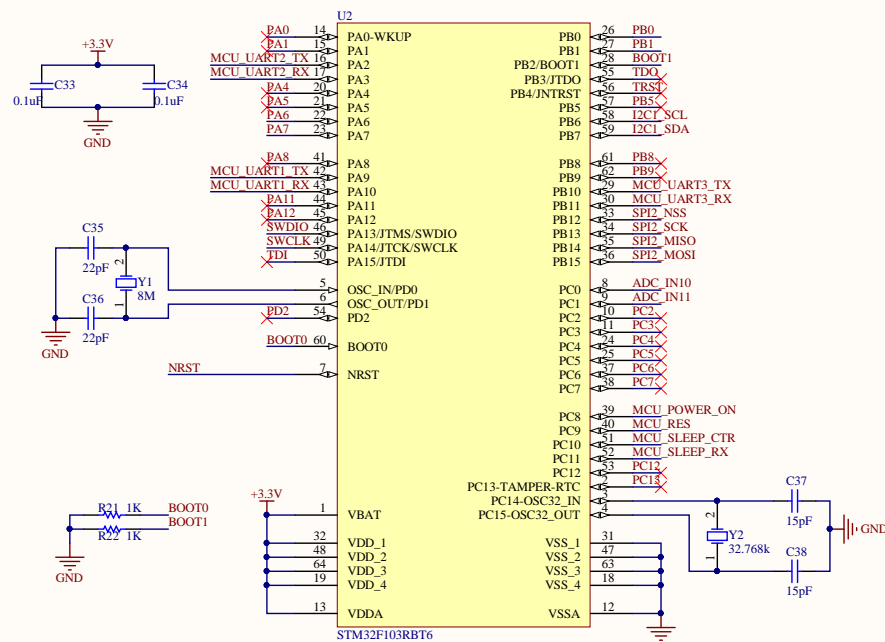
复位电路



SWD下载



核心板



Title		
Size	Number	Revision
A3		
Date:	2017/10/18 星期二	Sheet of
File:	D:\农业物联网\物联网PCB\主控板原理图\SWD	Doc