

REVISION RECORD			
LTR	ECO NO.	APPROVED	DATE

DESIGNER:		DATED:		DEPARTMENT:	
Miao.Li		2020-08-11		HWE	
EQA:		DATED:		COMPANY:	
Leiwen.Ma		2020-08-11		UNISOC	
APPROVED:		DATED:		CONFIDENTIAL AND PROPRIETARY	
Dean.Ji		2020-08-11			

TITLE & REV:  
UWS6130\_A\_CS\_E\_SCH\_V0.2.0  
DOCUMENT NO.:

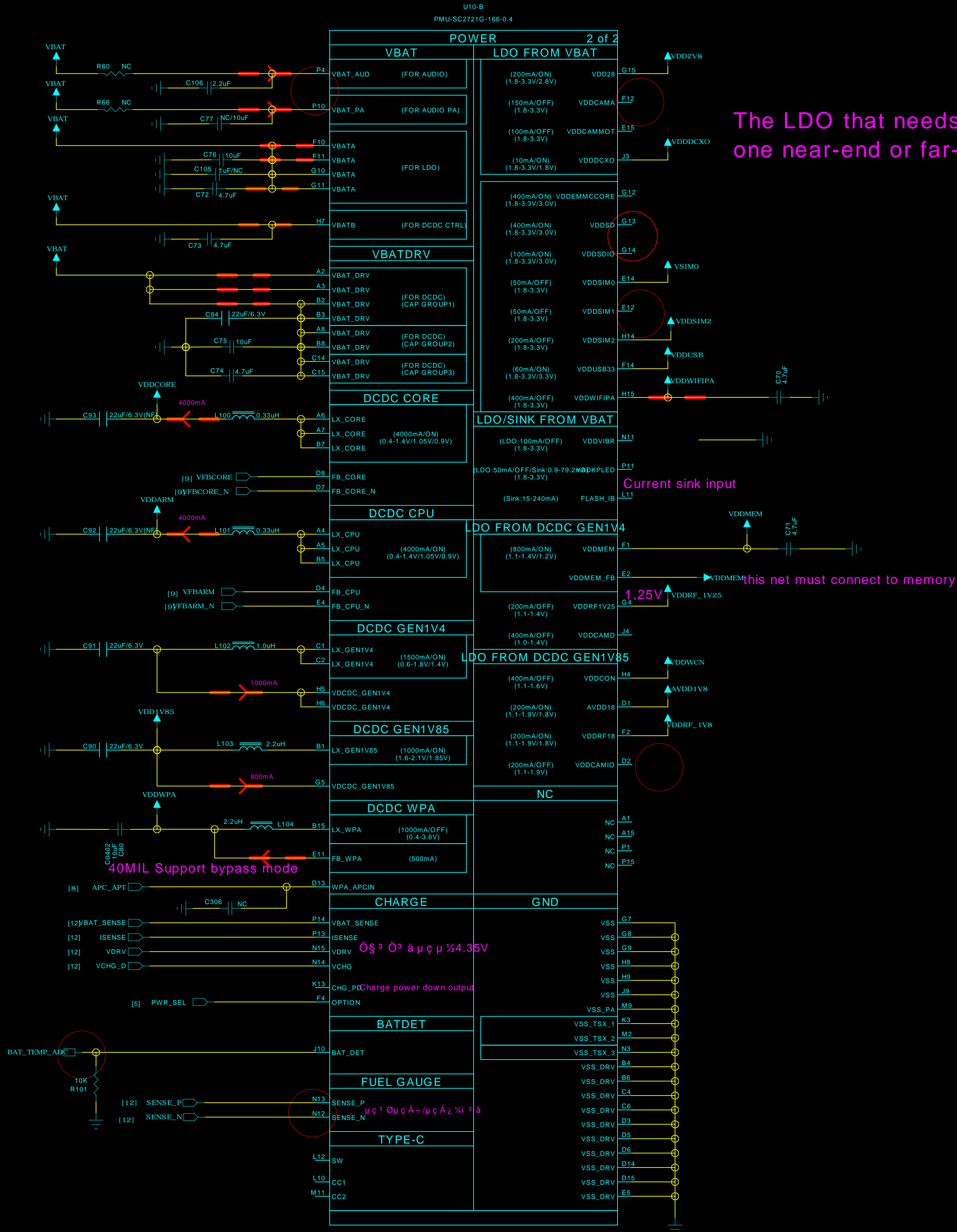


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VDDCORE

PMU\_POWER

INPUT	Description
VBAT_AUD	VBAT power input for audio
VBAT_PA	VBAT power input for audio speaker PA
VBATA	VBAT power input for LDO
VBATB	VBAT power input for KP_LED,VIB,VBATBK,Flash LED
VBATC	VBAT power input for LDO
VBAT_DRV	VBAT power input for DCDC



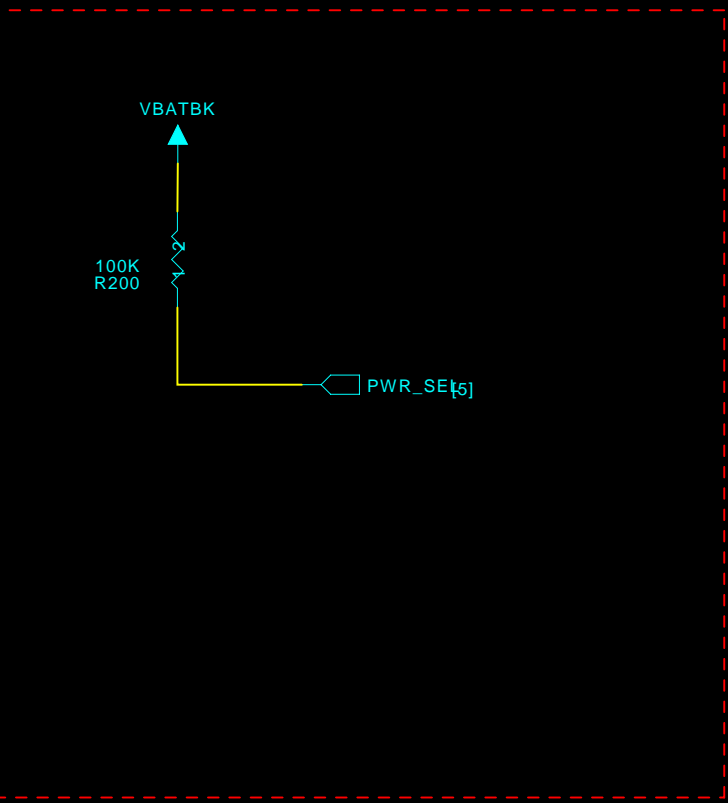
The LDO that needs to be used requires at least one near-end or far-end capacitor

SC2721 DCDC output

Input Power	Power Name	Output Voltage (V)	Output Current	Default Voltage	Default
VBAT	DCDCCPU	0.4-1.4/1.05/0.9	4000mA	0.9V	ON
	DCDCCORE	0.4-1.4/1.05/0.9	4000mA	0.9V	ON
	DCDCMEM	1.1-1.4	800mA	1.4V	ON
	DCDCGEN1V85	1.6-2.1	1000mA	1.85V	ON
	DCDCWPA	0.4-3.6	1000mA		OFF

SC2721 LDO output

Input Power	Power Name	Output Voltage (V)	Output Current	Default Voltage	Default
LDO from VBAT	VDDCAMA	1.8-3.3	150mA	2.8V	OFF
	VDDCAMMOT	1.8-3.3	100mA	2.8V	OFF
	VDDSIM0	1.8-3.3	50mA	1.8V	OFF
	VDDSIM1	1.8-3.3	50mA	1.8V	OFF
	VDDSIM2	1.8-3.3	200mA	1.8V	OFF
	VDD28	1.8-3.3	200mA	2.8V	ON
	VDEEMMCCOR	1.8-3.3	400mA	3.0V	ON
	VDDSDCORE	1.8-3.3	400mA	3.0V	ON
	VDDSDIO	1.8-3.3	100mA	3.0V	ON
	VBATBK	2.6-3.2	2mA	3.0V	ON
	VDDVIB	1.8-3.3	100mA	3.3V	OFF
	VDDKPLED	1.8-3.3	LDO:50mA/Sink:0.9-79.2mA		
	VDDWIFIPA	1.8-3.3	400mA	3.3V	OFF
	VDDDCXO	1.8-3.3	10mA	1.8V	ON
	VDDUSB33	1.8-3.3	60mA	3.3V	ON
LDO from DCDCGEN	VDDCAMD	1.0-1.4	400mA		OFF
	VDD_CON	1.1-1.6	400mA		OFF
	VDD_MEM	1.1-1.4	800mA	1.2V	ON
	VDDCAMIO	1.1-1.9	200mA		OFF
	AVDD18	1.1-1.9	200mA	1.8V	ON
	VDDRF18	1.1-1.9	200mA	1.8V	ON
LDO from DCDCGEN	VDDRF1V25	1.1-1.4	200mA		OFF



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		Spreadtrum communications, Inc.	
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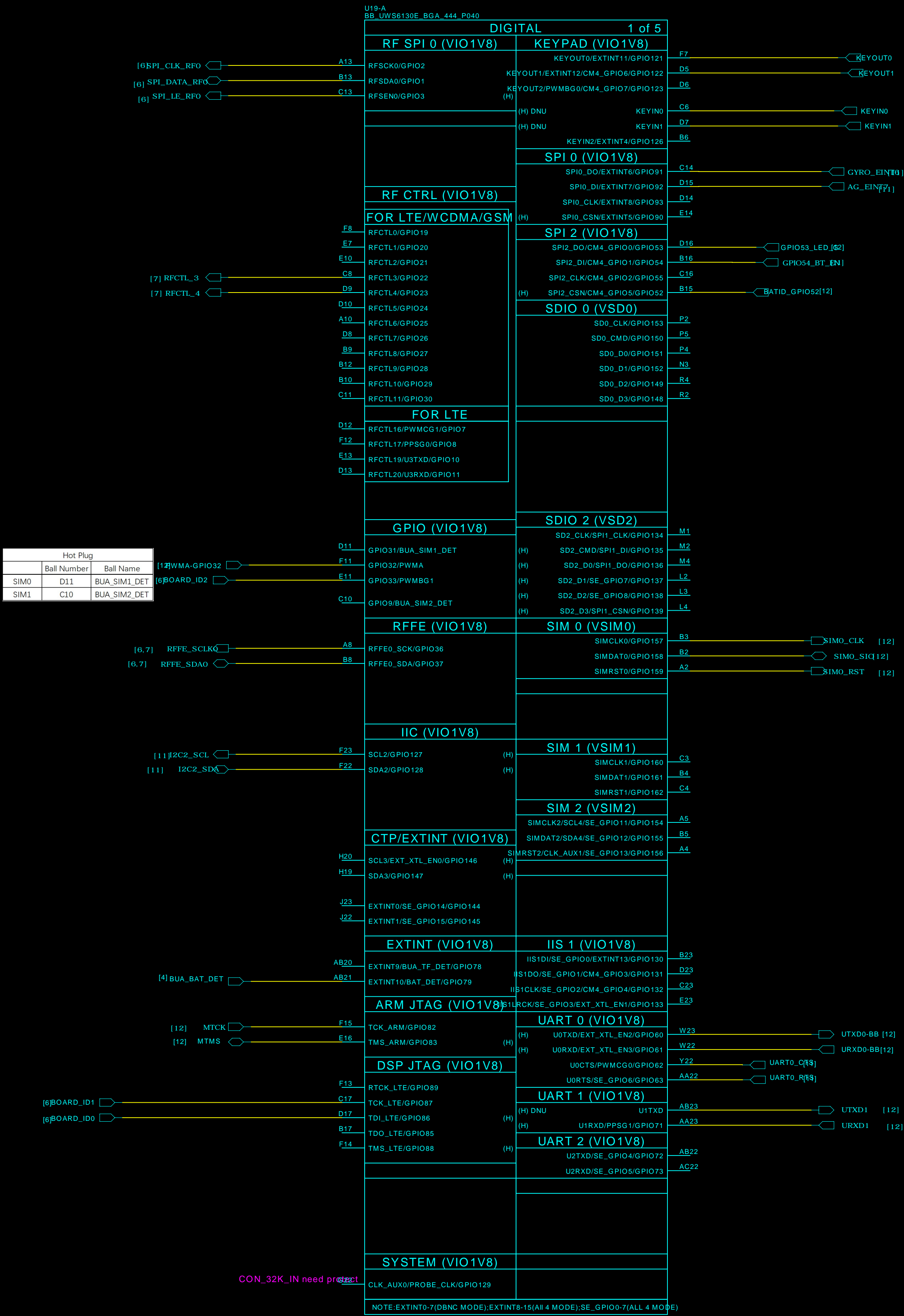






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DIGITAL

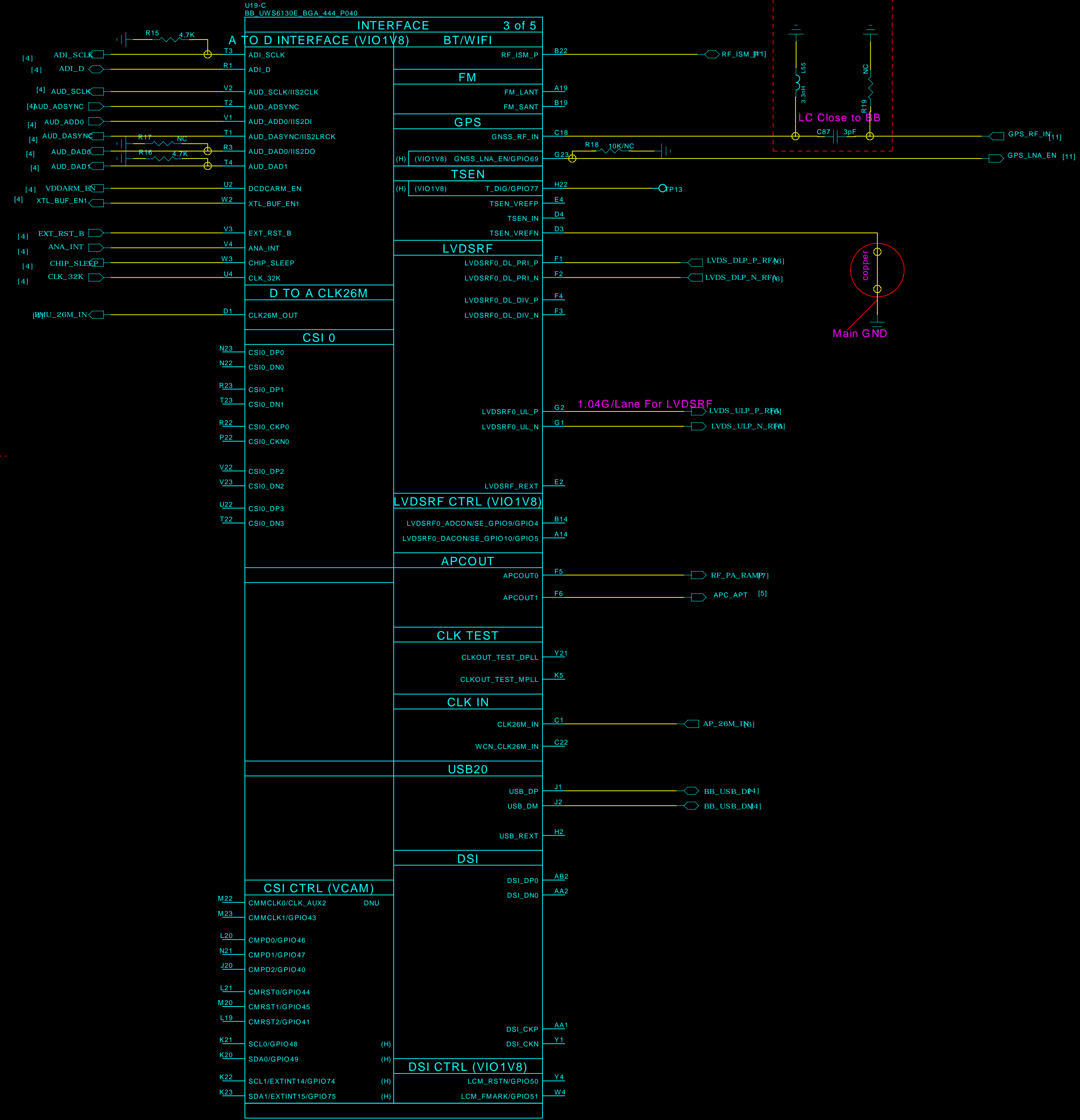


UWS6130E strapping pin

Ball name	Signal name in boot code	Descriptions	Default value
U1TXD	USB_DLOAD_EN	0 USB download (no time out) 1 UART download (Default)	1 (ie=1, wpu)

ARM_BOOT_MD2	ARM_BOOT_MD1	ARM_BOOT_MD0	Descriptions
0	0	0	TLC NAND ONFI
0	0	1	TLC NAND Toggle
0	1	0	MLC/SLC NAND Flash (ONFI)
0	1	1	SD boot (no time out)
1	0	0	MLC/SLC NAND Flash (Toggle)
1	0	1	TLC NAND ONFI (Hybrid)
1	1	0	Reserved
1	1	1	eMMC boot



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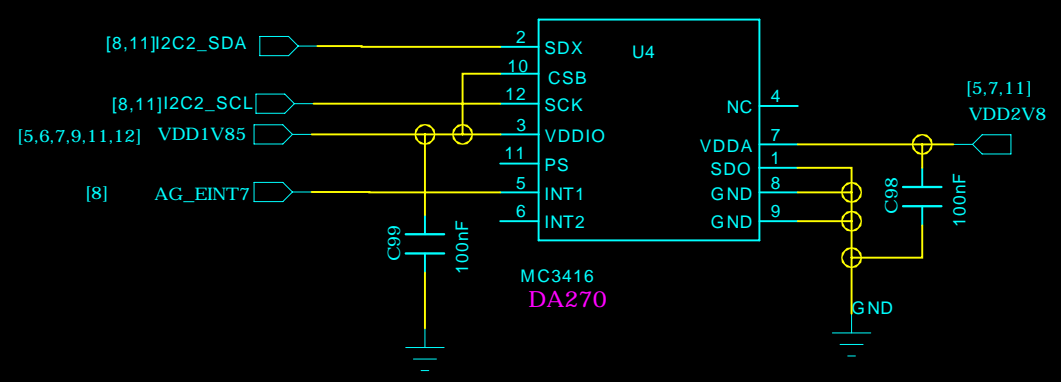




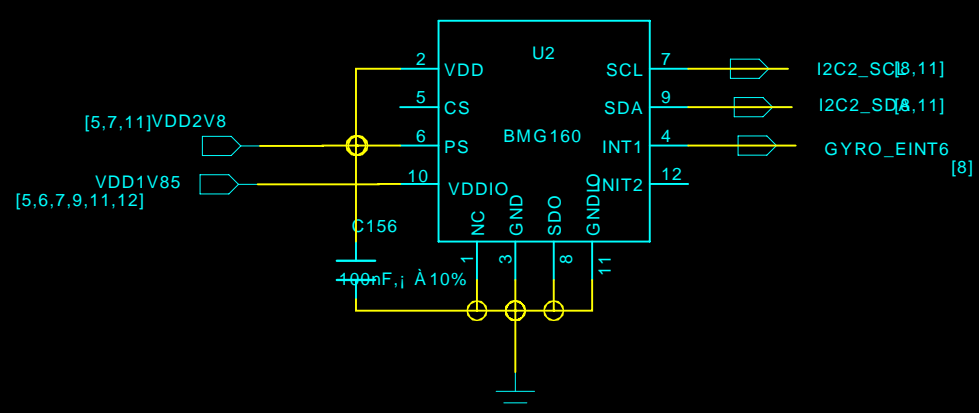


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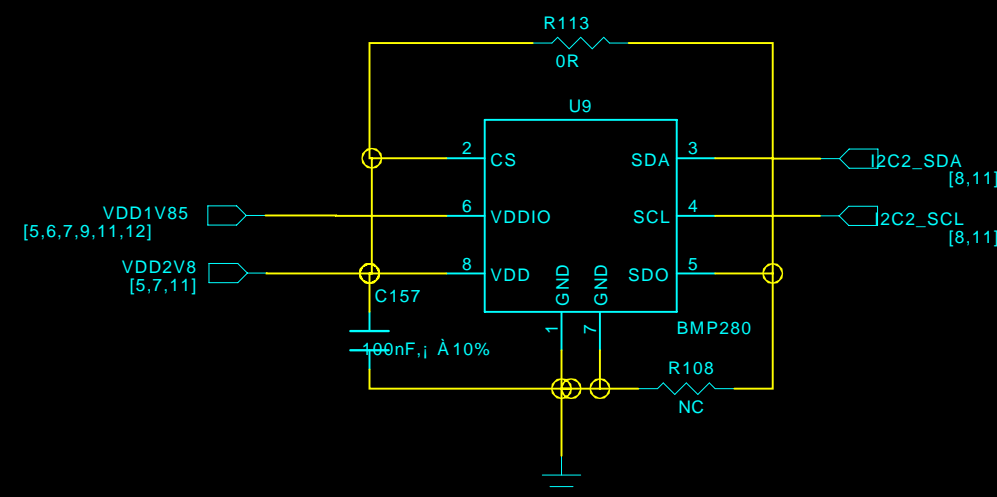
G\_SENSOR



gyroscope



pressure and temperature

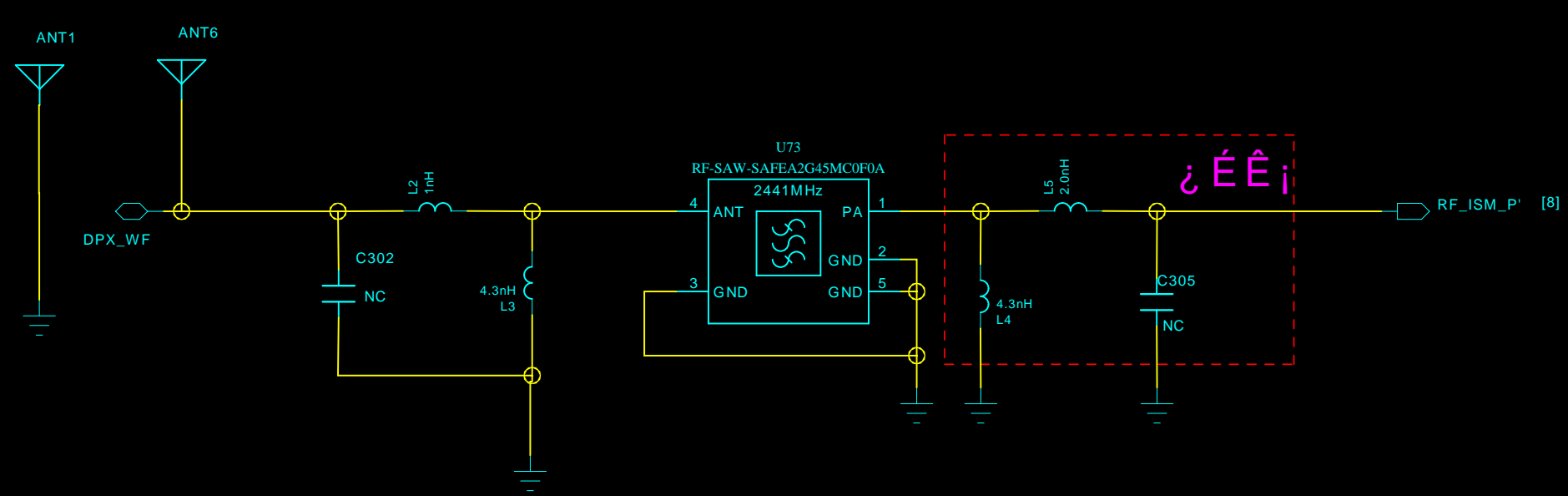


I2C ADDRESS

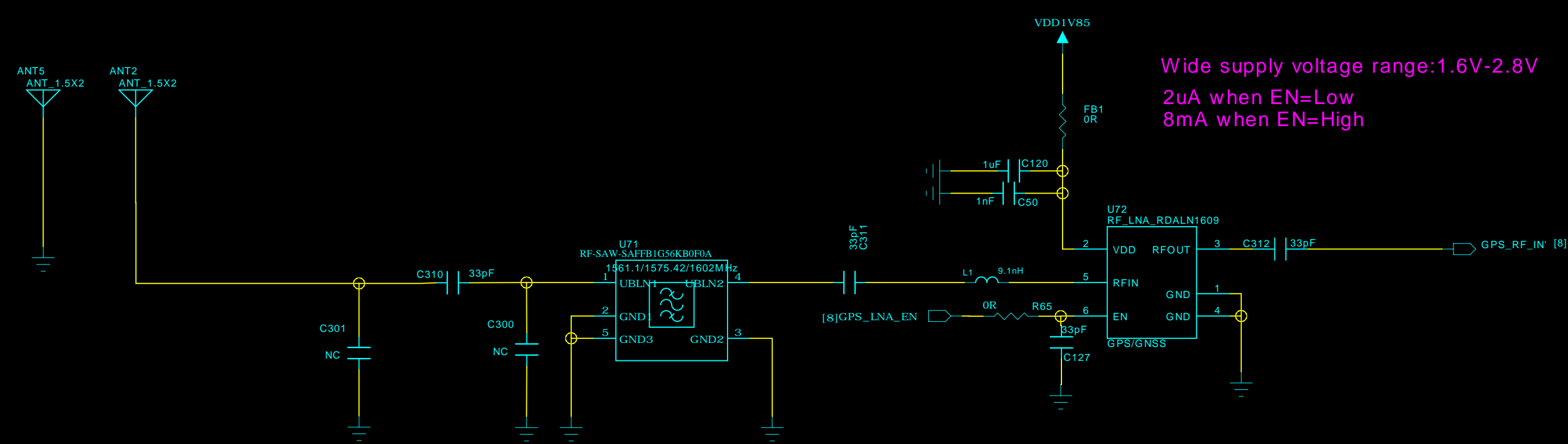
	Manufacturer	MPN	Address
A sensor	BOSCH	BMA253	0b0011001 (SDO=1)

PIN	Definition
VDD	1.62V - 3.6V
VDDIO	1.2V - 3.6V
SDO	Address select
SCx	SCL for I2C
SDx	SDA for I2C
PS	Protocol Selection:H---I2C
INT1	Interrupt PIN
INT2	Interrupt PIN
CSB	Chip select:H---I2C

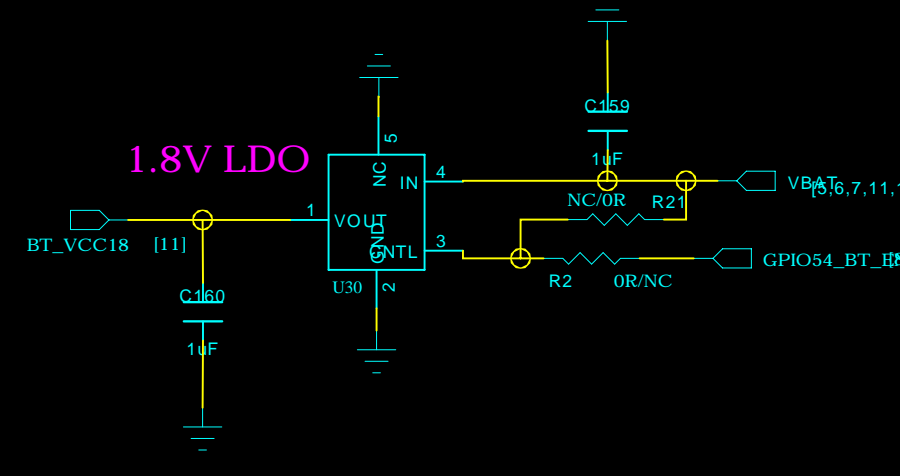
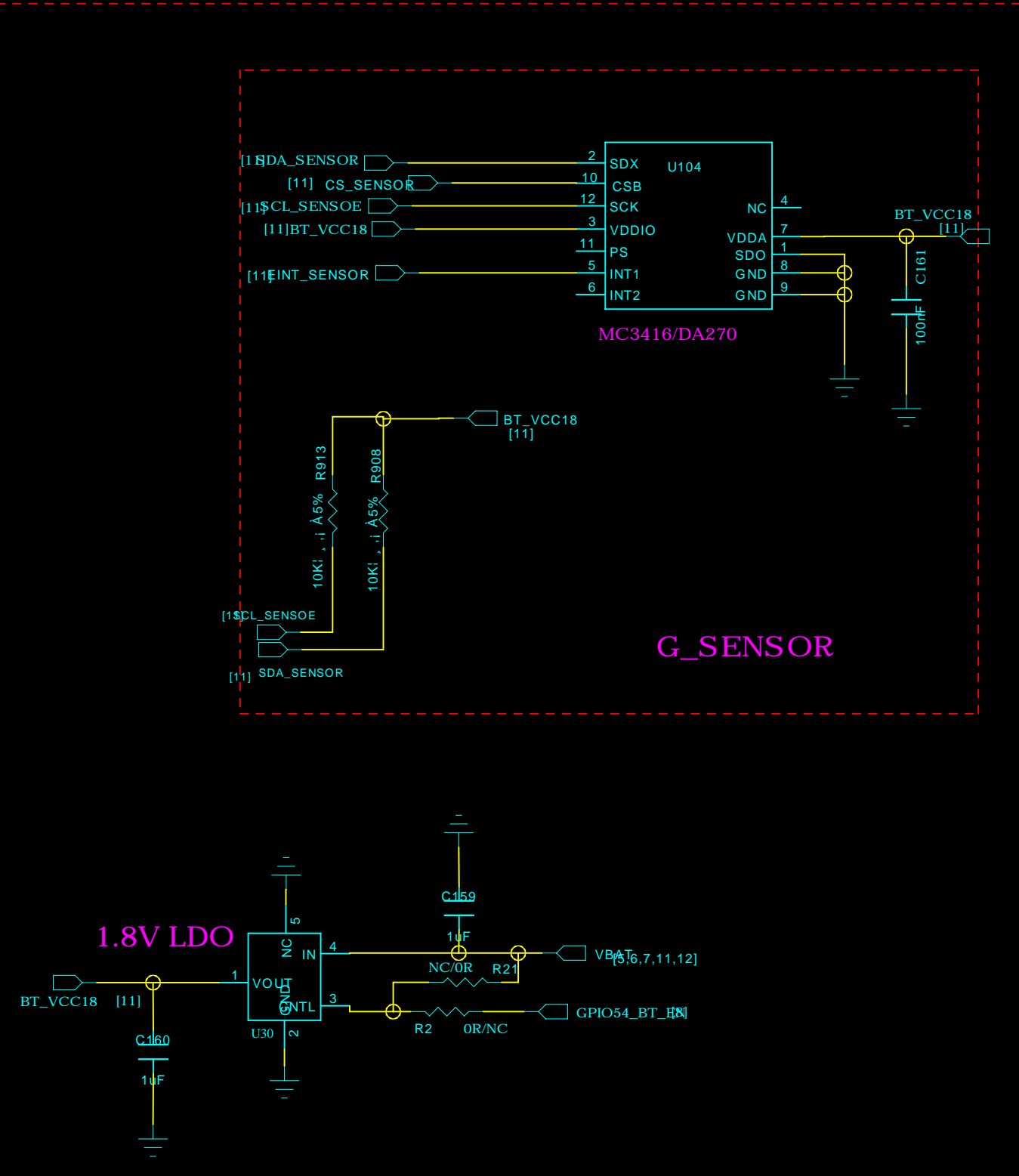
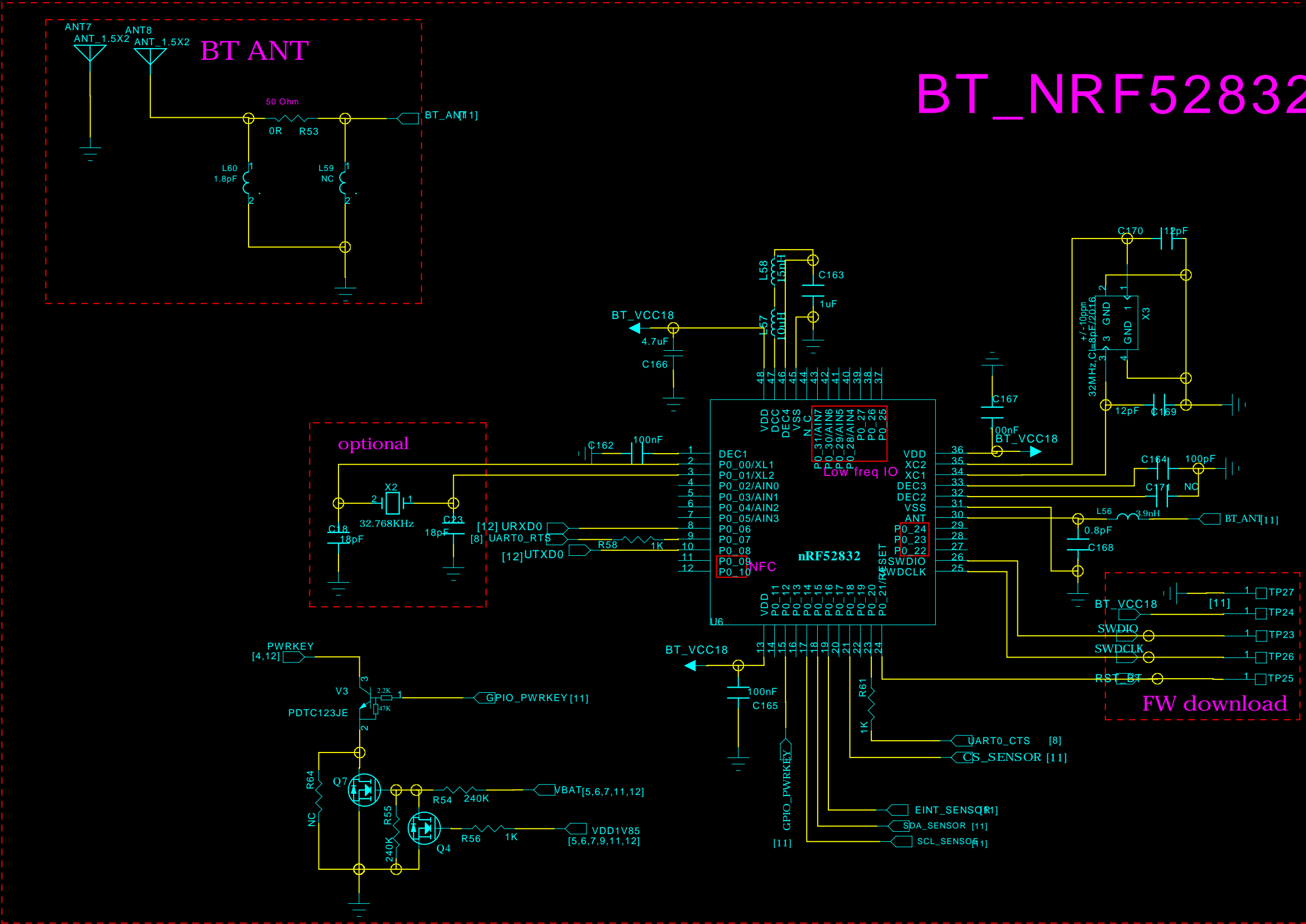
WIFI/BT 2.4G



GNSS SAW LNA



BT\_NRF52832 QFAA



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