Valentino Carneiro

Personal Data

Nationality	Portuguese
Languages	Portuguese, English (Written and spoken)
Age, gender	20, Male
Location	Porto, Portugal
Phone:	[redacted]
email:	valentinocarneirogoncalves [at] gmail [dot] com
Website:	https://xvalme.github.io
Github:	https://github.com/xvalme

Technical Skills

RF | Software-Defined Radio | Antennas | PCB Design | High Speed PCBs | Electronics

Education

since 10/2021	Bachelor of Electrical and Computer Engineering, Current average of [redacted]
until 06/2021	Secondary school, with an average of [redacted]

Technical Skills

PCB Design of high speed and RF boards: designing, soldering and testing

Programming languages: C, Python, Javascript (NodeJs, including the React-Native framework), Assembly, Verilog and a touch of SQL (Postgre)

Software: Altium, LTSpice, Matlab, Fusion360, Excel, Git

Prototyping skills: Soldering, Arduino, FPGA

Achievements and projects

Designed, Documented (while maintaining best practices), Soldered and Tested my LNA board to receive satellite images using a V-dipole antenna, with sucess.

Even before having a computer architecture class, wrote an ARM LegV8 CPU simulator in Verilog, tested with Icarus and a few test benches.

Winner of a honorable mention in the National scientific contest "Jovens cientistas" of 2020, by Ciência Viva, in the Physics field: [redacted] The project involved the building of a radio-telescope, and a RF system to filter, amplify and process the 1420-ish Mhz signal.

On the software side, created an open source, NodeJS + ReactNative, mobile e-book reader, called AbsolutReader with text analysis and pdf edition, available on playstore with about 100 downloads.

Maker by nature. Many small projects of electronics, but also some bigger ones, like a Chess Board With Voice Commands, that played movements with voice input and stepper motors.