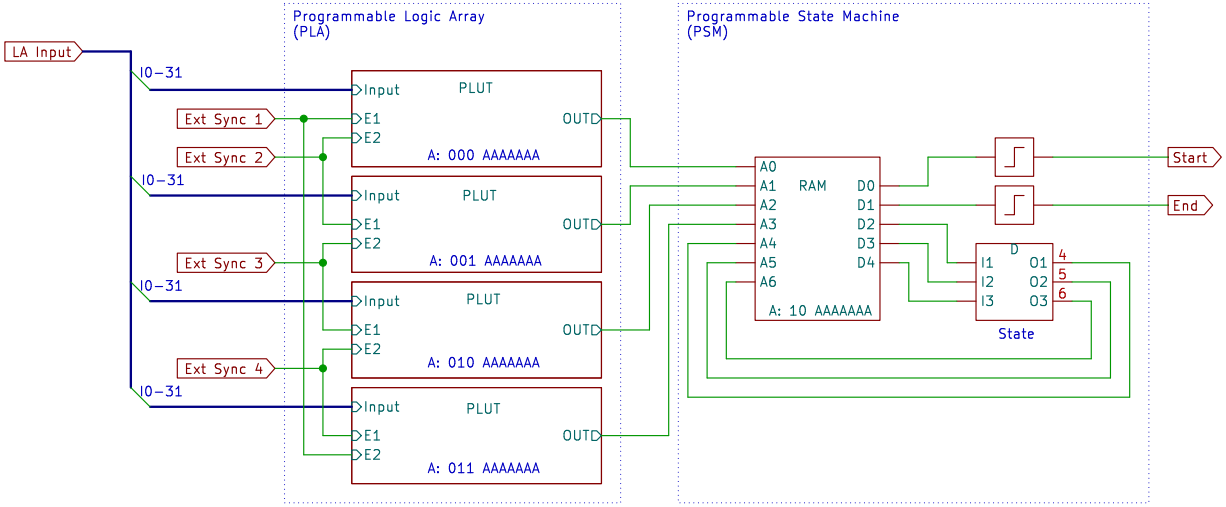
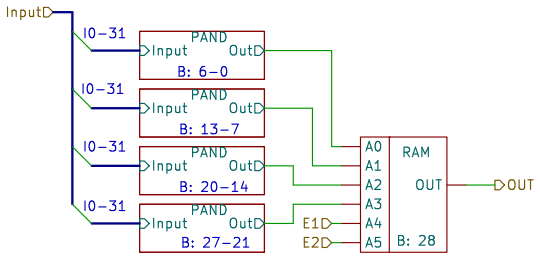


RAM Bits definition (inside single RAM word) legend:
B: +<N> – Bit with shift <N> inside Bitscale defined in upper level
B: <N> or B: <N>–<N> – Absolute bit or bits range

RAM Address definition legend:
A: <NNN> AA..A
<NNN> – Absolute value for appropriate bits of address
AA..A – Physical RAM address

Sheet: / File: triger–doc.kicad_sch		
Title: Trigger (top level)		
Size: A4	Date:	**Rev:**
KiCad E.D.A. 8.0.4		Id: 1/21



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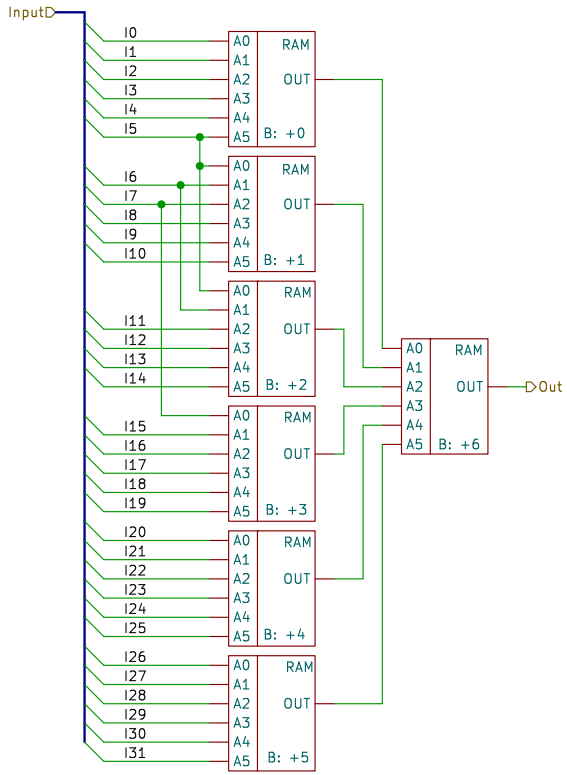
Size: A4	Date:	**Rev:**
KiCad E.D.A. 8.0.4		Id: 2/21



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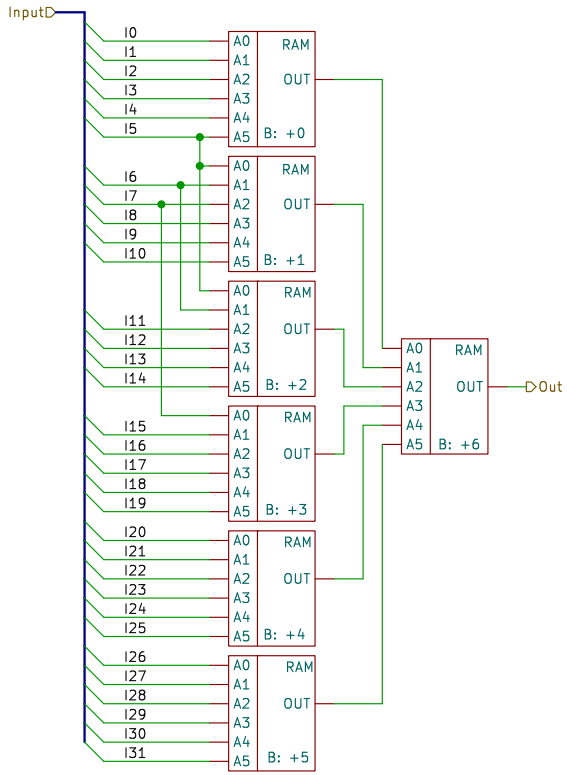
Size: A4	Date:	**Rev:**
KiCad E.D.A. 8.0.4		Id: 4/21



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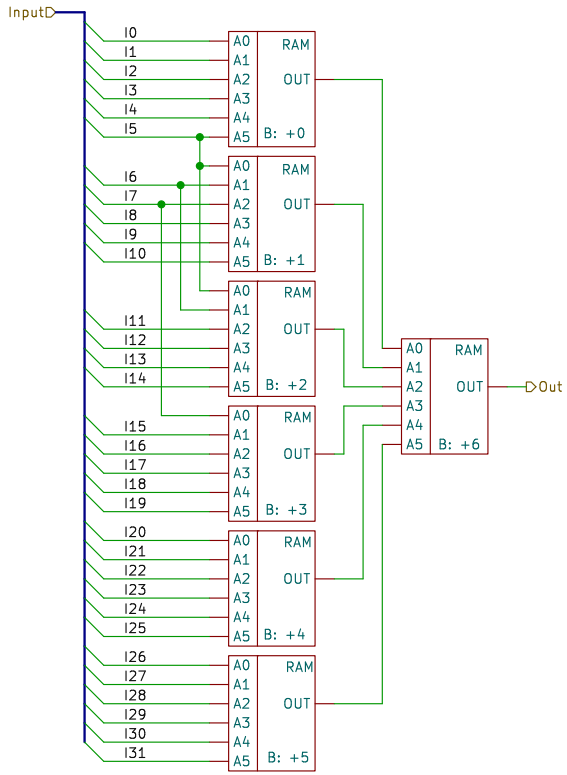
KiCad E.D.A. 8.0.4		Id: 5/21



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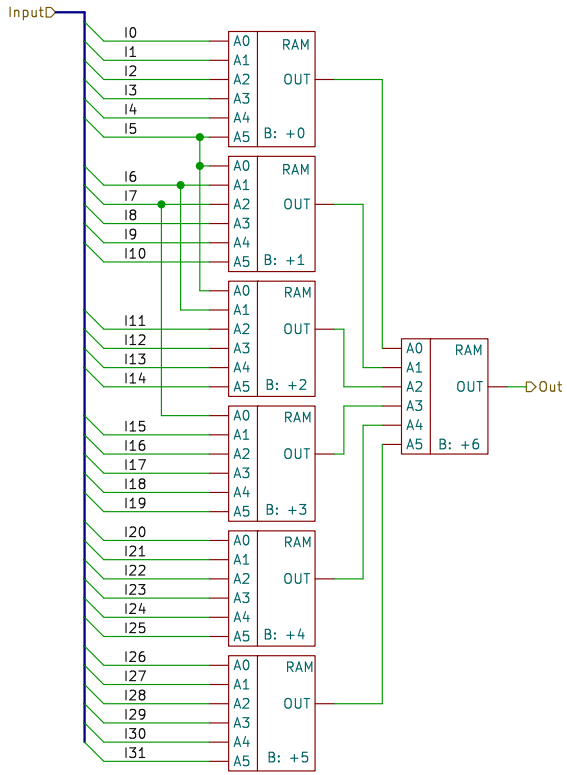
KiCad E.D.A. 8.0.4		Id: 6/21



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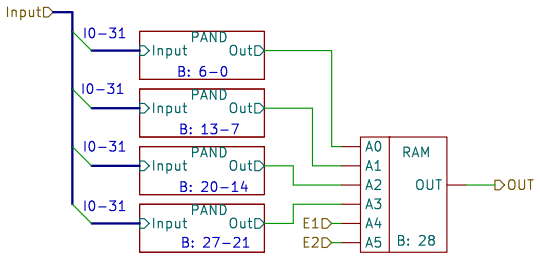
Size: A4	Date:	**Rev:**
KiCad E.D.A. 8.0.4		Id: 7/21



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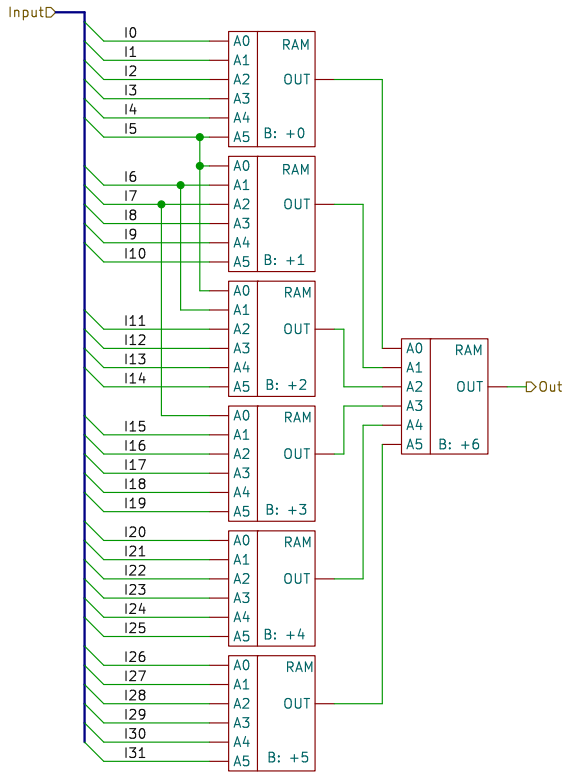
KiCad E.D.A. 8.0.4		Id: 8/21



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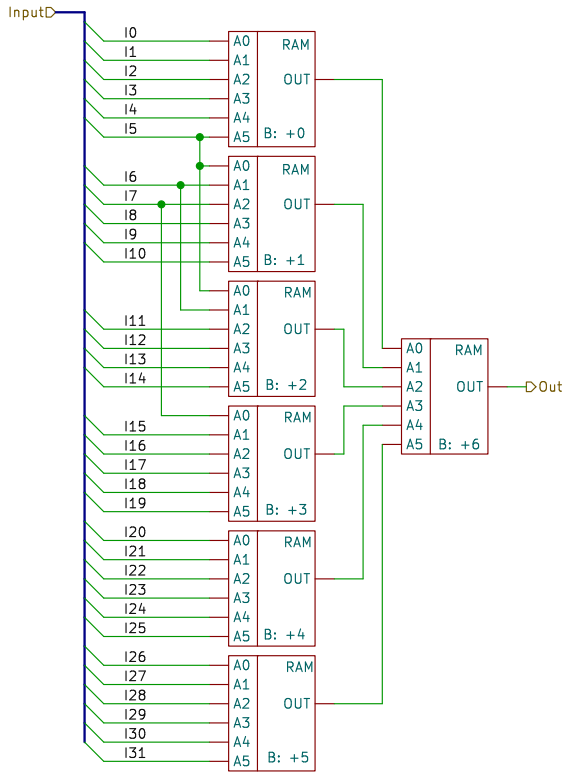

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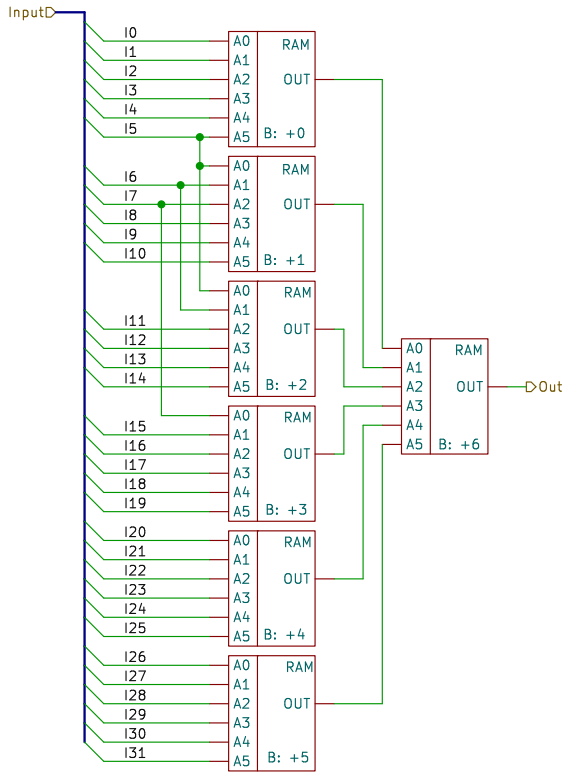

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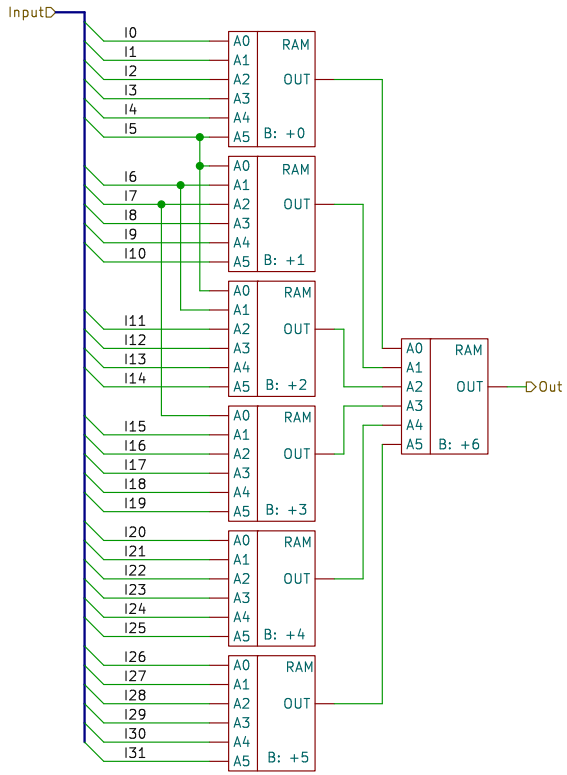

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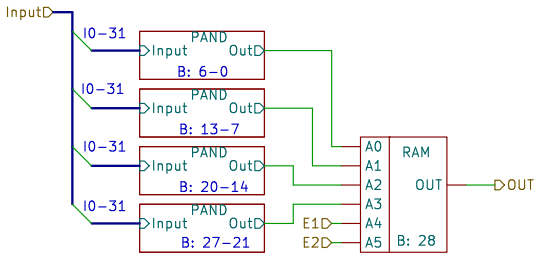

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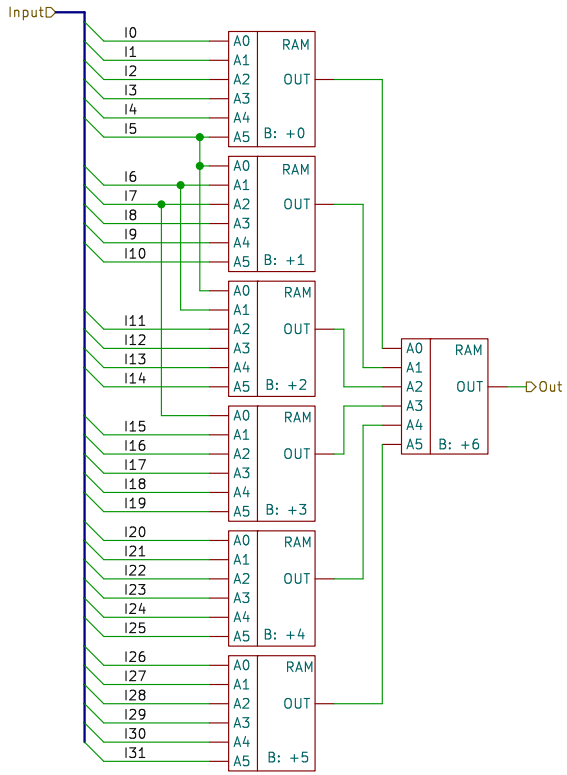

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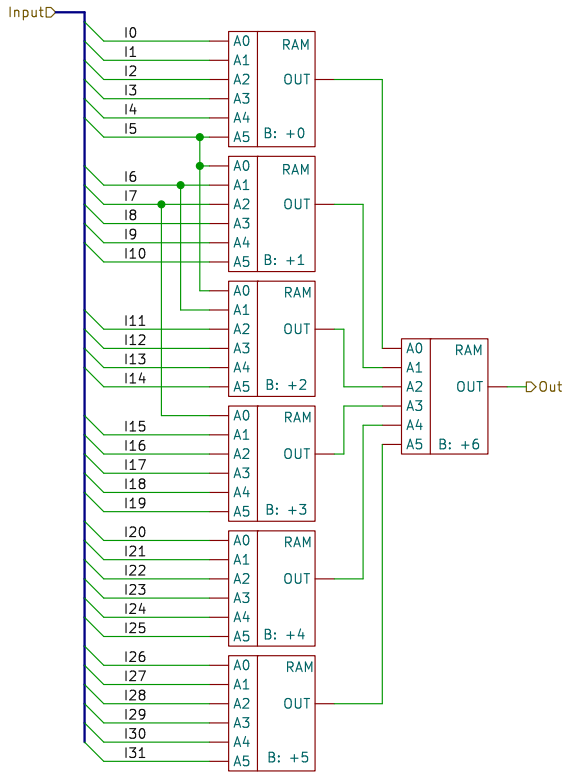

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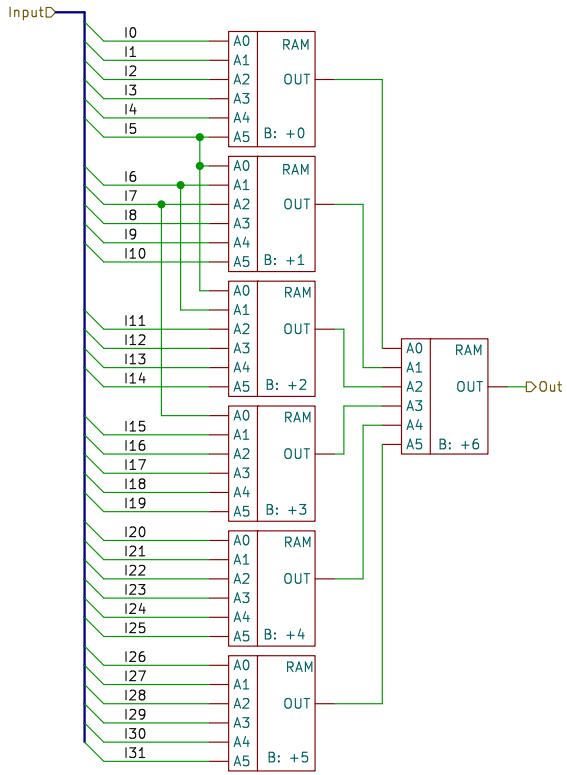

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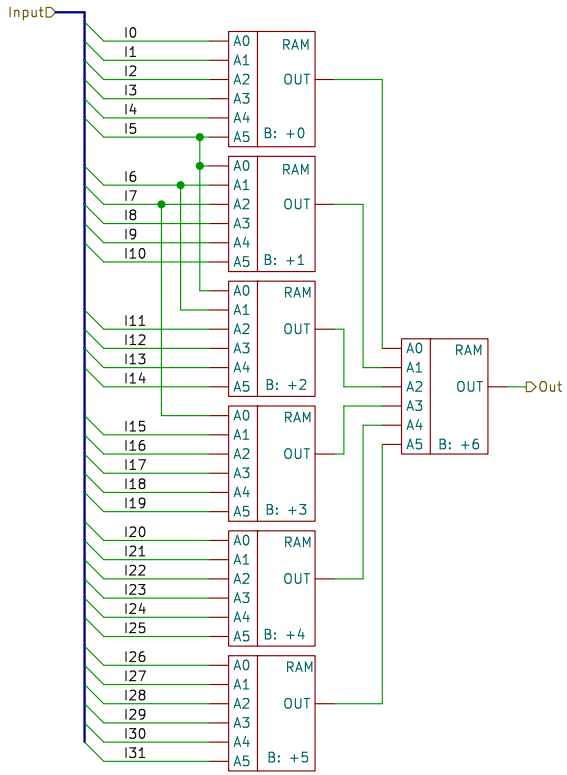

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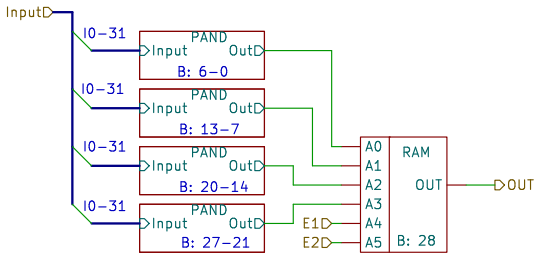

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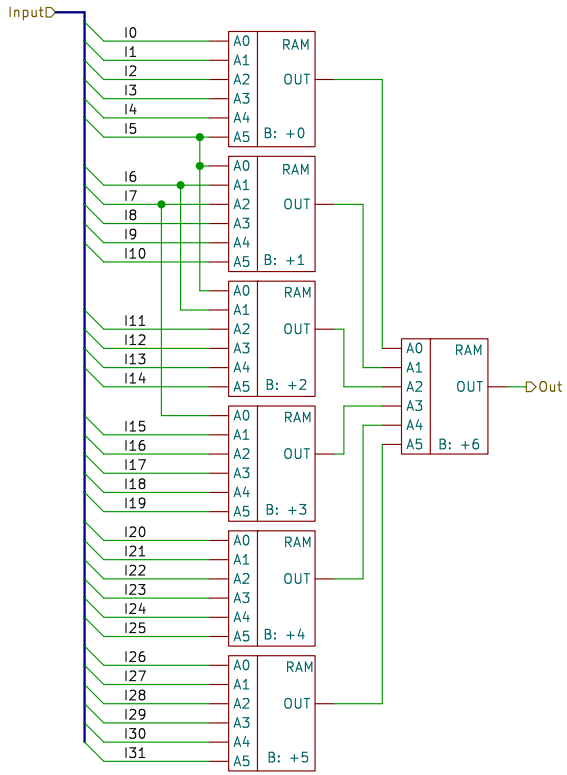

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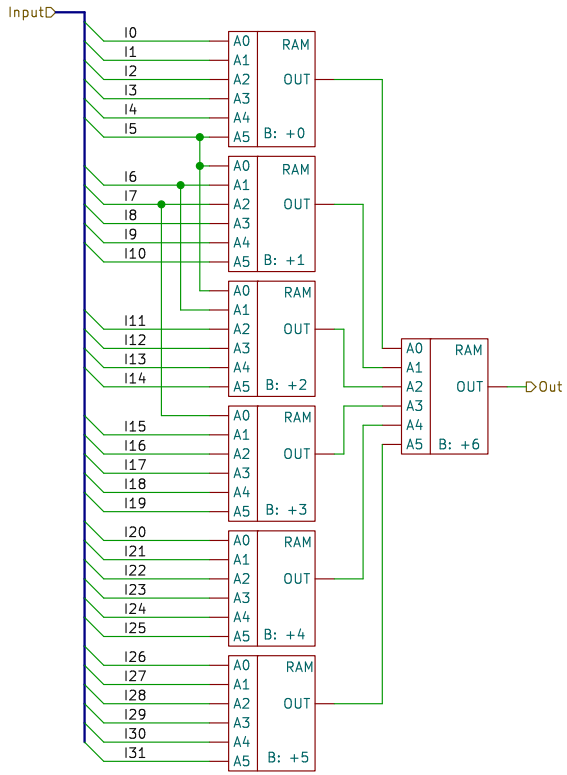
KiCad E.D.A. 8.0.4		Id: 19/21



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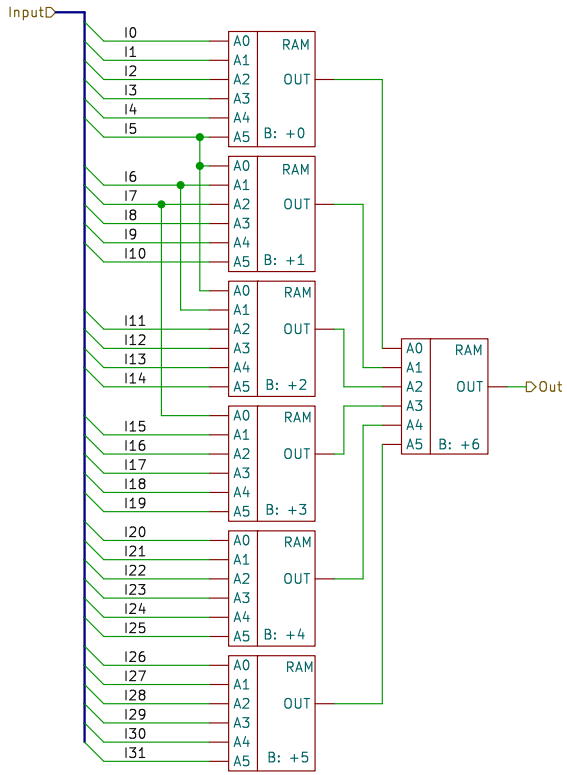

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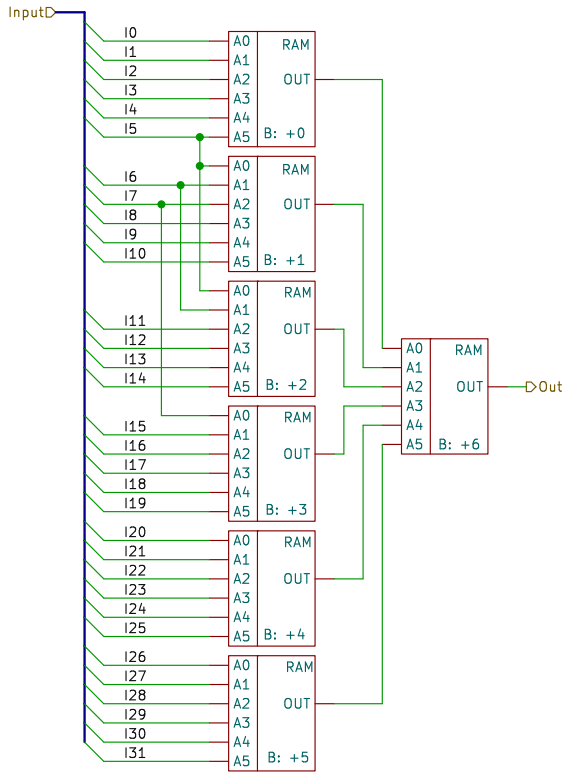

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KiCad E.D.A. 8.0.4		Id: 22/21