# **Exercise Session Synchronization / Resource allocation**

Operating Systems

Chalmers University of Technoloy – Gothenburg University

We have three threads A, B, and C taking care of operations opA, opB, and opC respectively. The threads use three semaphores with the following initial value: semA=1, semB=1, and semC=0.

```
Thread A:
                              Thread B:
                                                            Thread C:
                                                             wait(semA);
wait(semC);
                               wait(semA);
wait(semB);
                               wait(semB);
                                                             wait(semB);
                                                             opC; //some operation
 opA; // some operation
                               opB; //some operation
signal(semB);
                               signal(semB);
                                                             signal(semB);
                                                             signal(semA);
 signal(semA);
                                                             signal(semC);
```

Are the following executions possible or not and why?

```
(i) opA opB opC (ii) opB opC opA (iii) opC opA opB (iv) opB opA opC
```

Initial values: semA=1, semB=1, semC=0

```
Thread A:
                              Thread B:
                                                            Thread C:
wait(semC);
                               wait(semA);
                                                             wait(semA);
wait(semB);
                               wait(semB);
                                                             wait(semB);
opA; // some operation
                               opB; //some operation
                                                             opC; //some operation
signal(semB);
                               signal(semB);
                                                             signal(semB);
signal(semA);
                                                             signal(semA);
                                                             signal(semC);
```

Are the following executions possible or not and why?

(i) opA opB opC

no: A blocks at semC until C has executed opC

Initial values: semA=1, semB=1, semC=0

```
Thread A:
                              Thread B:
                                                            Thread C:
wait(semC);
                               wait(semA);
                                                             wait(semA);
wait(semB);
                               wait(semB);
                                                             wait(semB);
opA; // some operation
                               opB; //some operation
                                                             opC; //some operation
signal(semB);
                               signal(semB);
                                                             signal(semB);
signal(semA);
                                                             signal(semA);
                                                              signal(semC);
```

Are the following executions possible or not and why? (ii) opB opC opA

no: B "consumes" from semA but does not produce in it, hence C will block

Initial values: semA=1, semB=1, semC=0

```
Thread A:
                              Thread B:
                                                            Thread C:
                               wait(semA);
wait(semC);
                                                             wait(semA);
wait(semB);
                               wait(semB);
                                                             wait(semB);
opA; // some operation
                               opB; //some operation
                                                             opC; //some operation
signal(semB);
                               signal(semB);
                                                             signal(semB);
signal(semA);
                                                             signal(semA);
                                                             signal(semC);
```

Are the following executions possible or not and why? (iii) opC opA opB

Yes, it is possible.

Initial values: semA=1, semB=1, semC=0

```
Thread A:
                              Thread B:
                                                            Thread C:
wait(semC);
                               wait(semA);
                                                             wait(semA);
wait(semB);
                               wait(semB);
                                                             wait(semB);
opA; // some operation
                               opB; //some operation
                                                             opC; //some operation
signal(semB);
                               signal(semB);
                                                             signal(semB);
signal(semA);
                                                             signal(semA);
                                                              signal(semC);
```

Are the following executions possible or not and why? (iv) opB opA opC

no: same as (ii), now A blocks at semC until C has executed opC; ie if B executes first, no other thread can proceed.

- (a) Consider two threads, A and B. Thread B must execute operation opB only after thread A has completed operation opA. How can you guarantee this synchronization using semaphores?
- (b) Consider two threads, A and B which must forever take turns executing operation opA and operation opB, respectively. Thread A must be the one that executes opA first. How can you guarantee that using semaphores?

(a) Consider two threads, A and B. Thread B must execute operation opB only after thread A has completed operation opA. How can you guarantee this synchronization using semaphores?

Use semaphore flag, initialized to 0

Thread A: Thread B:

•••

opA wait(*flag*)

signal(flag) opB

Thread B will be able to proceed from wait(flag) only after signal(flag) is executed by thread A

- (a) Consider two threads, A and B. Thread B must execute operation opB only after thread A has completed operation opA. How can you guarantee this synchronization using semaphores?
- (b) Consider two threads, A and B which must forever take turns executing operation opA and operation opB, respectively. Thread A must be the one that executes opA first. How can you guarantee that using semaphores?

Now the threads work in a loop, using semaphores SA and SB. Thread A must wait for opB, except the first time, hence SB is initialized to 1. Thread B must wait for opA with SA initialized to 0. After opA (resp opB), A executes signal(SA) (resp B executes signal(SB), to generate the required signal to enable the other one to proceed.

	Thread A:	Thread B:
Initial values: SA = 0, SB = 1	while(true):	while(true):
	wait(SB)	wait(SA)
	орА	орВ
	signal(SA)	signal(SB)

Servers can be designed to limit the number of open connections. For example, a server may wish to have only N active socket connections at any point in time. As soon as N connections are made, the server will not accept a new connection until an existing one is released. With pseudocode, describe how semaphores can be used to limit the number of concurrent connections.

Servers can be designed to limit the number of open connections. For example, a server may wish to have only N active socket connections at any point in time. As soon as N connections are made, the server will not accept a new connection until an existing one is released. With pseudocode, describe how semaphores can be used to limit the number of concurrent connections.

General semaphore S, initialized to N; execute wait(S) and signal(s) before and after the connection between each client (socket) and server.

Show a method that solves the critical section problem for arbitrary number of threads using the atomic TestAndSet instruction that is available in several processor architectures. Use pseudocode in the description and argue about the properties of the solution, with respect to mutual exclusion, progress and fairness. (It is not necessary to describe a solution that guarantees fairness in this question, but if you can, of course it is ok).

Show a method that solves the critical section problem for arbitrary number of threads using the atomic TestAndSet instruction that is available in several processor architectures. Use pseudocode in the description and argue about the properties of the solution, with respect to mutual exclusion, progress and fairness. (It is not necessary to describe a solution that guarantees fairness in this question, but if you can, of course it is ok).

#### Without fairness:

```
boolean TAS(Boolean *target)
{
   boolean rv = *target;
   *target = true;
   return rv;
}
```

## Shared Boolean variable *lock* initialized to false Threads:

```
repeat
   while (TAS(&lock)); //busywait
   [critical section]
   lock = false;
   ...
forever
```

#### With fairness:

Shared Boolean variable *lock*, initialized to false Shared Boolean array waiting[0...n-1], initialized to false

#### **Threads:**

```
repeat
  waiting[i] = true;
  while (TAS(&lock) && waiting[i]); //busywait
  waiting[i] = false;
   [critical section]
  j = (i+1)\%n;
  while(j!=i && !waiting[j]) //find the next one waiting to hand over the lock
     j = (j+1)\%n;
  if (i==i)
     lock = false; //completed one round without handing over; release lock
  else
     waiting[j] = false; //handover the lock
forever
```