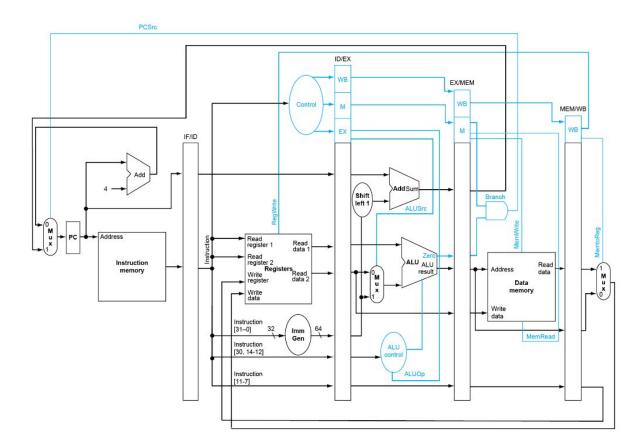
Exercici 1.8

1.8

Assumiu un processador RISC-V amb 5 etapes de pipeline.



	1	2	3	4	5	6	7	8	9	10	11	12	13	14
sd x29, 12(x16)														
ld x29, 8(x16)														
sub x17, x9, x14														
beqz x17, label														
add x15, x17, x14														
add x16, x15, x14														

	1	2	3	4	5	6	7	8	9	10	11	12	13	14
sd x29, 12(x16)	IF	ID	EX	MEM	WB									
ld x29, 8(x16)														
sub x17, x9, x14														
beqz x17, label														
add x15, x17, x14														
add x16, x15, x14														

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sd x29, 12(x16)	IF	ID	EX	MEM	WB									
ld x29, 8(x16)		IF	ID	EX	MEM	WB								
sub x17, x9, x14														
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sd x29, 12(x16)	IF	ID	EX	MEM	WB									
ld x29, 8(x16)		IF	ID	EX	MEM	WB								
sub x17, x9, x14			IF	ID	EX	MEM	WB							
beqz x17, label														
add x15, x17, x14														
add x16, x15, x14														

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sd x29, 12(x16)	IF	ID	EX	MEM	WB									
ld x29, 8(x16)		IF	ID	EX	MEM	WB								
sub x17 , x9, x14			IF	ID	EX	MEM	WB							
beqz x17, label				(stall)	(stall)	IF	ID	EX	MEM	WB				
add x15, x17, x14														
add x16, x15, x14														

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sd x29, 12(x16)	IF	ID	EX	MEM	WB									
ld x29, 8(x16)		IF	ID	EX	MEM	WB								
sub x17, x9, x14			IF	ID	EX	MEM	WB							
beqz x17, label				(stall)	(stall)	IF	ID	EX	MEM	WB				
add x15, x17, x14							IF	ID	EX	MEM	WB			
add x16, x15, x14														

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sd x29, 12(x16)	IF	ID	EX	MEM	WB									
ld x29, 8(x16)		IF	ID	EX	MEM	WB								
sub x17, x9, x14			IF	ID	EX	MEM	WB							
beqz x17, label				(stall)	(stall)	IF	ID	EX	MEM	WB				
add x15 , x17, x14							IF	ID	EX	MEM	WB			
add x16, x15 , x14								(stall)	(stall)	IF	ID	EX	MEM	WB

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add x15, x17, x14					IF	ID	EX	MEM	WB					
add x16, x15 , x14						IF	ID	EX	MEM	WB				