

Computer Architecture

計算機架構

Textbook

David A. Patterson, and John L. Hennessy,
Computer Organization and Design, 5th Ed./6th Ed.
Morgan Kaufmann, Elsevier

Instructor

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Course Site

Teams, “T1101.46619 計算機架構”課程團隊

Time

Wed 13:10~15:00, Thr 13:10~14:00

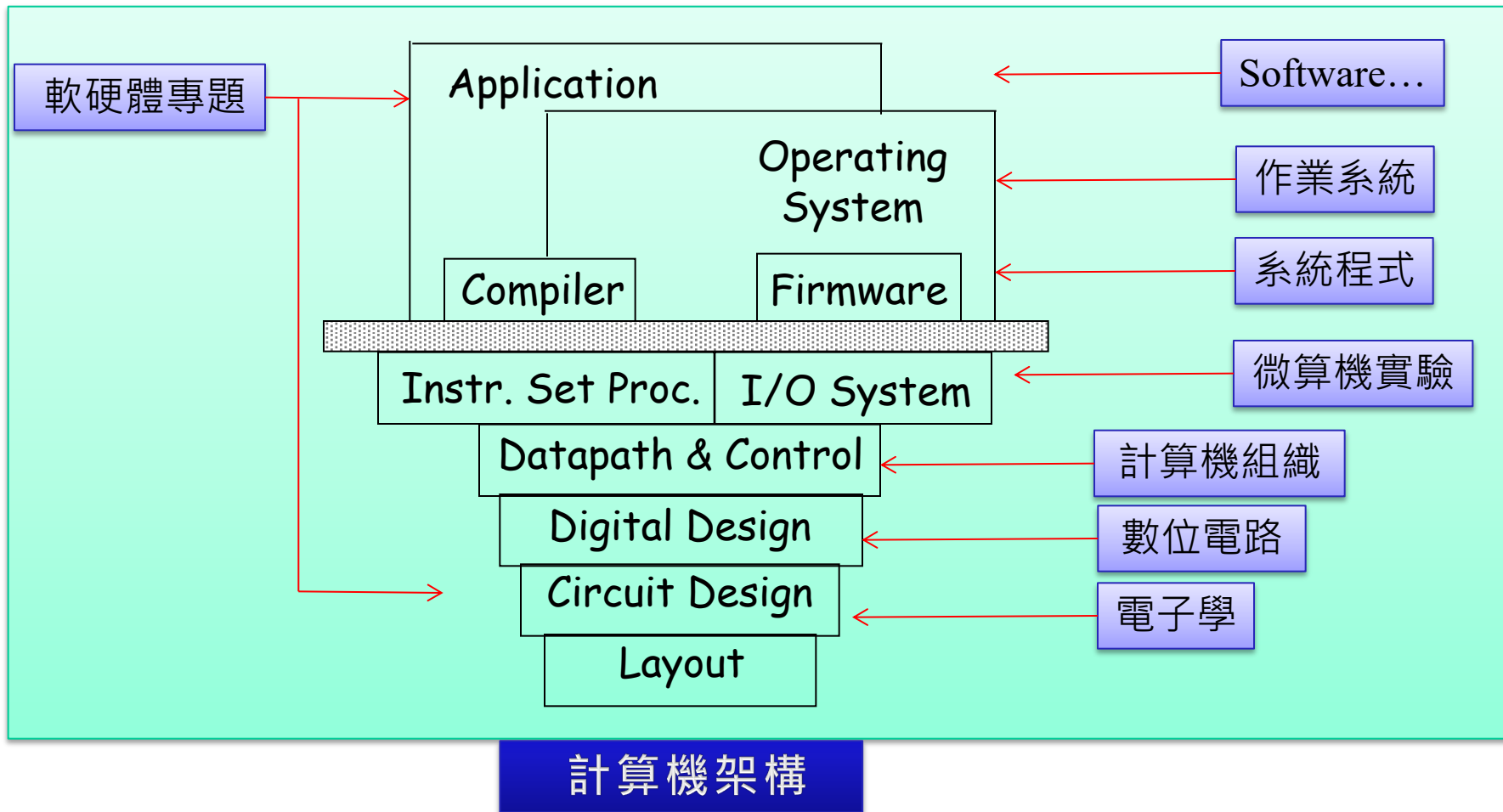
Classroom

B0106

TA

蔡雅如

What is “Computer Architecture” ?



- ❑ Coordination of many *levels of abstraction*

Outline

Chapter 1 Computer Abstractions and Technology

Chapter 4 The Processor

Controller

Chapter 2 Instructions:
Language of the Computer

Datapath

Chapter 3
Arithmetic for Computers

Memory

Chapter 5 Large and
Fast: Exploiting
Memory Hierarchy

Peripherals

Chapter 6 Storage and
Other I/O Topics

Chapter 7 Multicores, Multiprocessors, and Clusters

Core

Core

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Core

Grades

<input type="checkbox"/> Midterm exam	30%
<input type="checkbox"/> Final exam	30%~40%
<input type="checkbox"/> Homework	20~30%
<input type="checkbox"/> Quiz/attendance	20~30%
<input type="checkbox"/> Bonus	

Contact me:

Face-to-face: after each class, or making appointment in advance

Teams: any time (D000001209 謝萬雲)

E-mail: any time (wyshieh@mail.cgu.edu.tw)

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Line: any time (ID: shiehwy88)