**2019**

1. What are the three steps in the pulse modulation(PCM) encoder if we want to do analog-to-digital conversion?

取樣(Sampling)、量化(Quantizing)、編碼(Encoding)

1. What is the spread spectrum technology used for? Please illustrate the difference between the DSSS and FHSS and give an example of technology we use today to each one of them?
2. 展頻的目的主要是要抗干擾（人為刻意干擾或者自然的電磁波干擾）。
   1. 跳頻展頻（Frequency Hopping Spread Spectrum, FHSS）：在軍事用途上有使用過跳頻來竊聽。（每隔一段週期換一次頻率，以此來抗干擾，如藍芽）
   2. 直接序列展頻（Direct Sequence Spread Spectrum, DSSS）：IEEE 802.11協定中有採用直接序列展頻。（把多個通道當成一個bit傳送，即使一個通道有干擾也不會有太多影響，如Wi-Fi）
3. Use two-dimensional parity-check encoding to encode 1011011, 1100111, 0101000, 0111000.

1 0 1 1 0 1 1 1

1 1 0 0 1 1 1 1

0 1 0 1 0 0 0 0

0 1 1 1 0 0 0 1

0 1 0 1 1 0 0 1

1. If we want to design a 100 x 100 switch (N=100), we have to use 10,000 crosspoints inside the switch. However, the use of 10,000 crosspoints inside a switch will greatly increase the cost of the switch. One way to decrease the points of crosspoints is to use a three-stage switch design to implement the switch. Please illustrate how you design the three-stage switch if we want to reduce the number of crosspoints to be 1,200 crosspoints. Hint: , where k represents the number of crossbars and n is the number of lines of a ccrossbar. To fulfill this question you have to draw your design as well as show your calculation process.

≦ 1200

N = 100, let n = 10

∴2k100 + k = 300k

→ k ≦ 4

（要形成一個Three-stage switching array，而input switch的埠數n先預設為N的平方根）

1. Suppose you are using the cyclic redundancy check (CRC) to encode a dataword 10110011. The divisor 1011 is chosen. Please show the division in CRC encoder if you want to encode the dataword. What is your codeword finally? Your calculation process is needed.

1 0 0 0 0 0 1 1

1011 1 0 1 1 0 0 1 1 0 0 0

1 0 1 1

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0 0 0 0

0 0 0 0

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0 0 0 0

0 0 0 0

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0 0 0 1

0 0 0 0

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0 0 1 1

0 0 0 0

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0 1 1 0

0 0 0 0

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1 1 0 0

1 0 1 1

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1 1 1 0

1 0 1 1

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1 0 1

所以CRC code = 101（餘數）

the CodeWord : 10110011101（被除數+餘數）