

54321

COVER

Revision	Description	Date	Drawn	Checked
V10A	Initial	2019-02-13	xxx1zjxxx	

D

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Schematics Block:

DRV8711步进电机驱动测试

Power Block:

BUG

Schematics Index:

01 COVER
02 GPIO ASSIGNMENT
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GPIO ASSIGNMENT

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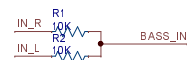
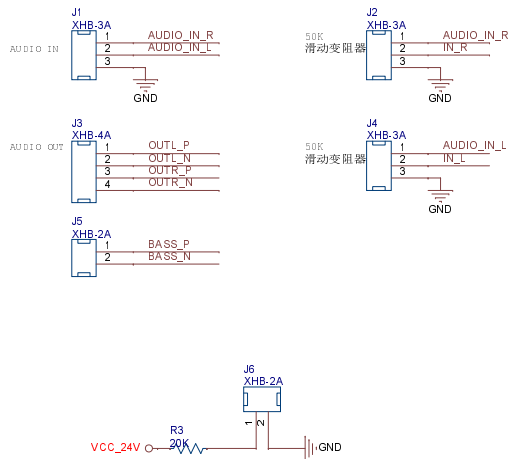
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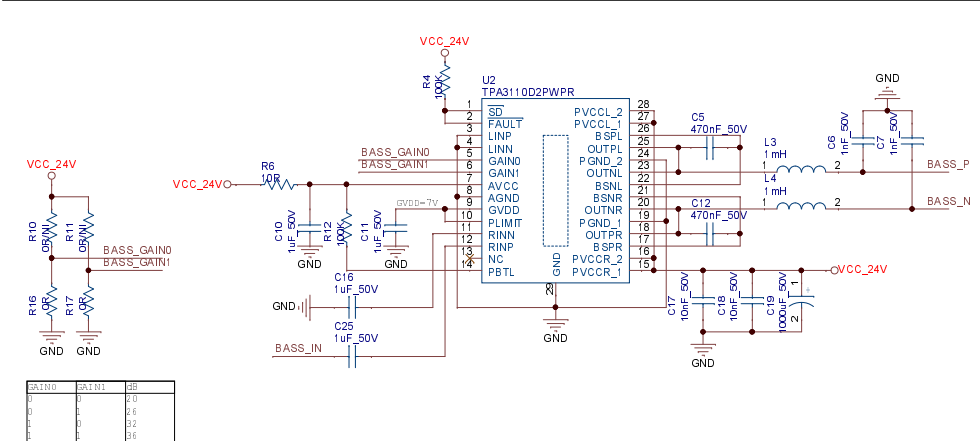
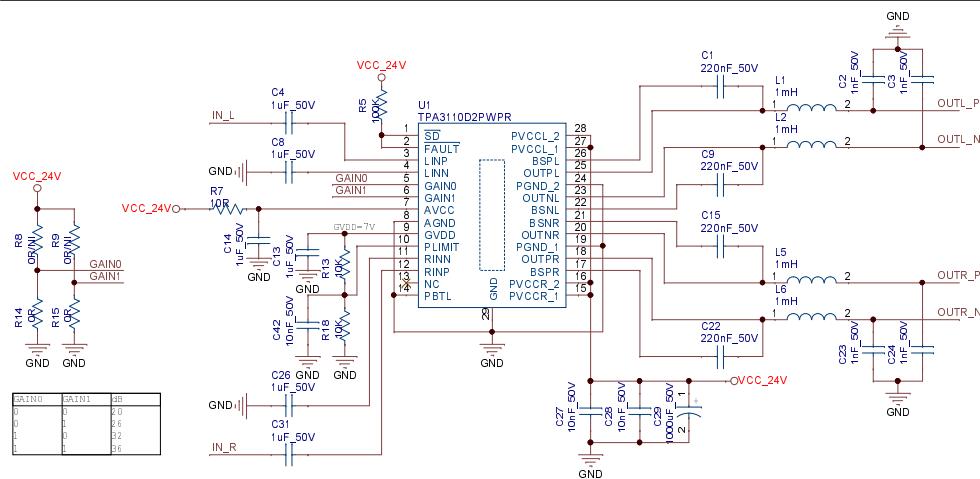
TPA3110D2

低频



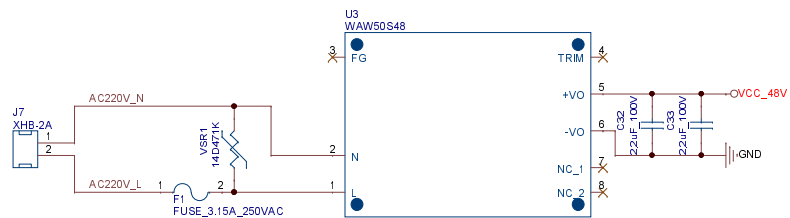
BTL

PBTL

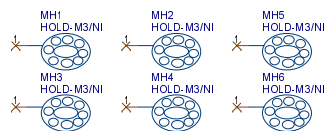
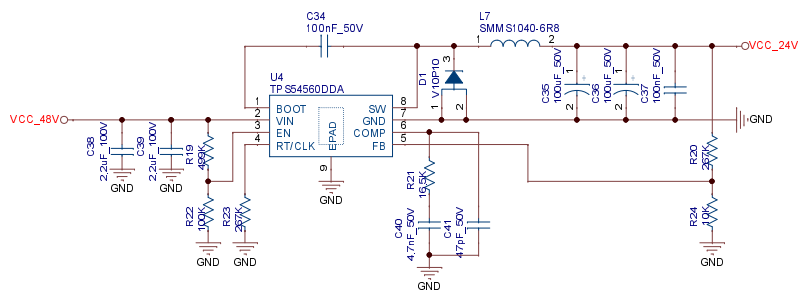


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