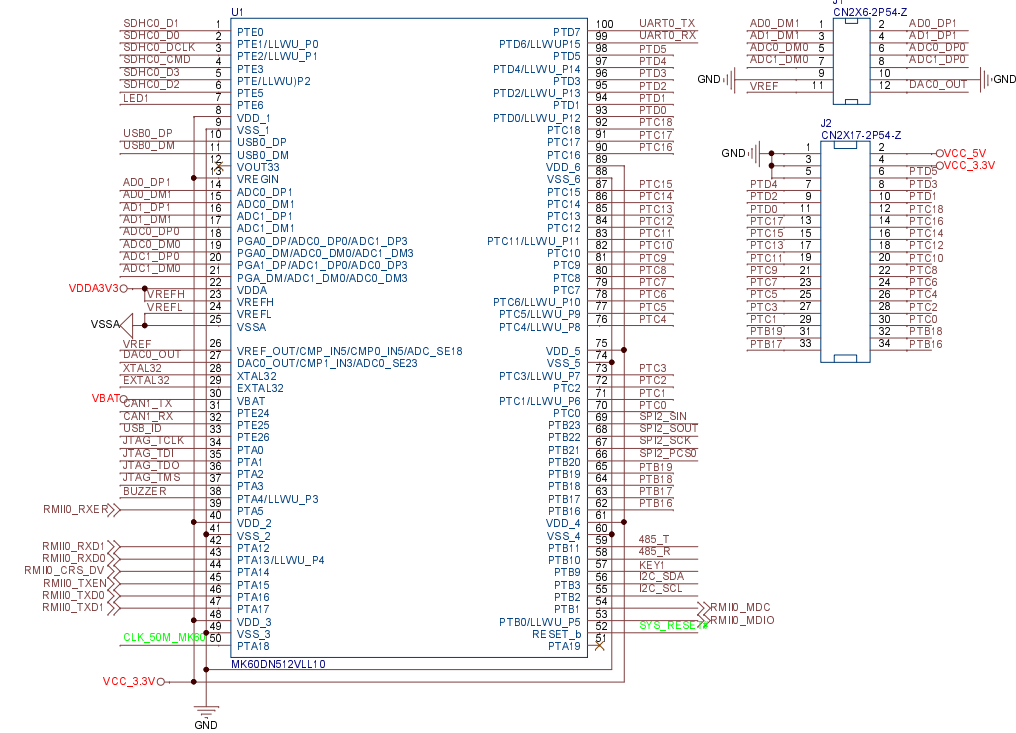


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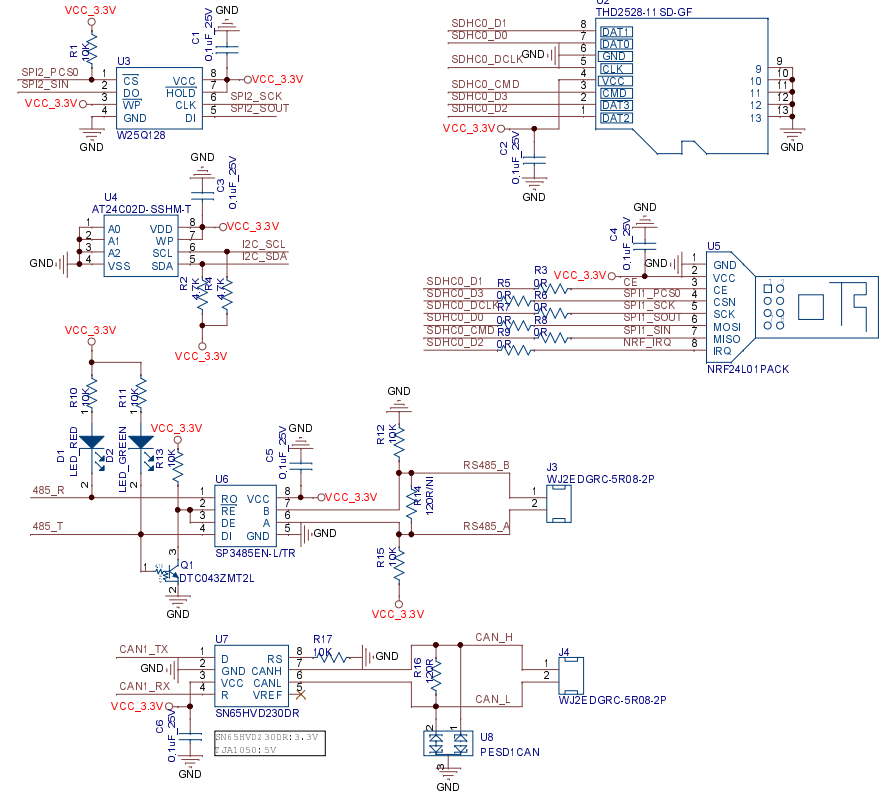
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D			MK60DN512VLL10											
C														
B			Power Block:											
A	Schematics Index:		BUG											
	01 COVER 02 MCU 03 POWER		<table><tr><td colspan="3">File &lt;File&gt;</td></tr><tr><td>Size A3</td><td>Document Number &lt;Doc&gt;</td><td>Rev ?</td></tr><tr><td>Date: Thursday, April 16, 2020</td><td>Sheet 1</td><td>of 3</td></tr></table>			File <File>			Size A3	Document Number <Doc>	Rev ?	Date: Thursday, April 16, 2020	Sheet 1	of 3
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Date: Thursday, April 16, 2020	Sheet 1	of 3												

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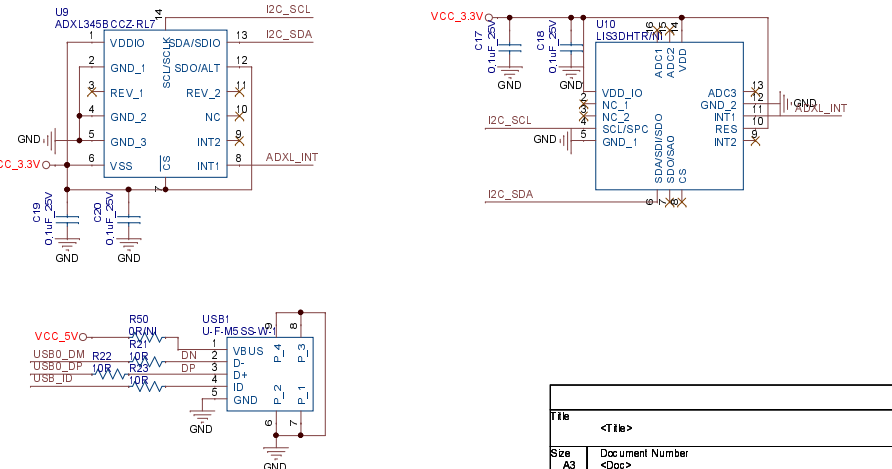
## 模块



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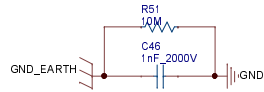
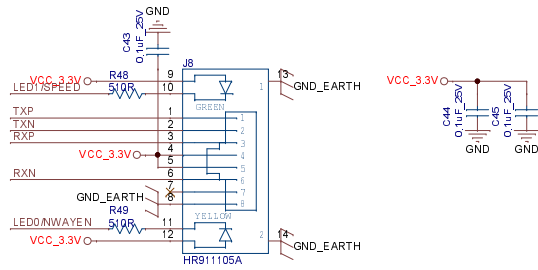
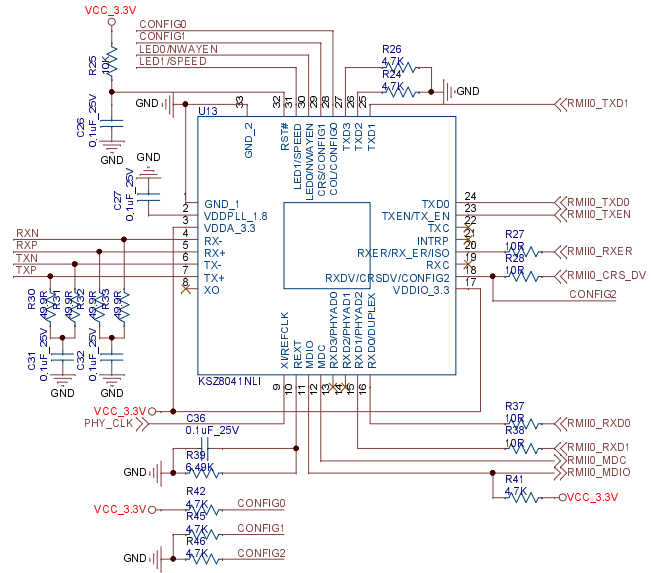


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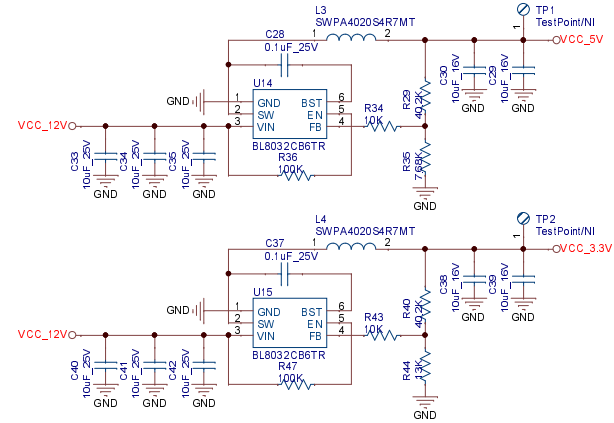
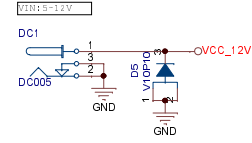


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# 网络



# 电源



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