

COVER

Revision	Description	Date	Drawn	Checked
V10A	Initial	2019-04-02	xxx1zjxxx	

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Schematics Block:

数码管时钟

Power Block:

BUG

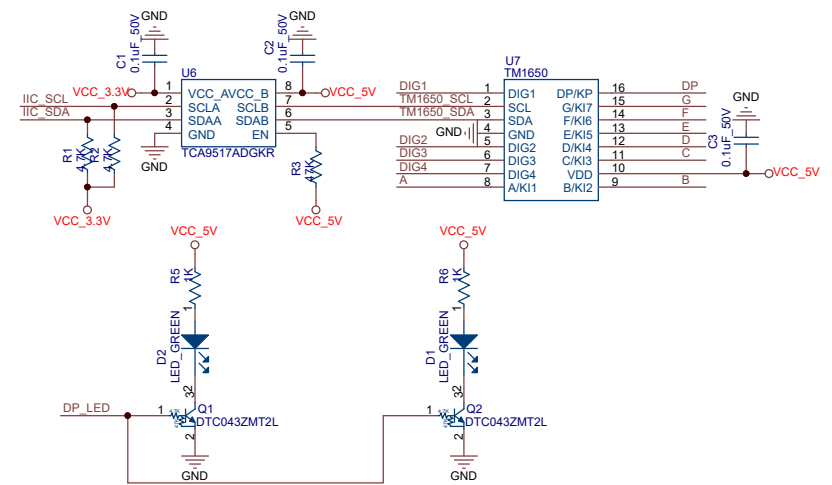
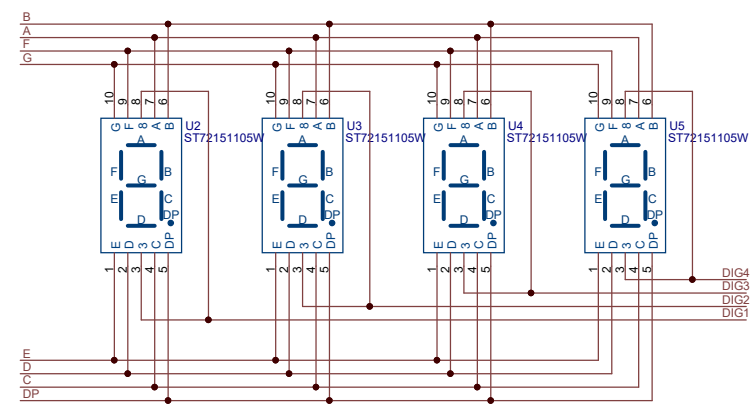
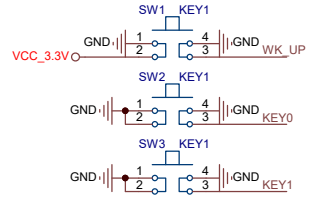
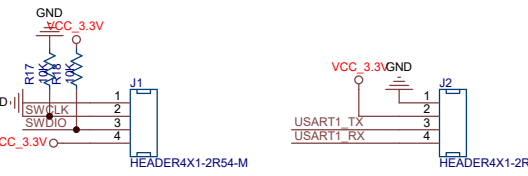
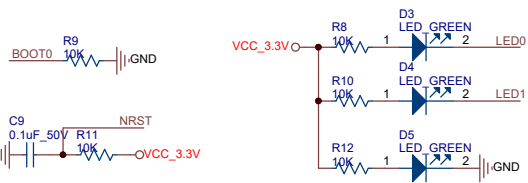
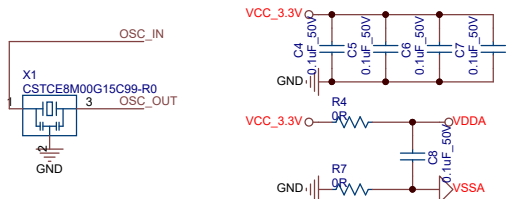
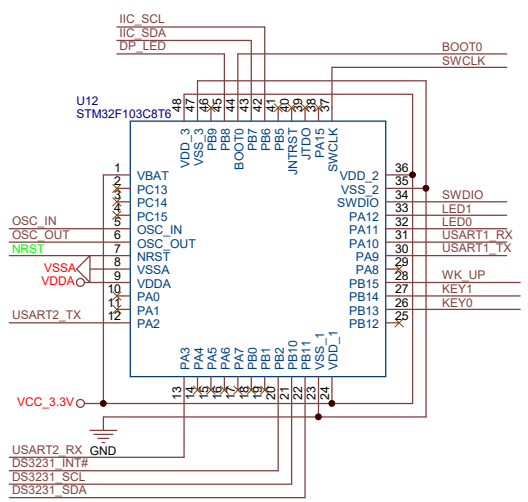
GPIO ASSIGNMENT

5		4		3		2		1	
D									
C									
B									
A									
5		4		3		2		1	

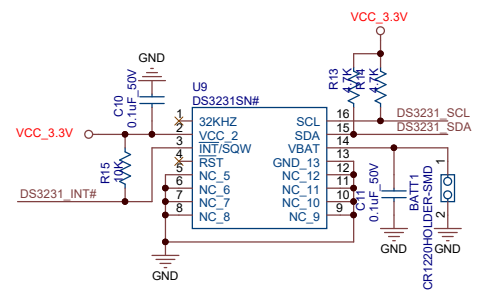
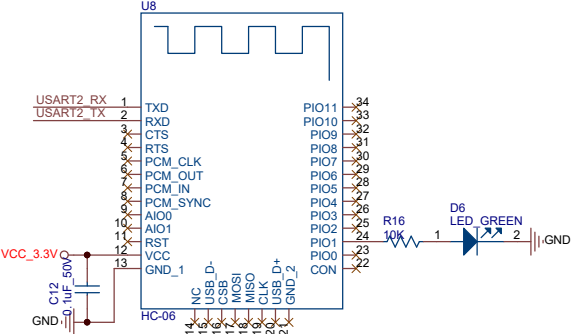
MCU:F103C8T6和L151CBT6兼容

驱动

D



蓝牙、RTC



A

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Title: DRV8711_TEST_V10A			
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DC输入

