

$$(1) A = \begin{bmatrix} a_{11} & a_{12} & a_{13} \\ a_{21} & a_{22} & a_{23} \\ a_{31} & a_{32} & a_{33} \end{bmatrix}, B = \begin{bmatrix} b_{11} & b_{12} & b_{13} \\ b_{21} & b_{22} & b_{23} \\ b_{31} & b_{32} & b_{33} \end{bmatrix}$$

Let  $b_{11}, b_{21}, b_{31}$  is B1,  $b_{12}, b_{22}, b_{32}$  is B2, and  $b_{13}, b_{23}, b_{33}$  is B3. Then  $B = [B1 \ B2 \ B3]$ .

$$AB = [AB1 \ AB2 \ AB3]$$

Let  $a_{11}, a_{12}, a_{13}$  is A1,  $a_{21}, a_{22}, a_{23}$  is A2, and  $a_{31}, a_{32}, a_{33}$  is A3. Then  $A = \begin{bmatrix} A1 \\ A2 \\ A3 \end{bmatrix}$ .

$$AB = \begin{bmatrix} A1B \\ A2B \\ A3B \end{bmatrix}.$$

Let  $A = [A1 \ A2]$ , A1 is  $m \times s$ , A2 is  $m \times (n-s)$ , and  $B = \begin{bmatrix} B1 \\ B2 \end{bmatrix}$ , B1 is  $s \times n$ , B2 is  $(n-s) \times n$ .

$$\text{Then } C_{ij} = \sum_{k=1}^n A_{ik} * B_{kj} = \sum_{k=1}^s A_{ik} * B_{kj} + \sum_{k=s+1}^n A_{ik} * B_{kj}.$$

If a matrix  $X = \begin{bmatrix} X_{11} & X_{12} \\ X_{21} & X_{22} \end{bmatrix}$ , and a matrix  $Y = \begin{bmatrix} Y_{11} & Y_{12} \\ Y_{21} & Y_{22} \end{bmatrix}$ , then if matrix  $C = XY$ , by above

proof, we know that  $X1 = \begin{bmatrix} X_{11} \\ X_{21} \end{bmatrix}$ ,  $X2 = \begin{bmatrix} X_{12} \\ X_{22} \end{bmatrix}$ ,  $Y1 = [Y_{11} \ Y_{12}]$ ,  $Y2 = [Y_{21} \ Y_{22}]$ , and

$$XY = [X1 \ X2] \begin{bmatrix} Y1 \\ Y2 \end{bmatrix} \Rightarrow X1 * Y1 = \begin{bmatrix} X_{11} * Y_{11} \\ X_{21} * Y_{11} \end{bmatrix} = \begin{bmatrix} X_{11} * Y_{11} & X_{11} * Y_{12} \\ X_{21} * Y_{11} & X_{21} * Y_{12} \end{bmatrix}.$$

$$\text{And so on, we can get } C = \begin{bmatrix} X_{11} * Y_{11} & \cdots & X_{1k} * Y_{kn} \\ \vdots & \ddots & \vdots \\ X_{m1} * Y_{m1} & \cdots & X_{mk} * Y_{kn} \end{bmatrix}$$

$$= \begin{bmatrix} C_{11} & \cdots & C_{1n} \\ \vdots & \ddots & \vdots \\ C_{m1} & \cdots & C_{mn} \end{bmatrix}.$$

(2) Equation 1:

By the question, we know the number of additions is  $n^3$  and number of multiples is  $n^3$ .

Equation 2:

$M = n/q$ , and  $M$  is new columns of  $A$  (new rows of  $B$ ). Similarly,  $N = n/p$ , and  $N$  is new row of  $A$  (new columns of  $B$ ). For  $C_{ij}$ , it will be added and multiplied  $M$  times, and for  $C$ , it exists  $N \times N$  element, so it need calculated  $N^2$  times.

For  $C_{ij}$ , there are  $p \times p$  elements and for each element we need calculated  $M \times q (=n)$  times. Hence, for each  $C_{ij}$ , the number of added and multiplied is  $p \times p \times n (n \times n^2/N^2)$ . Totally, there are  $N^2$  elements, so the number of added is  $N^2 \times n \times n^2/N^2 = n^3$ , and the number of multiplied is  $N^2 \times n \times n^2/N^2 = n^3$

(3)

執行情形：

```
4.642643 4.033143 3.859925 3.865853 4.616160 5.022504 5.099792
3.655306 3.604086 3.503014 3.608221 4.483868 4.912957 5.043349
3.534409 3.464788 3.466462 3.606980 4.423390 4.880773 4.985149
3.586524 3.505883 3.542294 3.601915 4.396093 4.798906 4.897858
3.947464 3.542253 3.470878 3.575130 4.364373 4.769017 4.820594
3.970912 3.545517 3.459854 3.572529 4.352968 4.762901 4.888511
3.982609 3.540711 3.685007 3.917209 4.966024 5.064197 4.960946
Program ended with exit code: 0
```

環境：

OS : macOS 10.15.5

CPU : 2.4GHz 4 cores intel Core i5

IDE : Xcode v12.0.1

(4)

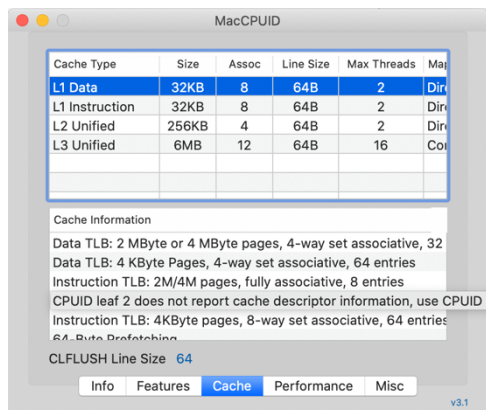
p \ q	4	8	16	32	64	128	256
4	4.642643	4.033143	3.859925	3.865853	4.616160	5.022504	5.099792
8	3.655306	3.604086	3.503014	3.608221	4.483868	4.912957	5.043349
16	3.534409	3.464788	3.466462	3.606980	4.423390	4.880773	4.985149
32	3.586524	3.505883	3.542294	3.601915	4.396093	4.798906	4.897858
64	3.947464	3.542253	3.470878	3.575130	4.364373	4.769017	4.820594
128	3.970912	3.545517	3.459854	3.572529	4.352968	4.762901	4.888511
256	3.982609	3.540711	3.685007	3.917209	4.966024	5.064197	4.960946

(5)

a.

快取(cache)是一種暫存的記憶體。在 CPU 處理資料的時候會先從 cache 中找尋資料，如果從中有找到暫存的資料或指令，就不需要到 main memory 中找尋，因為 CPU 的處理速度大多比 main memory 讀取速度快，使用 cache 較能有效的利用資源。此外，現今的 cache 還會分成 L1、L2、L3 等等，依據層級的不同，cache 的儲存量及存取速度也都不同。

b. cache size of my computer.



Cache Type	Size	Assoc	Line Size	Max Threads	Max
L1 Data	32KB	8	64B	2	Dir
L1 Instruction	32KB	8	64B	2	Dir
L2 Unified	256KB	4	64B	2	Dir
L3 Unified	6MB	12	64B	16	Cov

Cache Information

Data TLB: 2 MByte or 4 MByte pages, 4-way set associative, 32 entries  
 Data TLB: 4 KByte Pages, 4-way set associative, 64 entries  
 Instruction TLB: 2M/4M pages, fully associative, 8 entries  
 CPUID leaf 2 does not report cache descriptor information, use CPUID leaf 3  
 Instruction TLB: 4KByte pages, 8-way set associative, 64 entries  
 64-Byte Prefetching  
 CLFLUSH Line Size 64

Info Features **Cache** Performance Misc v3.1

c. Explain the reason of the performance differences for various p and q.

因為 matrix 的乘法會不斷讀取到 cache，但一般的 matrix multiple 無法有效的利用從 cache 拿出來的資料，造成時間的浪費，因此使用 block matrix multiple。

以我的電腦來看 cache L1 Data 的 size 是 32KB，而 size of cache line size 是 64byte，也就是共有 512 個 line，而一條 line 可以放 16 個 int。因此可以看出來當 p 相同的時候，q 是 16 的使用時間是最少的，這是因為可以一次拿出一條 cache 所有的資料並全數用完。當 q 大於 16，就需要讀到第二條 cache。而如果 q 小於 16，同一條 line 則需要重複讀取，浪費掉了資源。