# Vittor M. D. Longati

LinkedIn | GitHub | Website

## Work Experience

**Wagner Claims Adjustment –** Curitiba/PR Jan. 2025 – Present
**Claims Analyst**

- I manage the entire lifecycle of an insurance claim, from the initial paperwork all the way to its resolution.
- So far, I've successfully handled over **200 cases**, making sure each one was processed correctly from start to finish.

**Methal Company Group** – Fazenda Rio Grande/PR Apr. 2023 – Oct. 2024
**Manufacturing Technician Apprentice**

- My main goal was to use this data to find bottlenecks and come up with concrete ways to improve our production process.
- I built and implemented models in **Excel** and **Power BI**. With these tools, I discovered we were losing over **300 hours** of production every month from failures in machinery and logistics during parts manufacturing.

## Projects

**ZIP Search (React.js + Node.js)** • Personal project

- Developed a responsive and user-friendly **SPA** to provide real-time address lookups based on Brazilian postal codes (CEP).
- Integrated the external ViaCEP **RESTful API** to asynchronously fetch and display address information, implementing comprehensive error handling for invalid inputs or API failures.
- Managed application state, including user input, API data, and loading status, efficiently using **React Hooks**.

**Chess Game (Java Swing)** • Final grade of **100%**

- I created a **persistent game state** feature using **serialization**, allowing players to save the current board state to a file and load it later to resume their game.
- I constructed an interactive and intuitive **GUI** from the ground up using the **Swing** framework, featuring a clickable board and buttons for starting a new game, loading, and viewing rankings.
- I utilized a **HashMap** to cache resized image icons for the chess pieces, optimizing rendering performance by avoiding redundant disk I/O and image processing each time the board is updated.

**MIPS Simulator (C)** • Final grade of **100%**

- I co-engineered the program to read **MIPS** assembly code from either standard input or a specified file, providing flexibility for testing and execution.
- Implemented the core logic to process **R-type**, **I-type**, and **J-type** instructions, correctly manipulating a simulated register file and memory.
- I co-designed the simulator to output the final state of all **32 registers** after program execution, offering clear visibility into the program's impact on the **CPU state**.

# Education

**Bachelor's degree in Computer Science** Jul. 2023 – Jul. 2027
Positivo University • UP

**Technical degree in Information Technology** Feb. 2020 – Dec. 2022
Pontifical Catholic University of Paraná • PUCPR