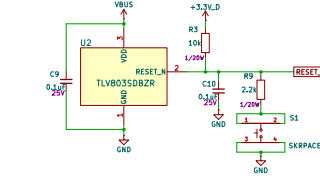
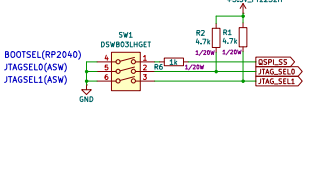


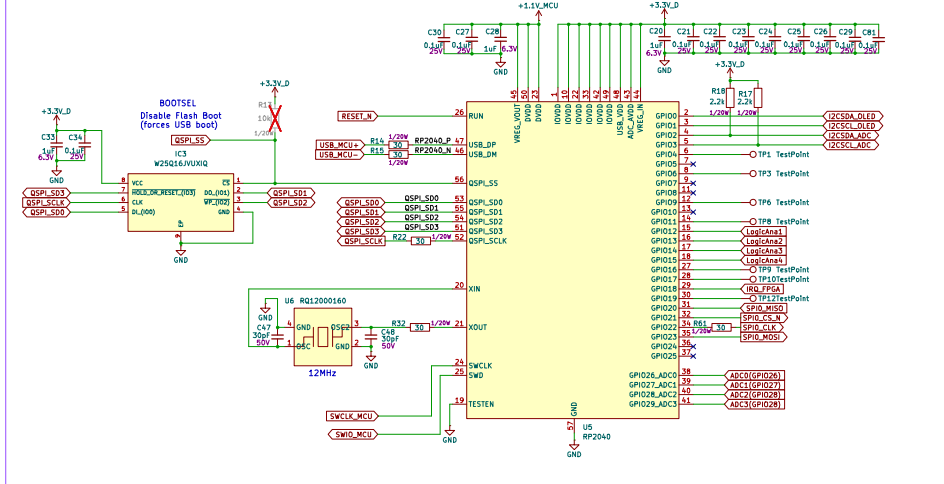
### Power on Reset



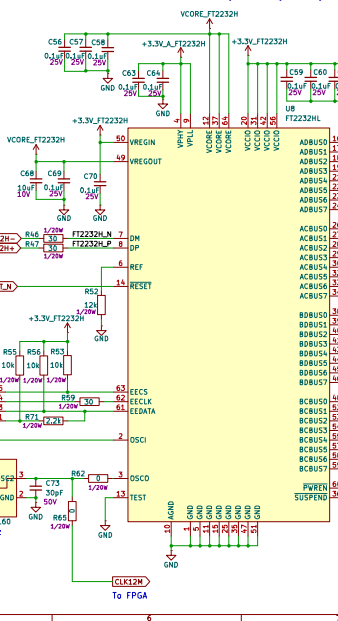
### DIP SW



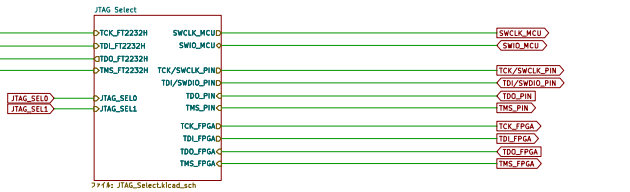
### CPU (RP2040)



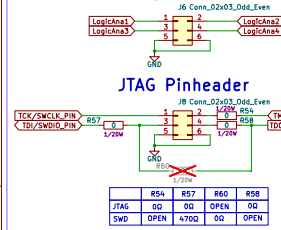
### USB to UART/JTAG/SPI/I2C (FT232HL)



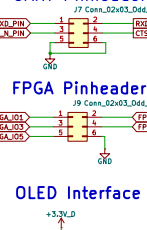
### JTAG Select



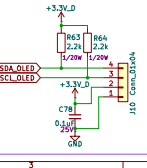
### Analyzer Pinheader



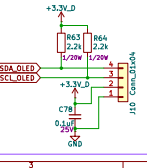
### UART Pinheader



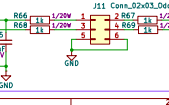
### FPGA Pinheader



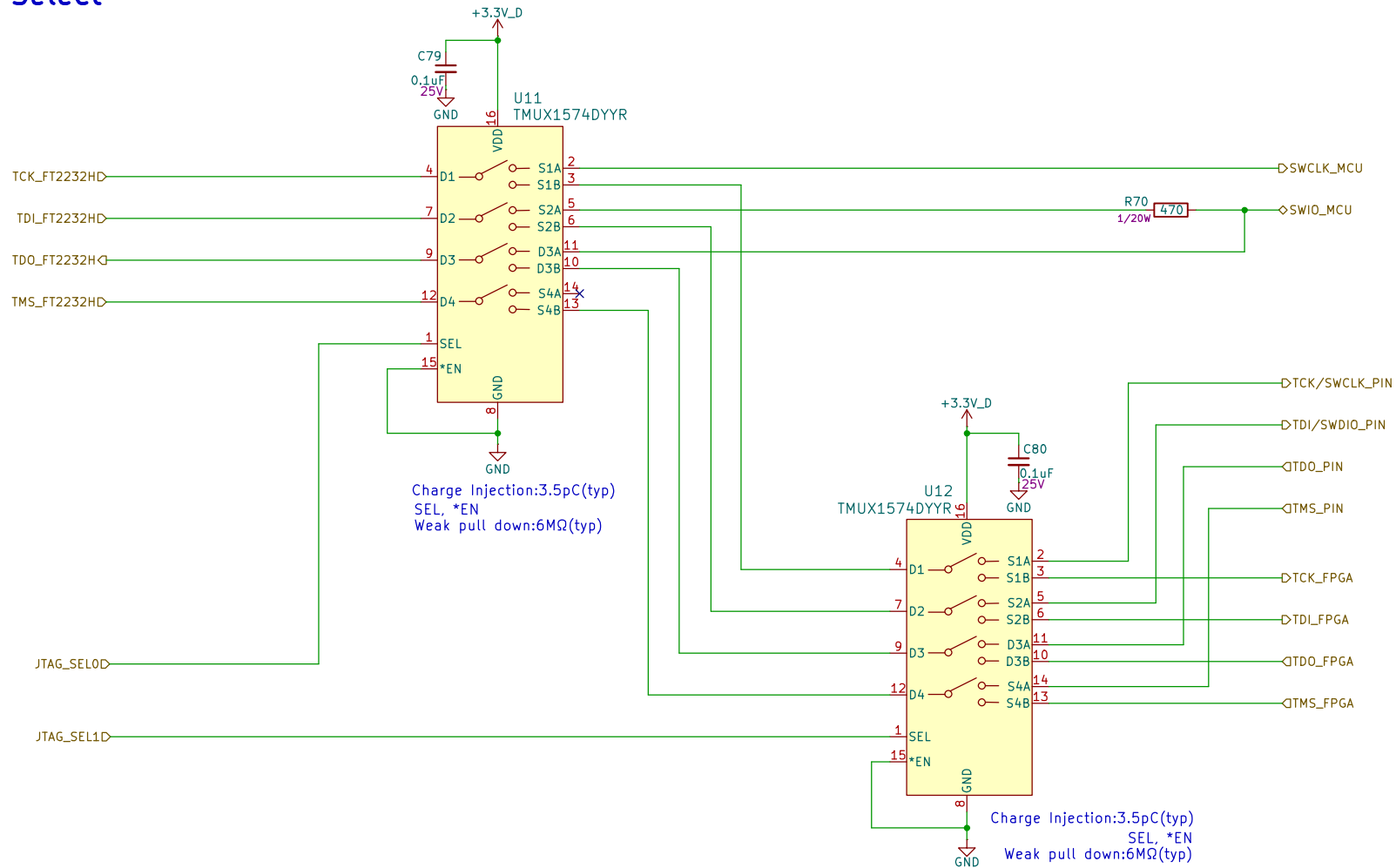
### OLED Interface



### ADC Pinheader



# JTAG Select



JTAG_SEL	接続先
00(default)	MCU
01	PIN Header
10	MCU
11	FPGA

Sheet: /JTAG Select/  
File: JTAG\_Select.kicad\_sch

## Title:

Size: A4

Date:

KiCad E.D.A. 8.0.5

Rev:

Id: 3/2