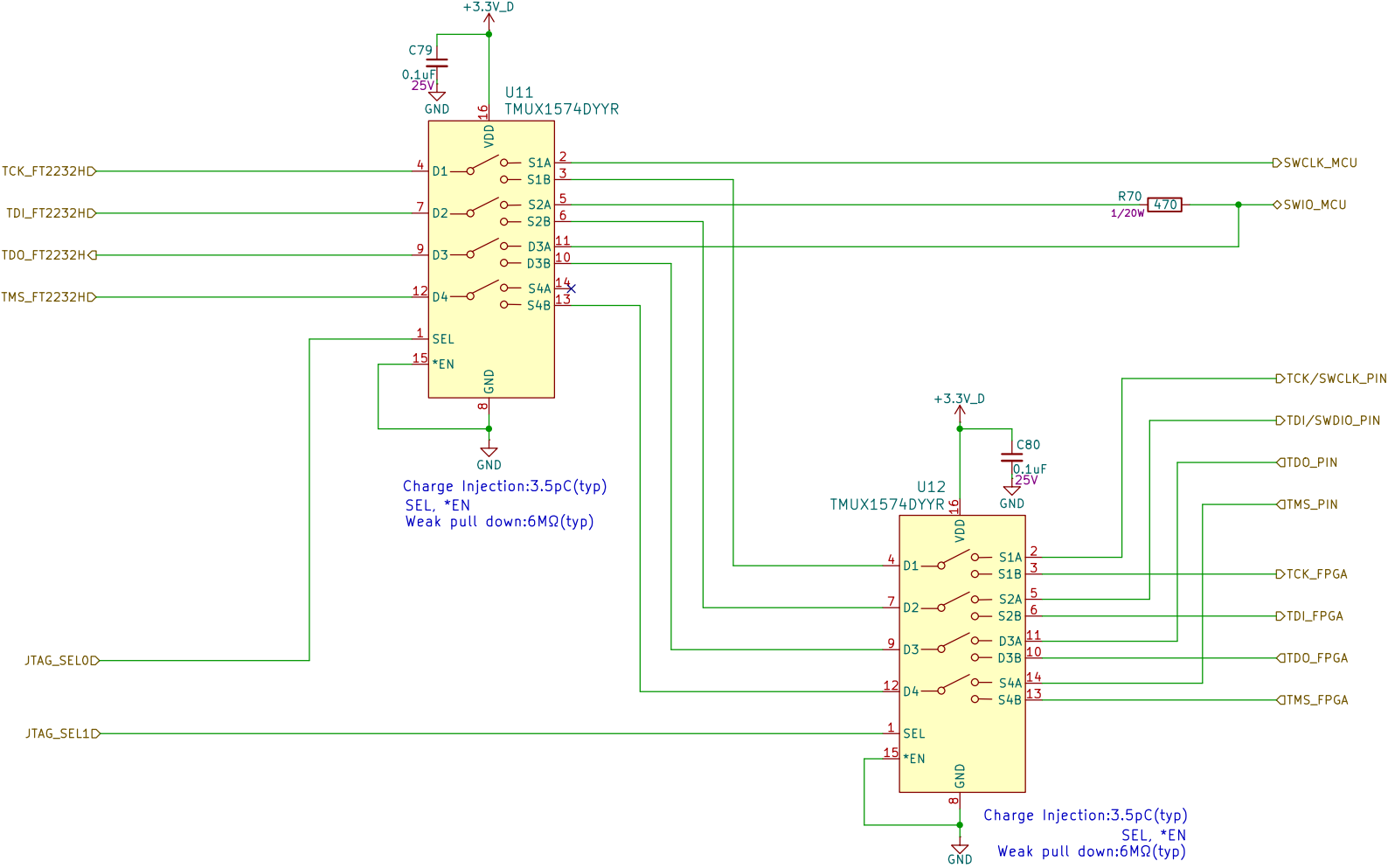


JTAG Select



JTAG_SEL(DPSW 2,3)	接続先
00/01	MCU
10	PIN Header
11(default)	FPGA