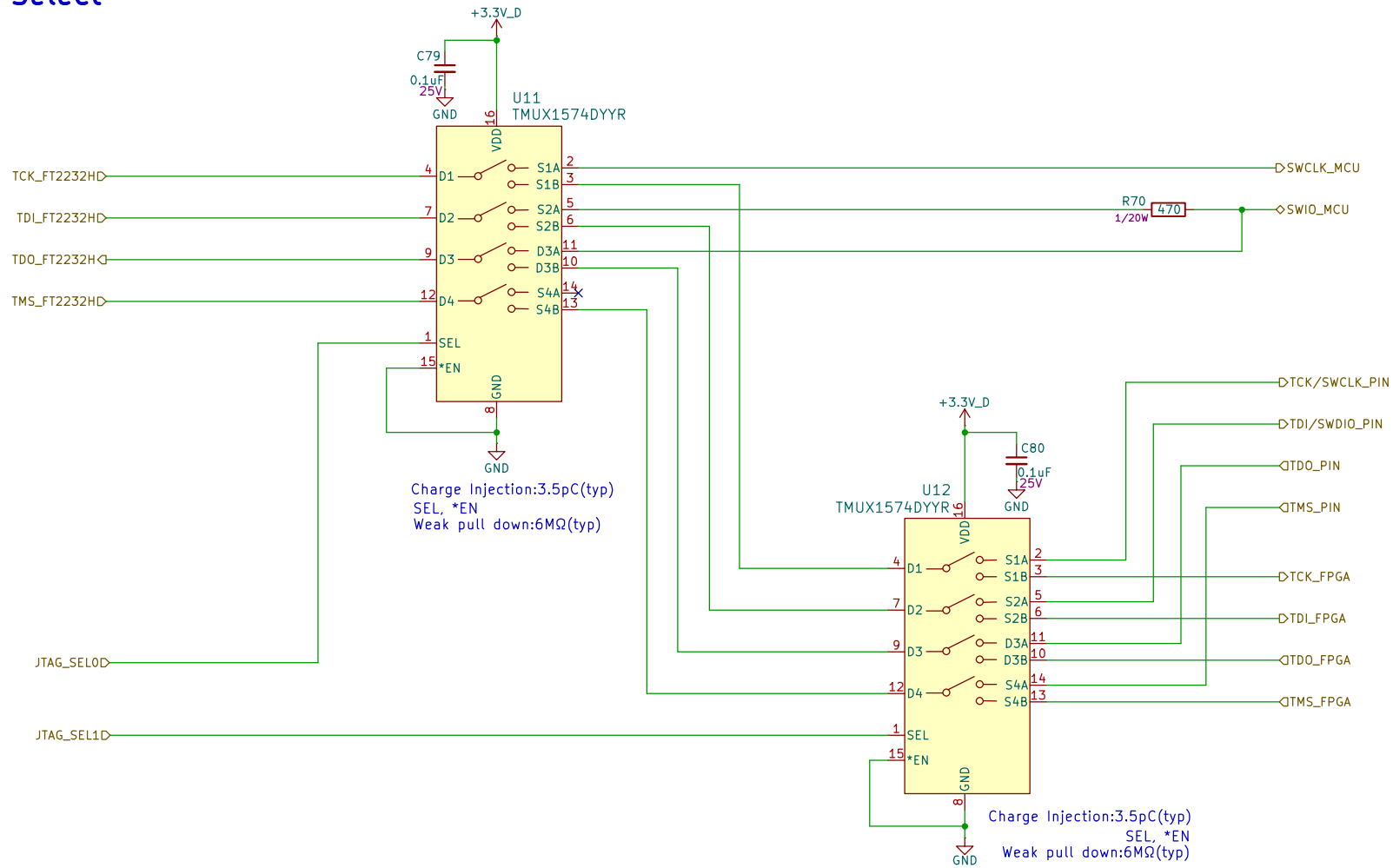


# JTAG Select



JTAG_SEL(DPSW 2,3)	接続先
00/01	MCU
10	PIN Header
11(default)	FPGA

Sheet: /JTAG Select/  
File: JTAG\_Select.kicad\_sch

## Title:

Size: A4

Date:

KiCad E.D.A. 8.0.8

Rev:

Id: 3/2