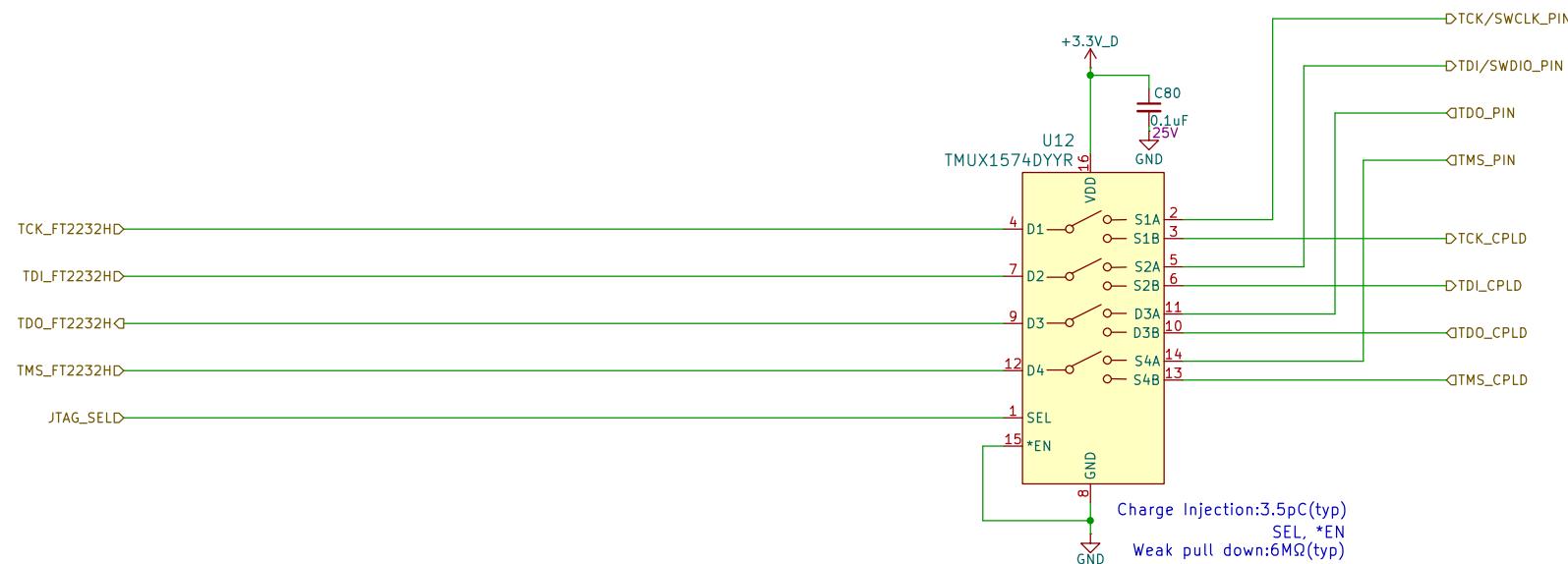


JTAG Select



JTAG_SEL(DPSW 2)	接続先
0	PIN Header
1(default)	CPLD

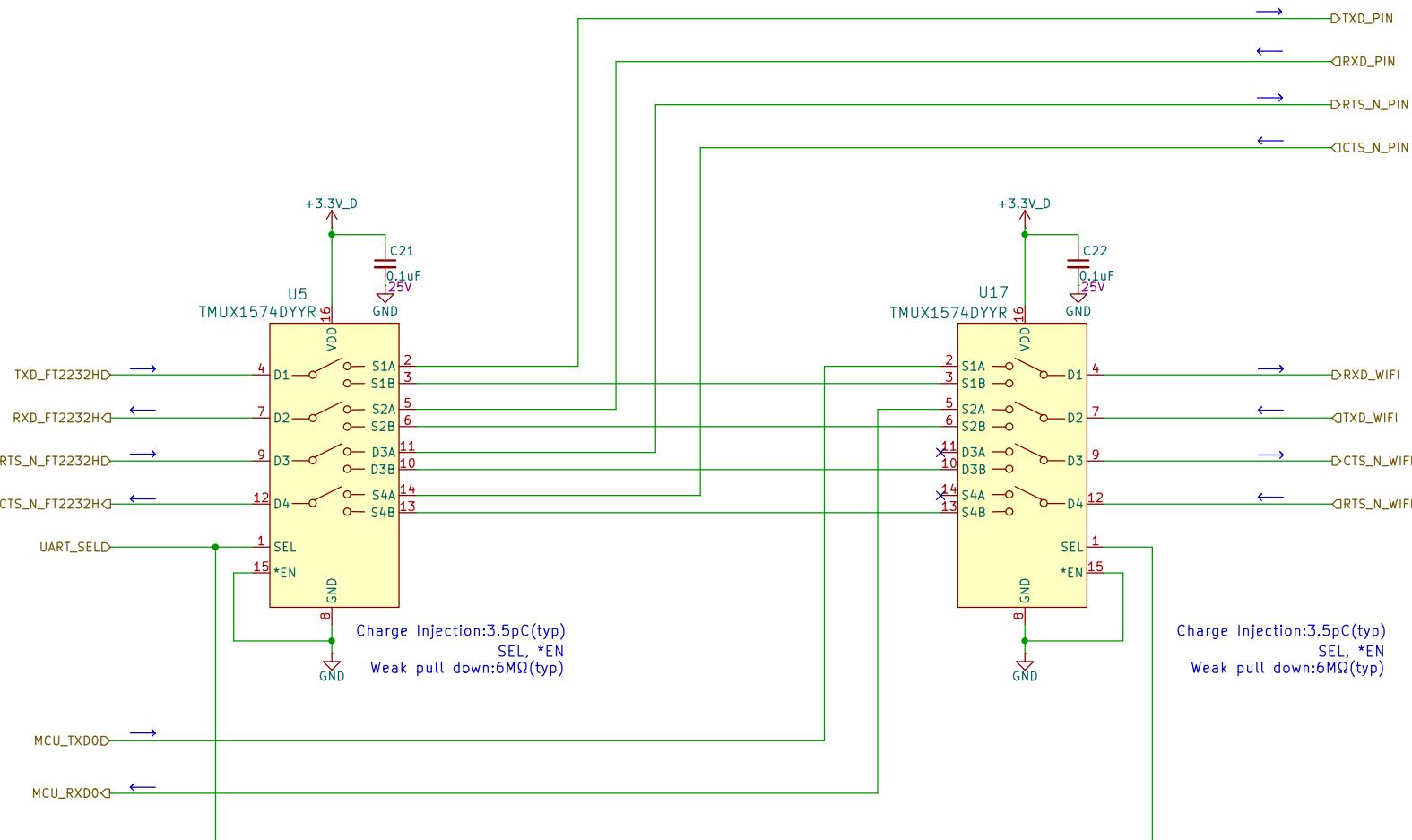
Sheet: /JTAG_Select/
File: JTAG_Select.kicad_sch

Title:

Size: A4 | Date:
KiCad E.D.A. 9.0.6

Rev:
Id: 3/3

UART Select



UART_SEL(DPSW 3)	接続先
0	FT2232H to PIN Header
1(default)	FT2232H to ESP-12F
	-

Sheet: /UART Select/
File: UART Select.kicad_sch

Title:

Size: A4 Date:
KiCad E.D.A. 9.0.6

Rev:
Id: 3/3