LDI R16, \$00; R16 = 00

LDI R17, \$00; R17 = 00

LDI R30, \$00; R30 = 00

LDI R31, \$01; R31 = 01

L1: LD R18, Z+; R18 = [R31:R30], Z++

CPI R30, \$00; R30?00

BREQ L3; If Equal then go L3 (Finish)

L2: LSL R18 ; Logical Shift Left R18 (and store C)

ADC R16, R17; Add 2R with C

CPI R18, \$00; R18? 00

BRNE L2 ;If Not Equal the go L2

RJMP L1 ;Jump L1

L3: