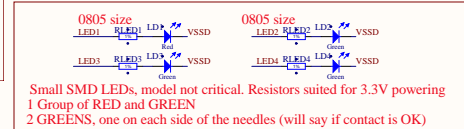
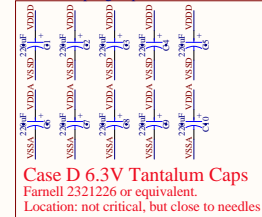
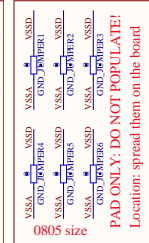
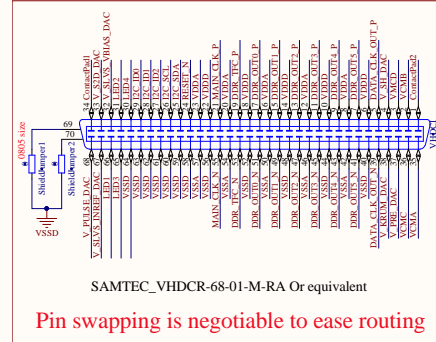
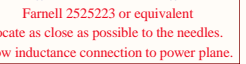


PRELIMINARY, SUBJECT TO CHANGES



Lines marked as differential pairs must have 100 Ohm +/-10% differential impedance.
 Vias and stubs should be avoided.
 Solid continuous VSSD copper planes under them are required. (Also over them if stripline approach used)
 Desired bandwidth > 960 MHz

Power lines are: VDDA, VSSA, VDDD, VSSD (A for Analogue, D for Digital)
 Solid planes are requested. Powering through a single line is not acceptable.
 One layer per signal is preferred
 35um copper thickness preferred

- Review Power Supply Pin Assignments
- Review Scan Enable, Test Mode, SDI, SDO, ...
- Review Decoupling Network
- Review Unused pads

PRELIMINARY, SUBJECT TO CHANGES