LEVEL02:

```
-rwsr-s--+ 1 level03 users 9452 Sep 10 2016 level02
level02@OverRide:~$ ./level02
  = [ Secure Access System v1.0 ] =
| You must login to access this system. |
--[ Username: dat_will
************
dat_will does not have access!
level02@OverRide:~$ ./level02
  = [ Secure Access System v1.0 ] =
I You must login to access this system. I
*********
-[ Username: level03
eeeeeeeeeeeeeeeeeeeeeeeeeeeeeeeeeee
level03 does not have access!
level02@OverRide:~$
```

We can observe that the program reads twice on stdin. We do not know where the strings are stored yet. It doesn't **segfault** from the inputs given above, maybe we haven't reach the limit but we do not seem to overflow any return address.

Readelf -h:

level02@OverRide:~\$ readelf -h level02 ELF Header: Magic: 7f 45 4c 46 02 01 01 00 00 00 00 00 00 00 00 00 ELF64 Class: Data: 2's complement, little endian Version: 1 (current) UNIX - System V OS/ABI: ABI Version: EXEC (Executable file) Type: Machine: Advanced Micro Devices X86-64 Version: 0x1 Entry point address: 0x400730 Start of program headers: 64 (bytes into file) Start of section headers: 4976 (bytes into file) Flags: 0x0 Size of this header: 64 (bytes) Size of program headers: 56 (bytes) Number of program headers: Size of section headers: 64 (bytes) Number of section headers: Section header string table index: 27 level02@OverRide:~\$

This is a 64bit binary

Strings:

```
ex02 strings ../Debug_files/level02
/lib64/ld-linux-x86-64.so.2
__amon_start__
libc.so.6
exit
fopen
strncmp
puts
stdin
printf
fgets
strcspn
fclose
stderr
system
fwrite
fread
__libc_start_main
GLIBC_2.2.5
fff.
)tRH
1$ L
t$(L
1$0H
/home/users/level03/.pass
ERROR: failed to open password file
ERROR: failed to read password file
==== [ Secure Access System v1.0 ] =====
/*****************************\
| You must login to access this system. |
--[ Username:
--[ Password:
************
Greetings, %s!
/bin/sh
does not have access!
GCC: (Ubuntu/Linaro 4.6.3-1ubuntu5) 4.6.3
.symtab
.strtab
```

```
00000000000400814 <main>:
 400814:
               55
                                        push
                                               %rbp
 400815:
                48 89 e5
                                               %rsp,%rbp
                                        mov
 400818:
                48 81 ec 20 01 00 00
                                                $0x120,%rsp
                                        sub
 40081f:
                89 bd ec fe ff ff
                                               %edi,-0x114(%rbp)
                                        mov
                                               %rsi,-0x120(%rbp)
 400825:
                48 89 b5 e0 fe ff ff
                                        mov
                48 8d 55 90
                                                -0x70(%rbp),%rdx
 40082c:
                                        lea
               b8 00 00 00 00
 400830:
                                               $0x0,%eax
                                        mov
 400835:
               b9 0c 00 00 00
                                        mov
                                                $0xc,%ecx
 40083a:
               48 89 d7
                                               %rdx,%rdi
                                        mov
 40083d:
                f3 48 ab
                                        rep stos %rax,%es:(%rdi)
 400840:
                48 89 fa
                                               %rdi,%rdx
                                        mov
 400843:
                89 02
                                               %eax,(%rdx)
                                        mov
 400845:
                48 83 c2 04
                                        add
                                                $0x4,%rdx
                48 8d 95 60 ff ff ff
                                               -0xa0(%rbp),%rdx
 400849:
                                        1ea
 400850:
                b8 00 00 00 00
                                                $0x0,%eax
                                        mov
 400855:
               b9 05 00 00 00
                                                $0x5,%ecx
                                        mov
                                               %rdx,%rdi
                48 89 d7
 40085a:
                                        mov
 40085d:
                f3 48 ab
                                        rep stos %rax,%es:(%rdi)
 400860:
                48 89 fa
                                               %rdi,%rdx
                                        mov
 400863:
                88 02
                                               %al,(%rdx)
                                        mov
                48 83 c2 01
 400865:
                                               $0x1,%rdx
                                        add
 400869:
                48 8d 95 f0 fe ff ff
                                        lea
                                                -0x110(%rbp),%rdx
               b8 00 00 00 00
 400870:
                                        mov
                                                $0x0,%eax
 400875:
               b9 0c 00 00 00
                                               $0xc,%ecx
                                        mov
 40087a:
                48 89 d7
                                                %rdx,%rdi
                                        mov
                f3 48 ab
 40087d:
                                        rep stos %rax,%es:(%rdi)
 400880:
                48 89 fa
                                        mov
                                               %rdi,%rdx
 400883:
                89 02
                                        mov
                                               %eax,(%rdx)
 400885:
                48 83 c2 04
                                        add
                                               $0x4,%rdx
 400889:
                48 c7 45 f8 00 00 00
                                               $0x0,-0x8(%rbp)
                                        movq
 400890:
                00
 400891:
                c7 45 f4 00 00 00 00
                                        movl
                                               $0x0,-0xc(%rbp)
                ba b0 0b 40 00
 400898:
                                                $0x400bb0, %edx
                                        mov
               b8 b2 0b 40 00
                                               $0x400bb2,%eax
 40089d:
                                        mov
 4008a2:
                48 89 d6
                                               %rdx,%rsi
                                        mov
 4008a5:
                48 89 c7
                                        mov
                                               %rax,%rdi
 4008a8:
                e8 53 fe ff ff
                                        callq 400700 <fopen@plt>
 4008ad:
                48 89 45 f8
                                               %rax,-0x8(%rbp)
                                        mov
 4008b1:
                48 83 7d f8 00
                                               $0x0,-0x8(%rbp)
                                        cmpq
 4008b6:
                75 2e
                                        jne
                                                4008e6 <main+0xd2>
 4008b8:
                48 8b 05 91 09 20 00
                                                                           # 601250 <stderr@@GLIBC_2.2.5>
                                               0x200991(%rip),%rax
                                        mov
 4008bf:
                48 89 c2
                                               %rax,%rdx
                                        mov
 4008c2:
               b8 d0 0b 40 00
                                                $0x400bd0,%eax
                                        mov
 4008c7:
                48 89 d1
                                               %rdx,%rcx
                                        mov
 4008ca:
                ba 24 00 00 00
                                                $0x24,%edx
                                        mov
                be 01 00 00 00
                                                $0x1,%esi
 4008cf:
                                        mov
  4008d4:
                48 89 c7
                                               %rax,%rdi
                                        mov
 4008d7:
                e8 44 fe ff ff
                                        callq 400720 <fwrite@plt>
```

```
bf 01 00 00 00
                                              $0x1,%edi
4008dc:
                                       mov
4008e1:
              e8 Za fe ff ff
                                       callq 400710 <exit@plt>
              48 8d 85 60 ff ff ff
4008e6:
                                              -0xa0(%rbp),%rax
                                       1ea
4008ed:
              48 8b 55 f8
                                              -0x8(%rbp),%rdx
                                       mov
4008f1:
              48 89 d1
                                       mov
                                              %rdx,%rcx
              ba 29 00 00 00
4008f4:
                                              $0x29,%edx
                                       mov
              be 01 00 00 00
4008f9:
                                       mov
                                              $0x1,%esi
4008fe:
              48 89 c7
                                              %rax,%rdi
                                       mov
400901:
              e8 8a fd ff ff
                                       callq 400690 <fread@plt>
              89 45 f4
400906:
                                       mov
                                              %eax,-0xc(%rbp)
                                              -0xa0(%rbp),%rax
$0x400bf5,%esi
400909:
              48 8d 85 60 ff ff ff
                                       lea
400910:
              be f5 0b 40 00
                                       mov
400915:
                                              %rax,%rdi
              48 89 c7
                                       mov
400918:
              e8 b3 fd ff ff
                                       callq
                                              4006d0 <strcspn@plt>
40091d:
              c6 84 05 60 ff ff ff
                                       mo∨b
                                              $0x0,-0xa0(%rbp,%rax,1)
400924:
              00
400925:
              83 7d f4 29
                                       cmpl
                                              $0x29,-0xc(%rbp)
400929:
              74 52
                                              40097d <main+0x169>
                                       jе
40092b:
              48 8b 05 1e 09 20 00
                                              0x20091e(%rip),%rax
                                       mov
                                                                          # 601250 <stderr@@GLIBC_2.2.5>
400932:
              48 89 c2
                                       mov
                                              %rax,%rdx
                                              $0x400bf8,%eax
400935:
              b8 f8 0b 40 00
                                       mov
40093a:
              48 89 d1
                                              %rdx,%rcx
                                       mov
40093d:
              ba 24 00 00 00
                                              $0x24,%edx
                                       mov
400942:
              be 01 00 00 00
                                              $0x1,%esi
                                       mov
400947:
              48 89 c7
                                       mov
                                              %rax,%rdi
              e8 d1 fd ff ff
40094a:
                                       calla
                                              400720 <fwrite@plt>
40094f:
              48 8b 05 fa 08 20 00
                                              0x2008fa(%rip),%rax
                                                                          # 601250 <stderr@@GLIBC_2.2.5>
400956:
              48 89 c2
                                              %rax,%rdx
                                       mov
400959:
              b8 f8 0b 40 00
                                              $0x400bf8, %eax
                                       mov
              48 89 d1
40095e:
                                       mov
                                              %rdx,%rcx
400961:
              ba 24 00 00 00
                                              $0x24,%edx
                                      mov
              be 01 00 00 00
                                              $0x1,%esi
400966:
                                       mov
40096b:
              48 89 c7
                                              %rax,%rdi
                                       mov
40096e:
              e8 ad fd ff ff
                                              400720 <fwrite@plt>
                                       calla
400973:
              bf 01 00 00 00
                                       mov
                                              $0x1,%edi
              e8 93 fd ff ff
400978:
                                       callq 400710 <exit@plt>
              48 8b 45 f8
40097d:
                                       mov
                                              -0x8(%rbp),%rax
400981:
              48 89 c7
                                              %rax,%rdi
                                       mov
400984:
              e8 17 fd ff ff
                                       callq 4006a0 <fclose@plt>
              bf 20 0c 40 00
400989:
                                       mov
                                              $0x400c20,%edi
40098e:
              e8 ed fc ff ff
                                       callq 400680 <puts@plt>
              bf 50 0c 40 00
400993:
                                       mov
                                              $0x400c50, %edi
400998:
              e8 e3 fc ff ff
                                       callq 400680 <puts@plt>
40099d:
              bf 80 0c 40 00
                                              $0x400c80, %edi
                                       mov
4009a2:
              e8 d9 fc ff ff
                                       callq 400680 <puts@plt>
              bf b0 0c 40 00
                                              $0x400cb0,%edi
4009a7:
                                       mov
              e8 cf fc ff ff
4009ac:
                                       calla
                                              400680 <puts@plt>
              b8 d9 0c 40 00
4009b1:
                                              $0x400cd9,%eax
                                       mov
4009b6:
              48 89 c7
                                              %rax,%rdi
                                       mov
              b8 00 00 00 00
4009b9:
                                              $0x0,%eax
                                       mov
```

```
4006c0 <printf@plt>
4009be:
              e8 fd fc ff ff
                                       calla
              48 8b 05 7e 08 20 00
4009c3:
                                       mov
                                              0x20087e(%rip),%rax
                                                                          # 601248 <__bss_start>
4009ca:
              48 89 c2
                                              %rax,%rdx
                                              -0x70(%rbp),%rax
4009cd:
              48 8d 45 90
                                       lea
4009d1:
              be 64 00 00 00
                                              $0x64,%esi
                                       mov
4009d6:
              48 89 c7
                                       mov
                                              %rax,%rdi
4009d9:
              e8 12 fd ff ff
                                       calla
                                              4006f0 <fgets@plt>
4009de:
              48 8d 45 90
                                       lea
                                               -0x70(%rbp),%rax
                                              $0x400bf5,%esi
4009e2:
              be f5 0b 40 00
                                       mov
              48 89 c7
4009e7:
                                       mov
                                              %rax,%rdi
              e8 e1 fc ff ff
                                              4006d0 <strcspn@plt>
4009ea:
                                       calla
4009ef:
              c6 44 05 90 00
                                       movb
                                              $0x0,-0x70(%rbp,%rax,1)
4009f4:
              b8 e8 0c 40 00
                                       mov
                                               $0x400ce8,%eax
4009f9:
              48 89 c7
                                              %rax,%rdi
                                       mov
4009fc:
              b8 00 00 00 00
                                              $0x0,%eax
                                       mov
              e8 ba fc ff ff
400a01:
                                       calla
                                              4006c0 <printf@plt>
400a06:
              48 8b 05 3b 08 20 00
                                              0x20083b(%rip),%rax
                                                                          # 601248 <__bss_start>
                                       mov
400a0d:
              48 89 c2
                                              %rax,%rdx
                                              -0x110(%rbp),%rax
400a10:
              48 8d 85 f0 fe ff ff
                                       1.ea
              be 64 00 00 00
                                              $0x64,%esi
400a17:
                                       mov
              48 89 c7
400a1c:
                                       mov
                                              %rax,%rdi
400a1f:
              e8 cc fc ff ff
                                       calla
                                              4006f0 <fgets@plt>
400a24:
              48 8d 85 f0 fe ff ff
                                       lea
                                               -0x110(%rbp),%rax
                                              $0x400bf5,%esi
400a2b:
              be f5 0b 40 00
                                       mov
400a30:
              48 89 c7
                                       mov
                                              %rax,%rdi
400a33:
              e8 98 fc ff ff
                                       callq
                                              4006d0 <strcspn@plt>
              c6 84 05 f0 fe ff ff
400a38:
                                       mo∨b
                                              $0x0,-0x110(%rbp,%rax,1)
400a3f:
400a40:
              bf f8 0c 40 00
                                              $0x400cf8,%edi
                                       mov
400a45:
              e8 36 fc ff ff
                                       callq 400680 <puts@plt>
              48 8d 8d f0 fe ff ff
                                              -0x110(%rbp),%rcx
400a4a:
                                       lea
400a51:
              48 8d 85 60 ff ff ff
                                       lea
                                               -0xa0(%rbp),%rax
400a58:
                                              $0x29,%edx
              ba 29 00 00 00
                                       mov
400a5d:
              48 89 ce
                                              %rcx,%rsi
                                       mov
400a60:
              48 89 c7
                                              %rax,%rdi
                                       mov
              e8 08 fc ff ff
400a63:
                                       calla
                                              400670 <strncmp@plt>
400a68:
              85 c0
                                       test
                                              %eax,%eax
400a6a:
              75 2a
                                       jne
                                              400a96 <main+0x282>
400a6c:
              b8 22 0d 40 00
                                              $0x400d22,%eax
                                       mov
400a71:
              48 8d 55 90
                                       lea
                                               -0x70(%rbp),%rdx
              48 89 d6
400a75:
                                       mov
                                              %rdx,%rsi
400a78:
              48 89 c7
                                              %rax,%rdi
                                       mov
400a7b:
              b8 00 00 00 00
                                               $0x0,%eax
                                              4006c0 <printf@plt>
400a80:
              e8 3b fc ff ff
                                       calla
400a85:
              bf 32 0d 40 00
                                              $0x400d32,%edi
                                       mov
400a8a:
              e8 21 fc ff ff
                                       calla
                                              4006b0 <system@plt>
400a8f:
              b8 00 00 00 00
                                              $0x0,%eax
                                       mov
400a94:
              c9
                                       leaveq
400a95:
              c3
                                       reta
400a96:
              48 8d 45 90
                                       lea
                                               -0x70(%rbp),%rax
400a9a:
              48 89 c7
                                              %rax,%rdi
                                       mov
              b8 00 00 00 00
400a9d:
                                       mov
                                              $0x0,%eax
400aa2:
              e8 19 fc ff ff
                                       callq
                                              4006c0 <printf@plt>
400aa7:
              bf 3a 0d 40 00
                                       mov
                                               $0x400d3a,%edi
              e8 cf fb ff ff
400aac:
                                       calla
                                              400680 <puts@plt>
400ab1:
              bf 01 00 00 00
                                               $0x1,%edi
```

From the source file I created from reversing, I can observe that fgets() reads a number of bytes that do not overflow. And so we open the .pass file of level03, and we compare it

callq

nop

nop

nop

nop

400710 <exit@plt>

400ab6:

400abb:

400abc:

400abd:

400abe:

400abf:

e8 55 fc ff ff

90

90

90

90

90

with the password we wrote on stdin (after the username). The comparaison is done on maximum 0x29 octet (size of what's read from the .pass file). But strncmp() stop comparing after it reaches a '\0'

But anyway if we wanted to fill our password input with '\0', strncmp() would still return 1, because the it compares the first byte of the .pass file with '\0'...

Maybe I can overwrite the address of exit in the .got.plt section, but I don't see how, or trigger the strncmp() with the password input...

I would like to overwrite the emplacement of the .pass file by the password input, they are contiguous but s2(password input) is 0x70 large before s1(.pass file) and we stored only 0x64 from the fgets()..

Ok, after analyzing the source file, and understanding better strcspn(), I see that if the character $\ll n$ » is not present ... We can not trigger strcspn() that returns at most the index of the first '\0', which is added by fgets after 0x64 octet from stdin...

Because we call printf(<username>) before exit, maybe we can write at an address using the printf exploit.

Because printf is called after the strncmp(), we can try overwrite the call to exit in the .got.plt by the call to the address of where system is called.

For that we need to find the destination address somewhere in the stack, which can be in our printf arguments, itself stored on the stack.

so if are argument is stored at s0.

In x64 calling convention, arguments are stored in 4 registers, then on the stack, so first arg is our chain, in 1st reg, then second '%' is on 2nd register,

third '%' is in 3rd reg, fourth '%' in 4th reg, then fifth, sixth, seventh respectively pop stack 1, 2, 3, 4.

From the code we know that our username input is stored 16 bytes or 2 elem from the stack pointer of printf, which will of

course get his ebp and eip pushed on his stack, so we will need to pop 4times.

the target address in .got.plt: 0x601228 the value of where we want to jump instead of exit addr: 0x400a85

So we must write 0x85 at 0x601228 then 0x400a at 0x601229

the stack is at 0x7fffffffe480 when printf is called, and our chain is stored in 0x7fffffffe530, 0xb0 byte further, but 5 arguments are used before poping form the stack. The thing is it never pops my arg...

But actually printf may print the content of the stack where is

stored the .pass file:

5* '%p%p' for the 5 registers, 16 * '%p%p' from 0x7fffffffe480 to 0x7fffffffe500 then 6 * '%p%' for the 41 chars extend to 48:

0x756e505234376848 unPR47hH 0x45414a3561733951 EAJ5as9Q 0x377a7143574e6758 7zqCWNgX 0x354a35686e475873 5J5hnGXs 0x48336750664b394d H3gPfK9M

Flag: Hh74RPnuQ9sa5JAEXgNWCqz7sXGnh5J5M9KfPg3H