TERASIC CYCLONE II EP2C35 Development & Education BOARD

SCHEMATIC	CONTENT	PAGE
TOP	COVER PAGE , TOP	01 ~ 03
AUDIO	WM8731	04 ~ 04
DISPLAY	LCD , LED , 7SEGMENT	05 ~ 06
EP2C35	EP2C35 BANK1..BANK8 , POWER , CONFIG	07 ~ 11
ETHERNET	DM9000A	12 ~ 12
IN/OUT	CLOCK , PS2 , RS232 , KEY , SWITCH , CONNECT	13 ~ 17
MEMORY	SRAM , DRAM , FLASH , SD CARD	18 ~ 19
POWER	POWER	20 ~ 20
USB BLASTER	USB BLASTER	21 ~ 21
USB DEVICE	USB DEVICE	22 ~ 22
VIDEO	ADV7181 , ADV7123	23 ~ 24

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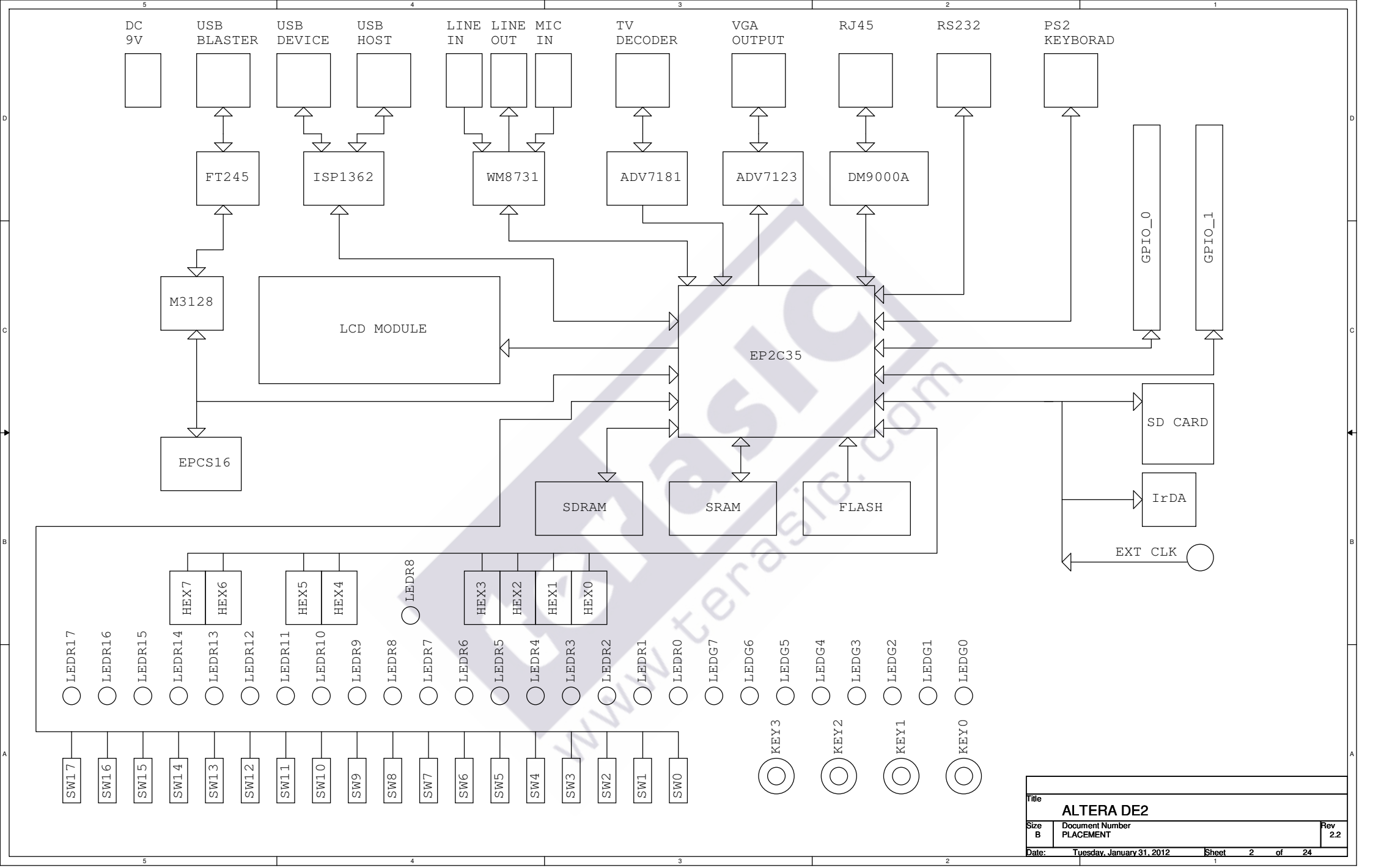
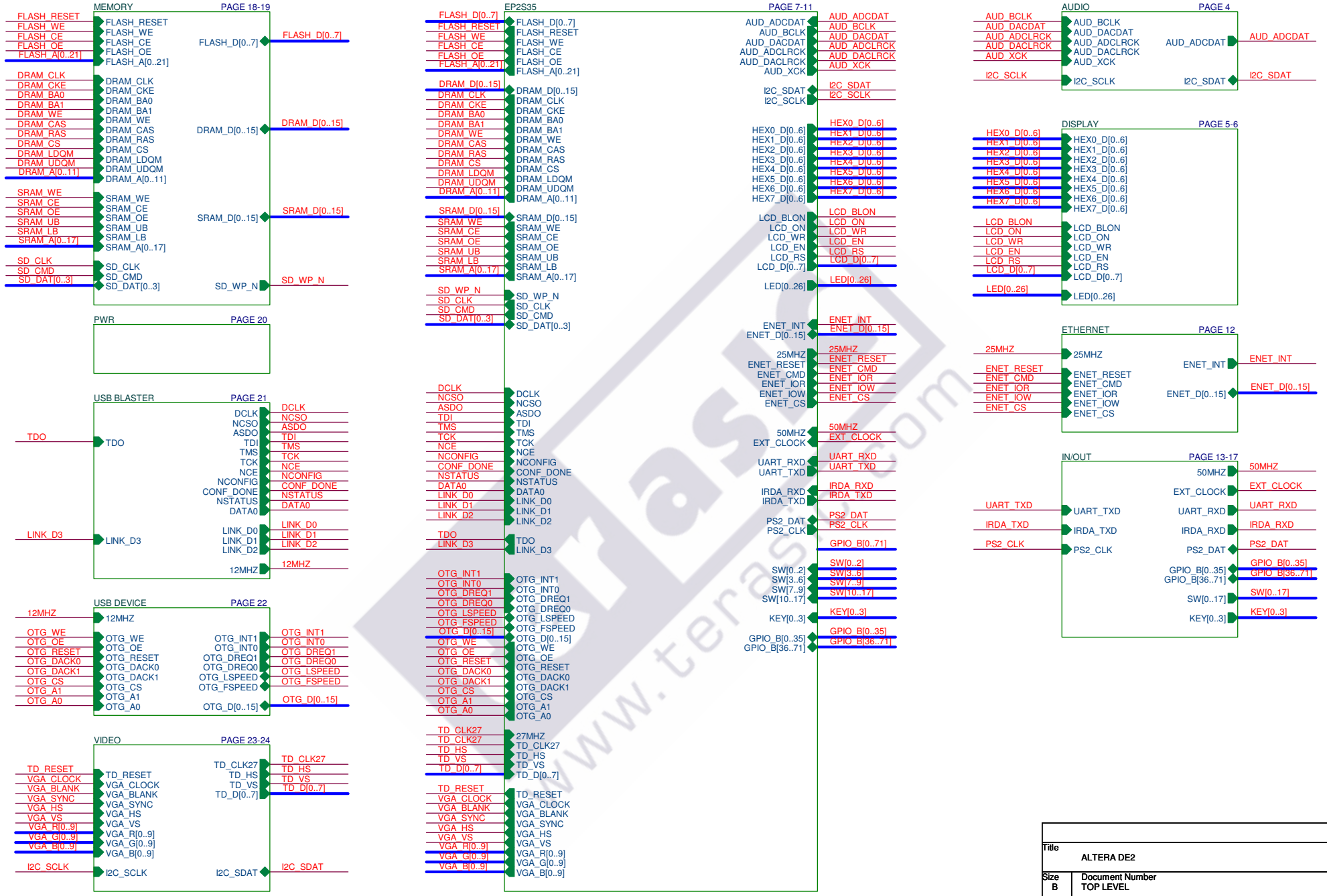
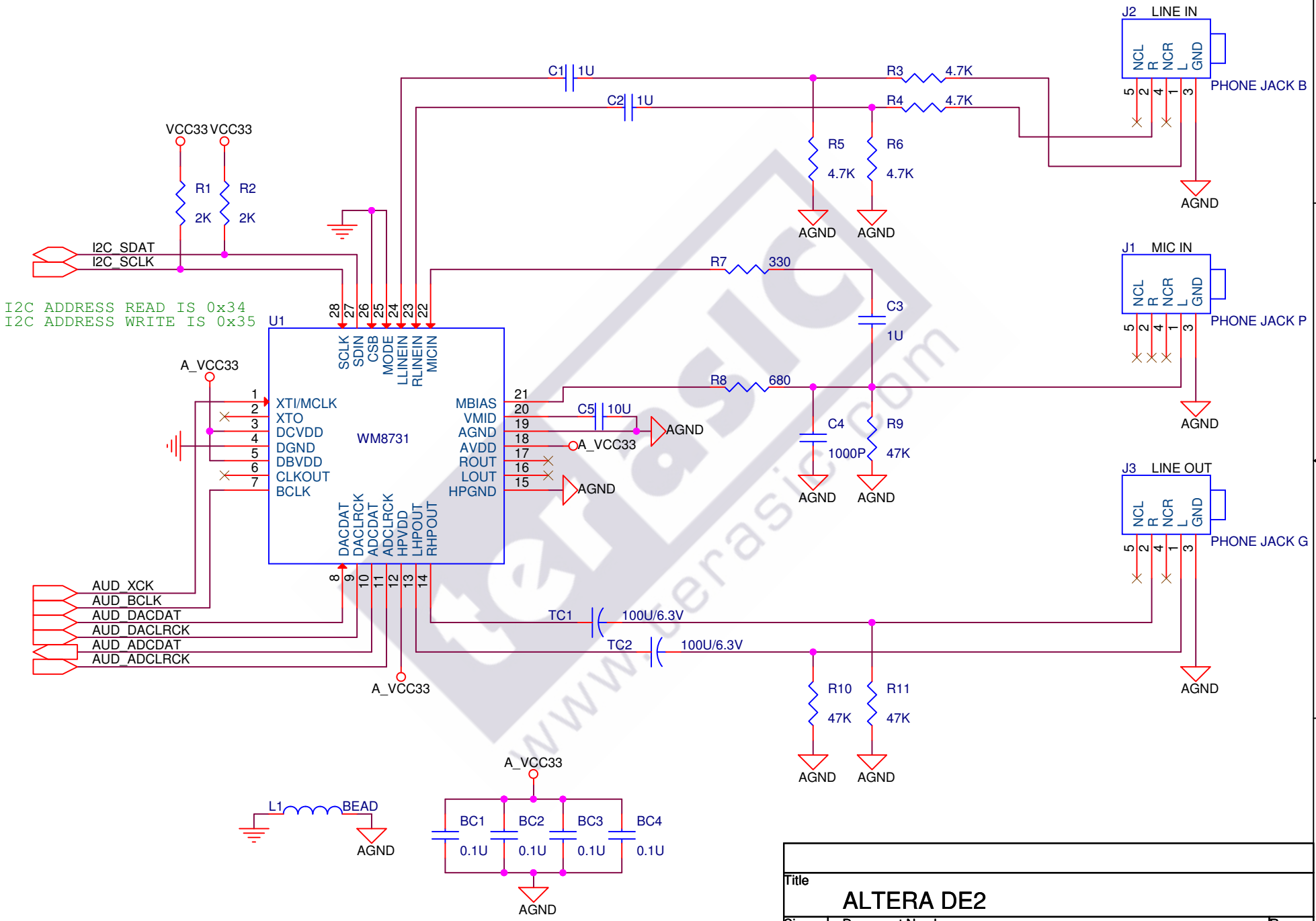
Title: ALTERA DE2		
Size: B	Document Number: COVER PAGE	Rev: 2.2
Date: Tuesday, January 31, 2012	Sheet 1 of 24	



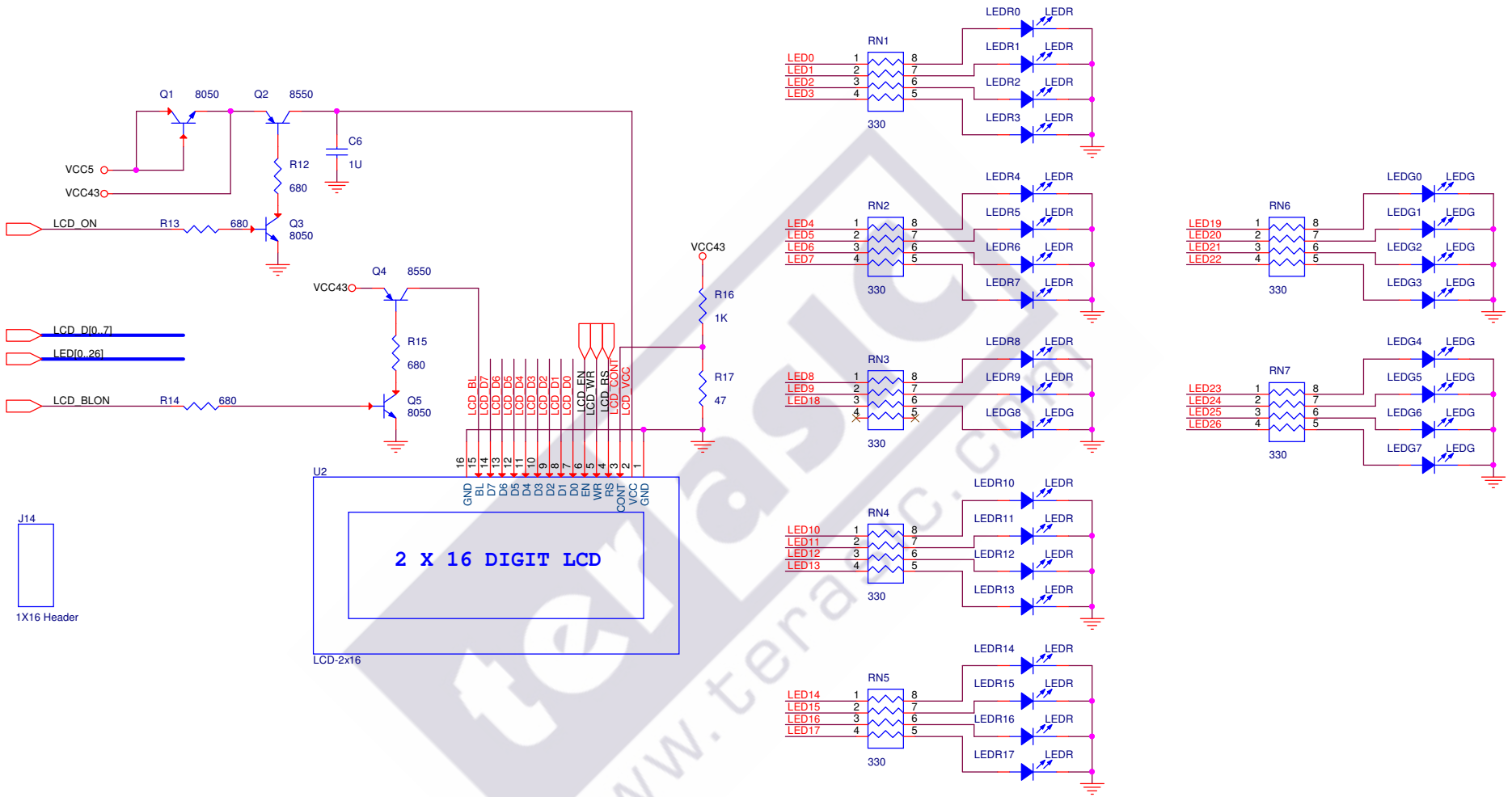
Title	ALTERA DE2	
Size B	Document Number PLACEMENT	Rev 2.2
Date: Tuesday, January 31, 2012	Sheet 2 of 24	



Title	ALTERA DE2	
Size B	Document Number TOP LEVEL	Rev 2.2
Date: Tuesday, January 31, 2012	Sheet 3 of 24	



Title	ALTERA DE2	
Size: A	Document Number: AUDIO	Rev: 2.2
Date: Tuesday, January 31, 2012	Sheet 4 of 24	



Title	ALTERA DE2	
Size B	Document Number LCD AND LED	Rev 2.2
Date: Tuesday, January 31, 2012	Sheet 5 of 24	

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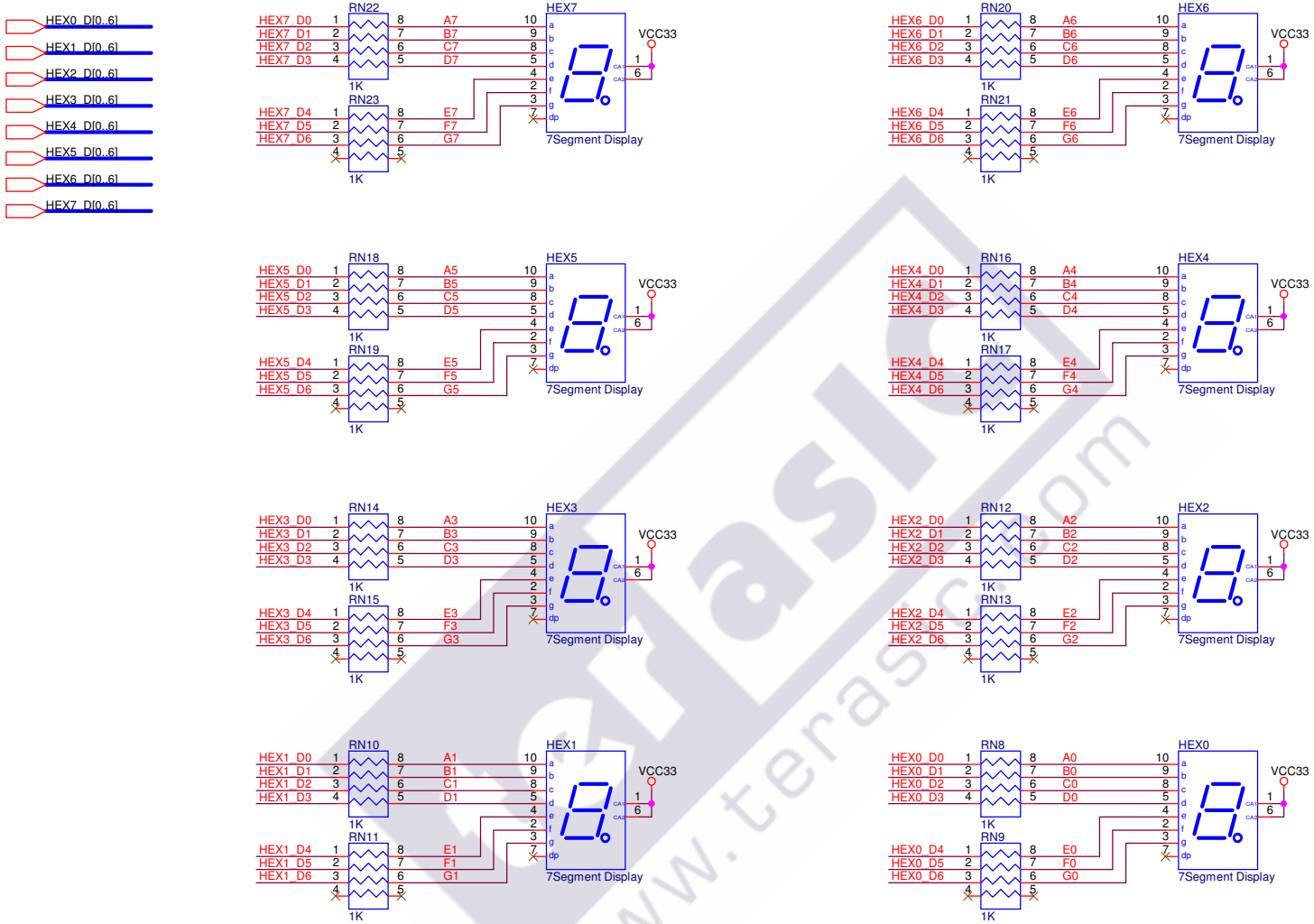

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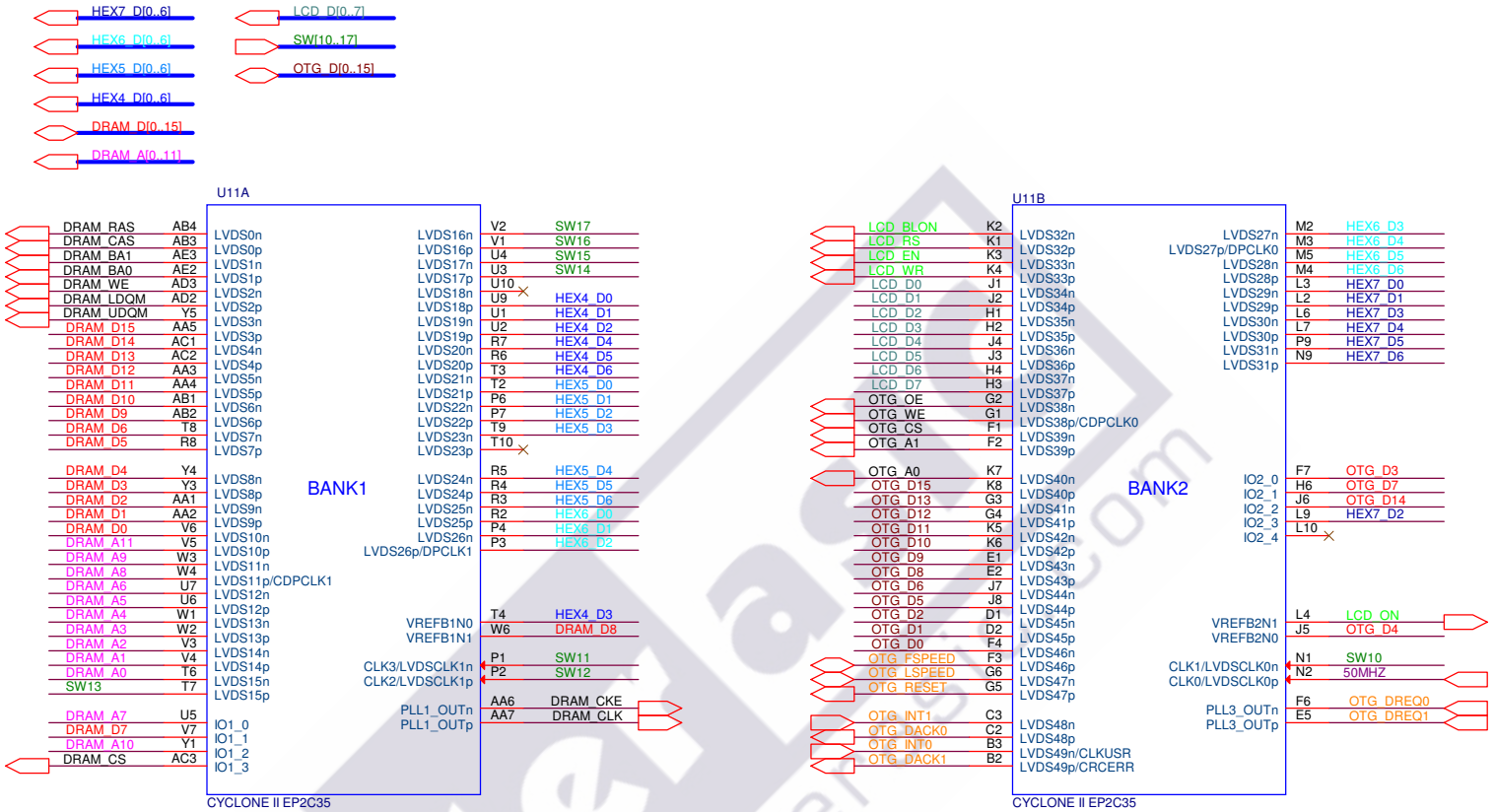
Title	ALTERA DE2	
Size: B	Document Number: 7 SEGMENT	Rev: 2.2
Date: Tuesday, January 31, 2012	Sheet 6 of 24	



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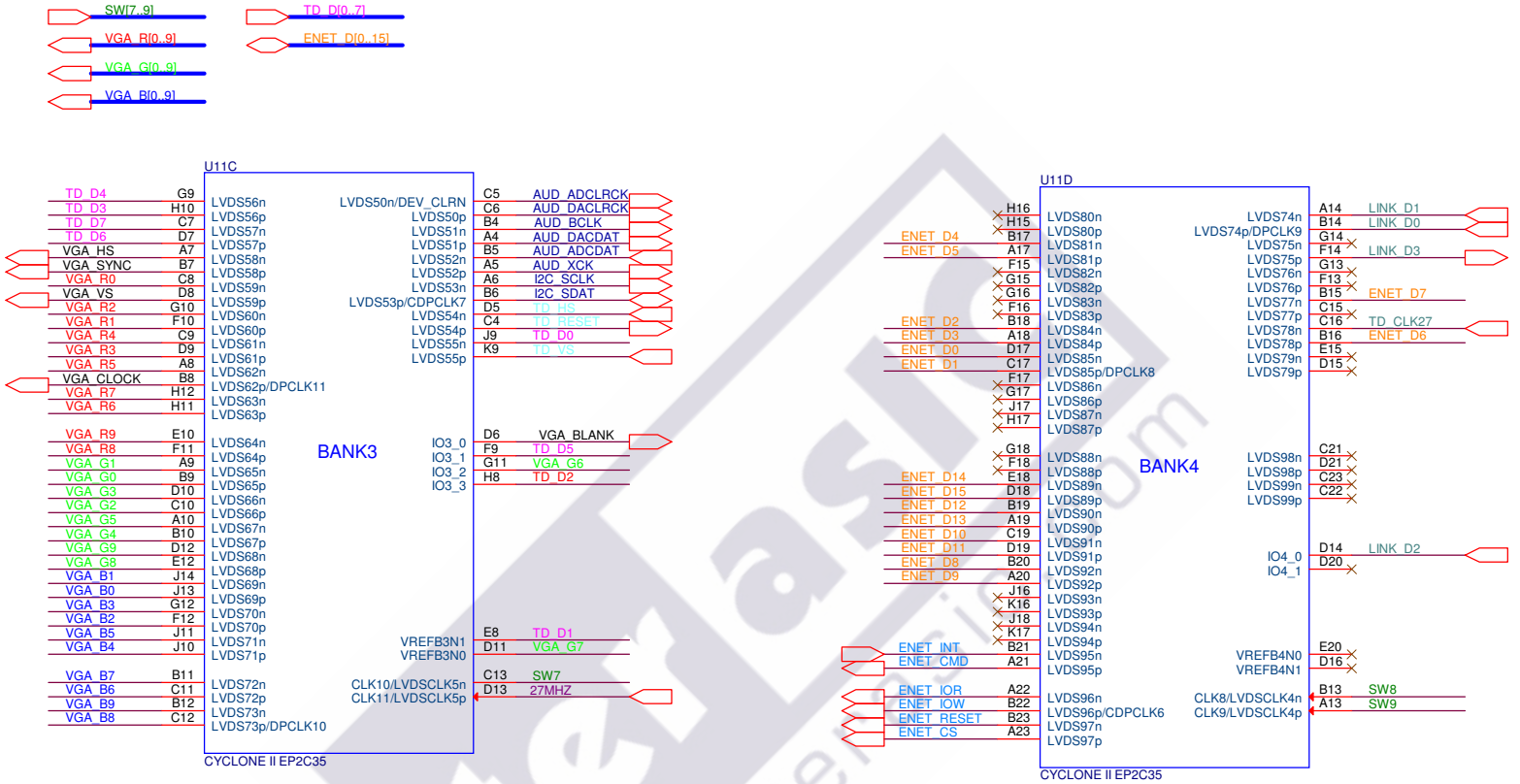
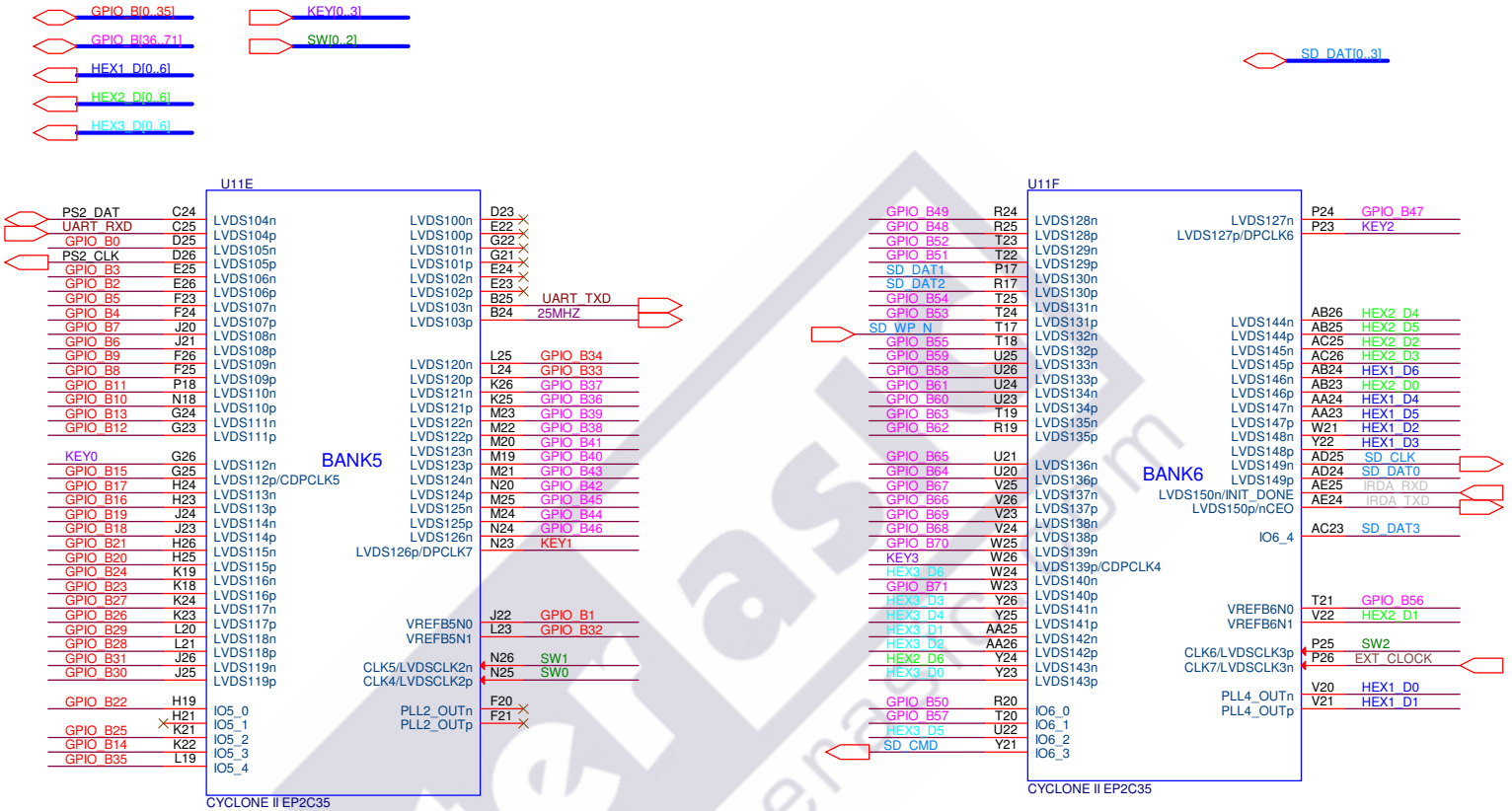
Title	ALTERA DE2	
Size B	Document Number EP2C35 BANK1 AND BANK 2	Rev 2.2
Date: Tuesday, January 31, 2012	Sheet 7 of 24	



Title	ALTERA DE2
Size	B
Document Number	EP2C35 BANK3 AND BANK 4
Rev	2.2
Date:	Tuesday, January 31, 2012
Sheet	8 of 24



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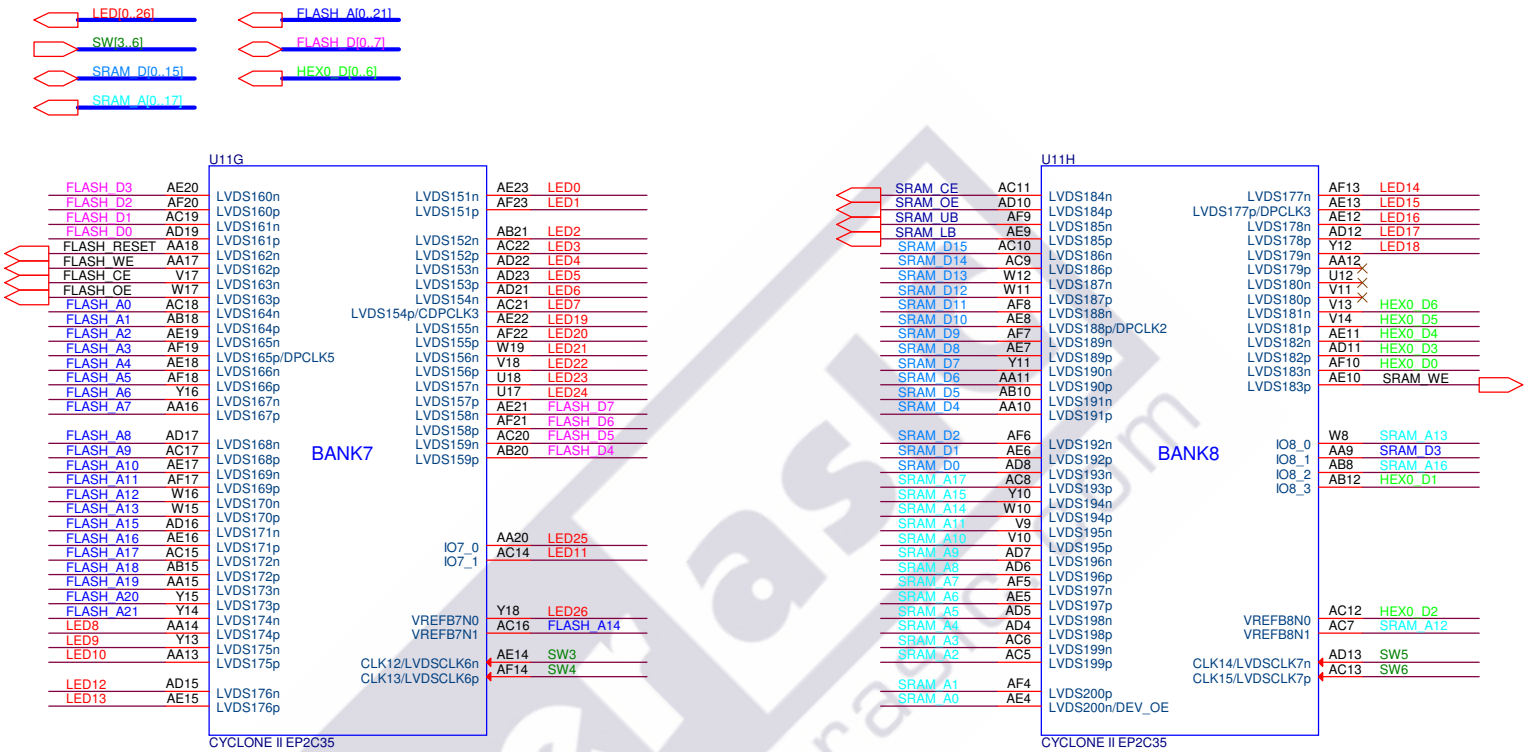
Title	ALTERA DE2	
Size B	Document Number EP2C35 BANK5 AND BANK 6	Rev 2.2
Date: Tuesday, January 31, 2012	Sheet 9 of 24	



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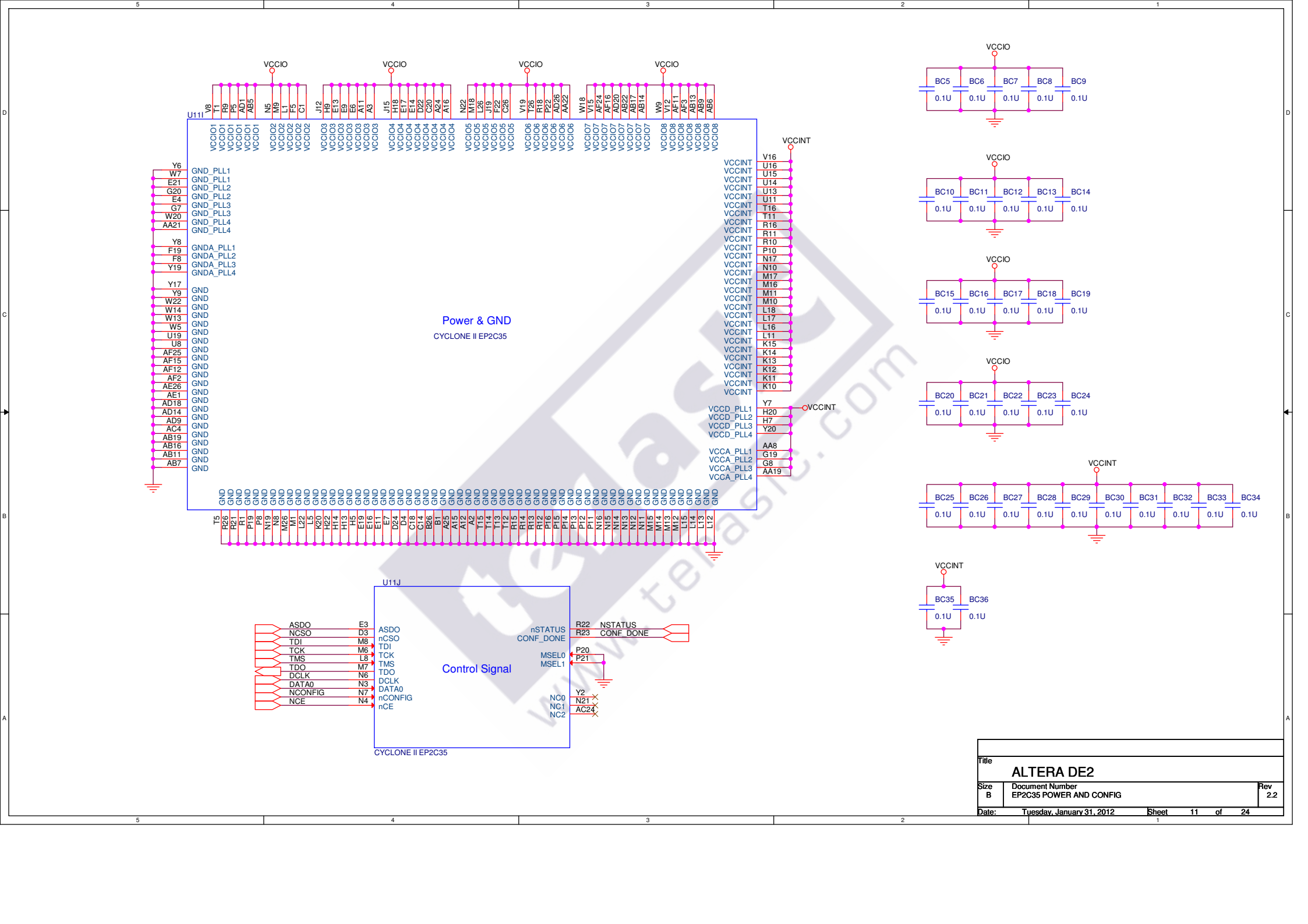
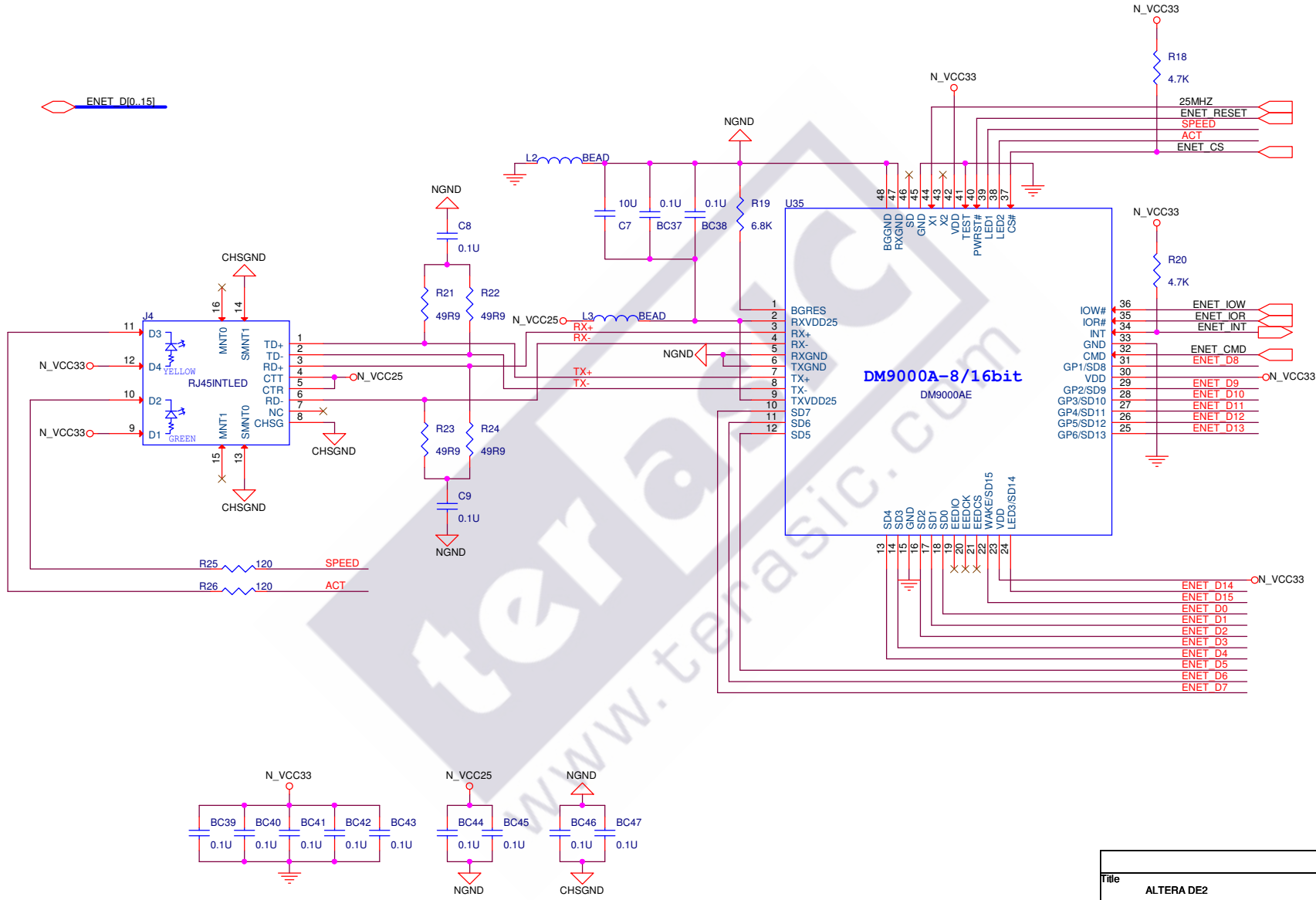
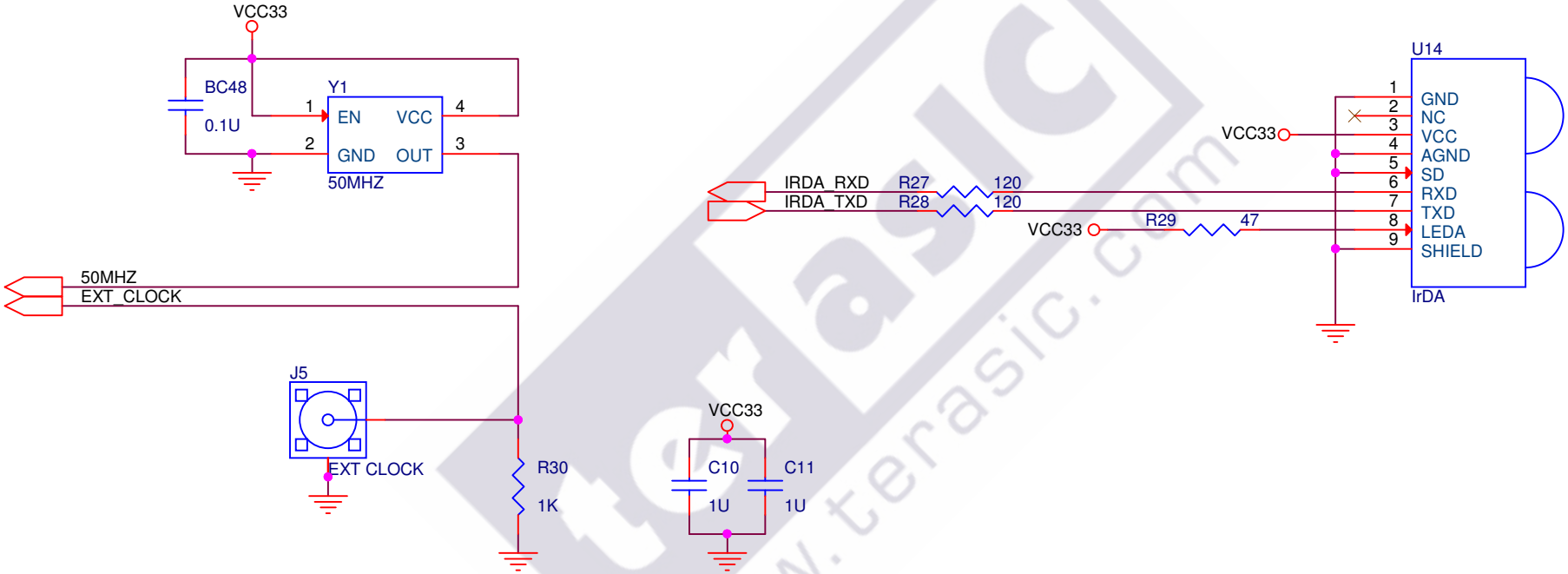
Title	ALTERA DE2	
Size B	Document Number EP2C35 BANK7 AND BANK 8	Rev 2.2
Date: Tuesday, January 31, 2012	Sheet 10 of 24	


U11I
Power & GND
CYCLONE II EP2C35
VCCIO
VCCINT
VCCD_PLL1
VCCD_PLL2
VCCD_PLL3
VCCD_PLL4
VCCA_PLL1
VCCA_PLL2
VCCA_PLL3
VCCA_PLL4
GND_PLL1
GND_PLL2
GND_PLL3
GND_PLL4
GNDA_PLL1
GNDA_PLL2
GNDA_PLL3
GNDA_PLL4
GND
U11J
Control Signal
CYCLONE II EP2C35
ASDO
NCSO
TDI
TCK
TMS
TDO
DCLK
DATA0
NCONFIG
NCE
nSTATUS
CONF_DONE
MSEL0
MSEL1
NC0
NC1
NC2
NSTATUS
BC5
BC6
BC7
BC8
BC9
BC10
BC11
BC12
BC13
BC14
BC15
BC16
BC17
BC18
BC19
BC20
BC21
BC22
BC23
BC24
BC25
BC26
BC27
BC28
BC29
BC30
BC31
BC32
BC33
BC34
BC35
BC36
0.1U
Title
ALTERA DE2
Size
B
Document Number
EP2C35 POWER AND CONFIG
Rev
2.2
Date:
Tuesday, January 31, 2012
Sheet
11
of
24



Title	ALTERA DE2	
Size B	Document Number DM9000AE	Rev 2.2
Date: Tuesday, January 31, 2012	Sheet 12 of 24	



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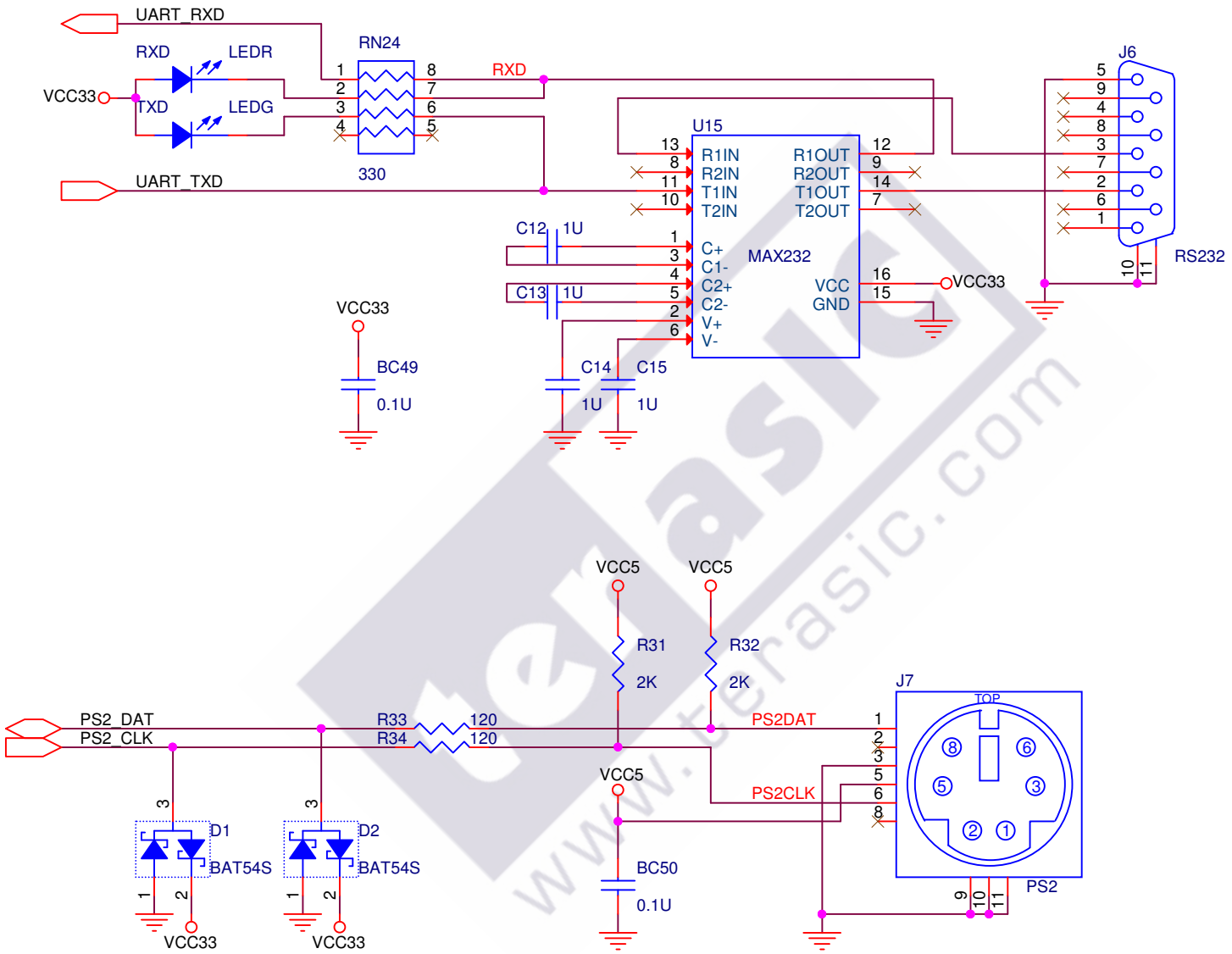
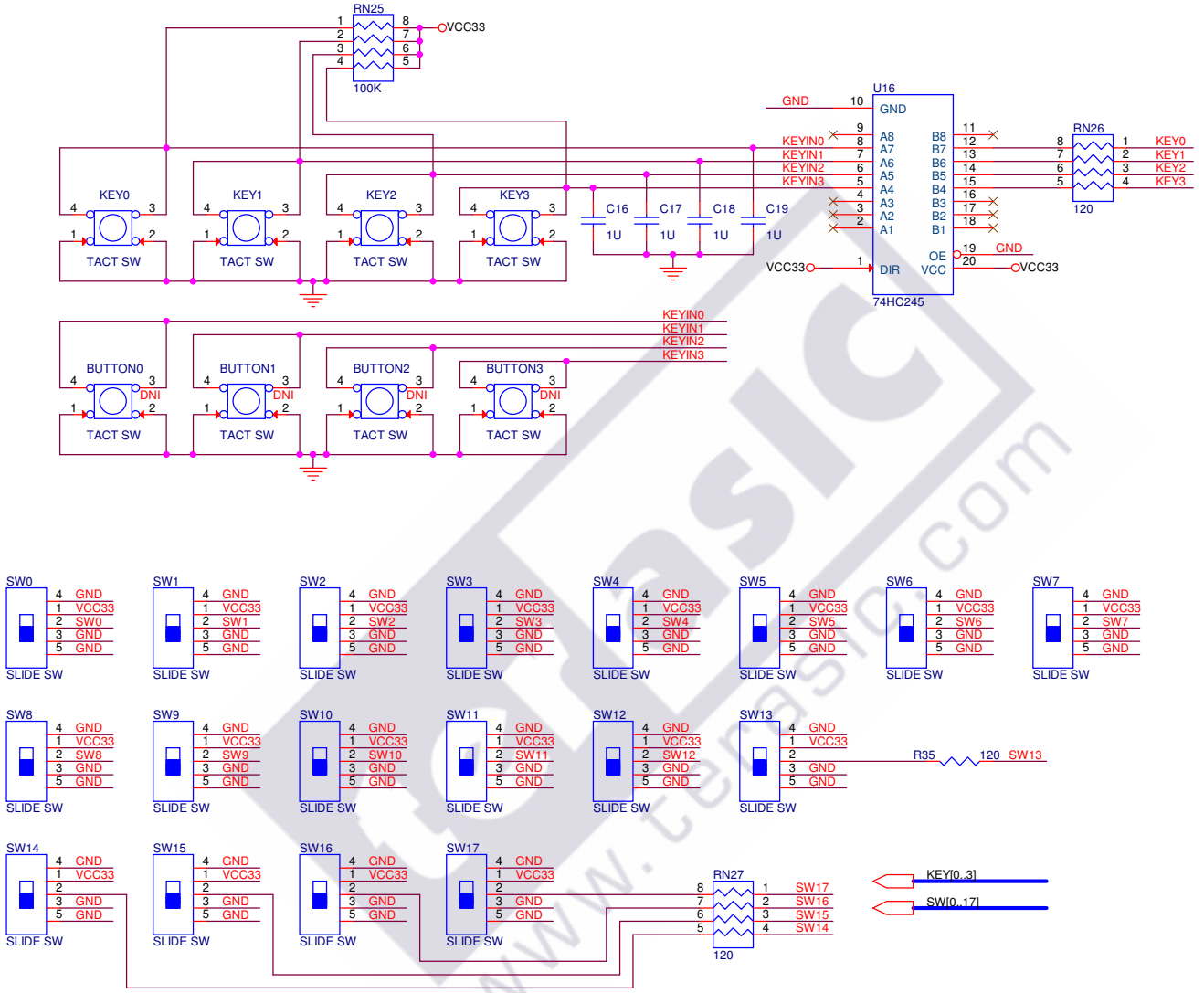
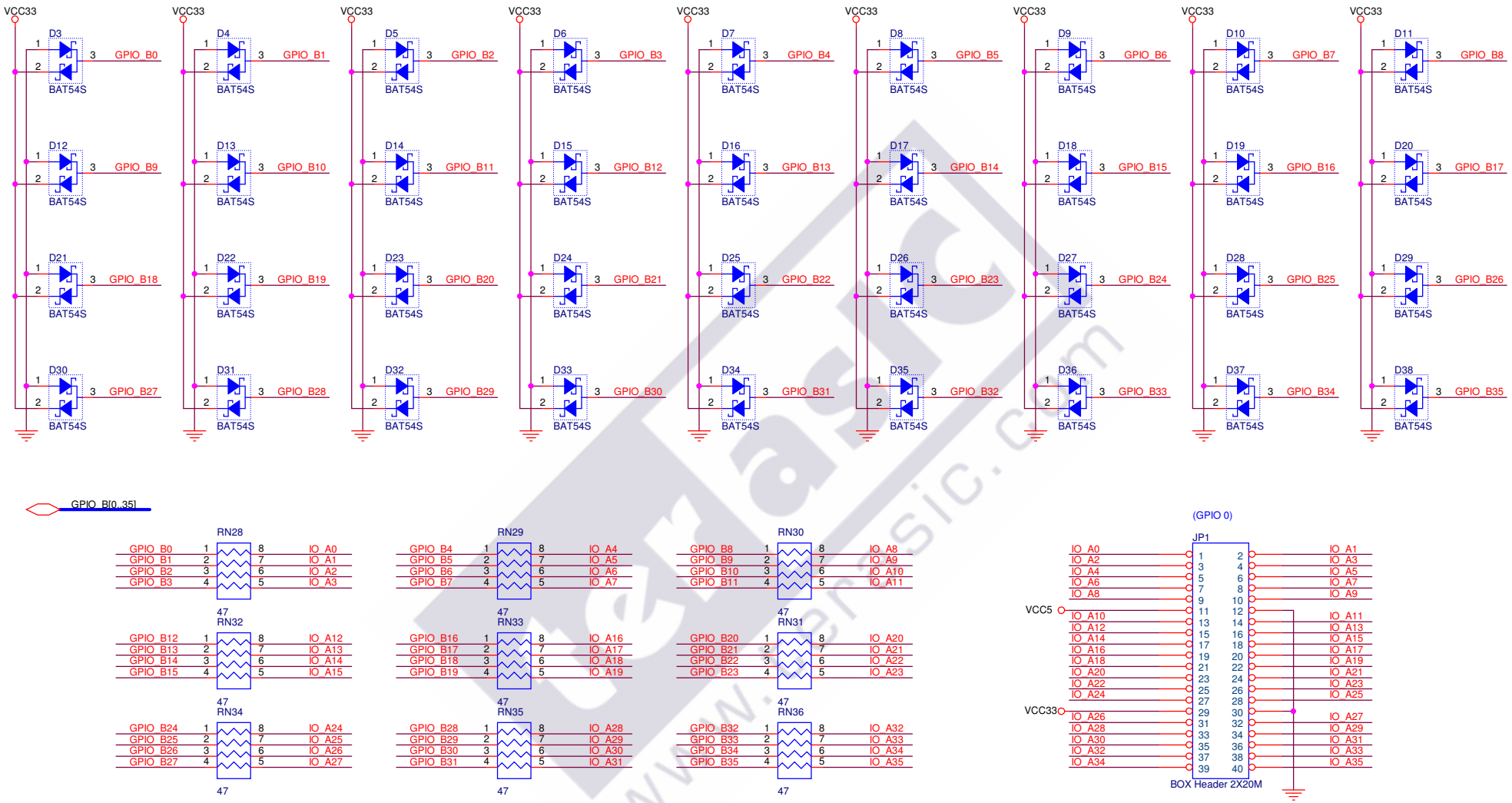
Title	ALTERA DE2	
Size: A	Document Number: CLOCK and IrDA	Rev: 2.2
Date: Tuesday, January 31, 2012	Sheet 13 of 24	



Title	ALTERA DE2	
Size: A	Document Number: PS2 AND RS232	Rev: 2.2
Date: Tuesday, January 31, 2012	Sheet 14 of 24	



Title	ALTERA DE2	
Size B	Document Number KEY AND SWITCH	Rev 2.2
Date: Tuesday, January 31, 2012	Sheet 15 of 24	



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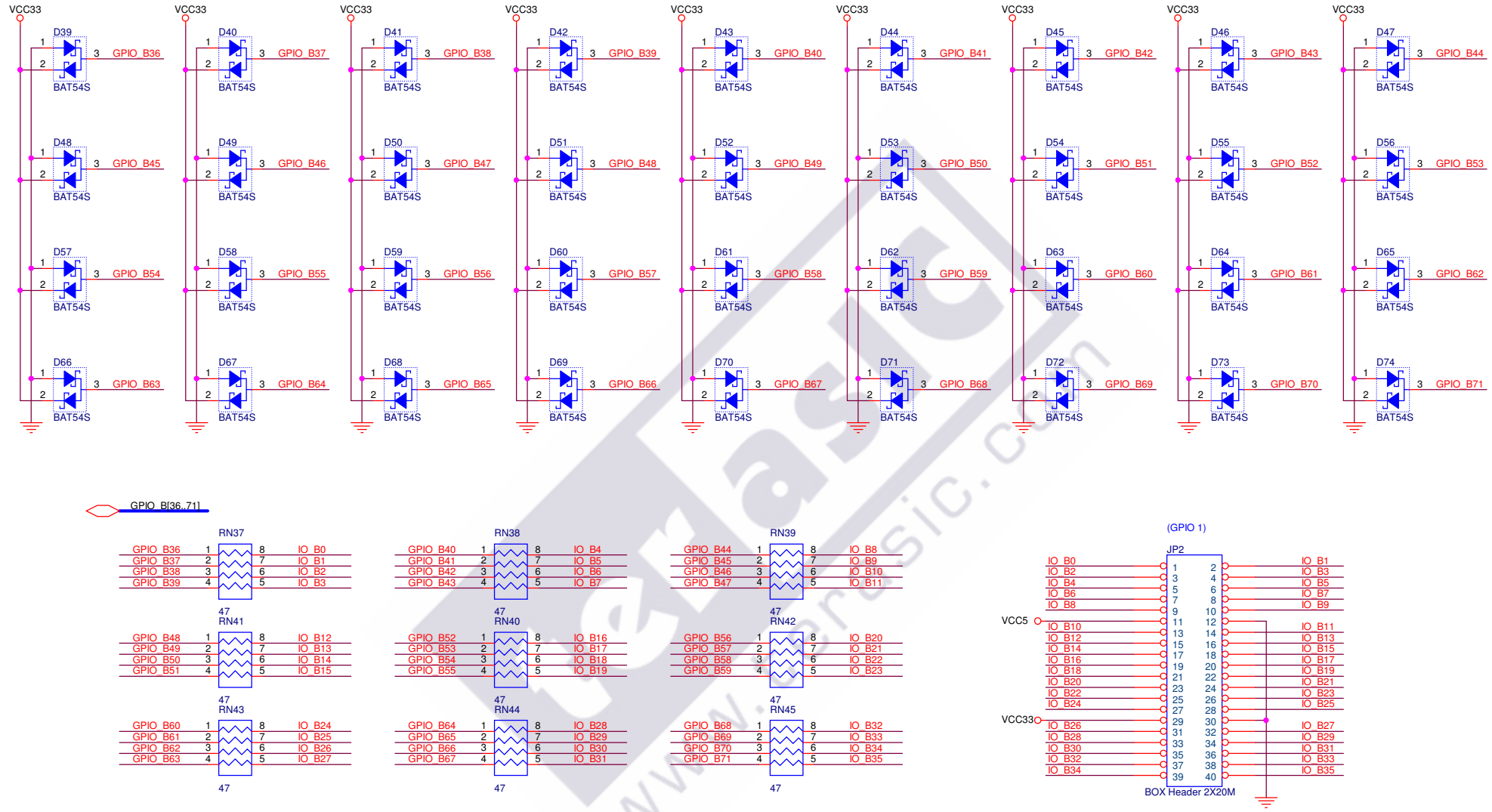
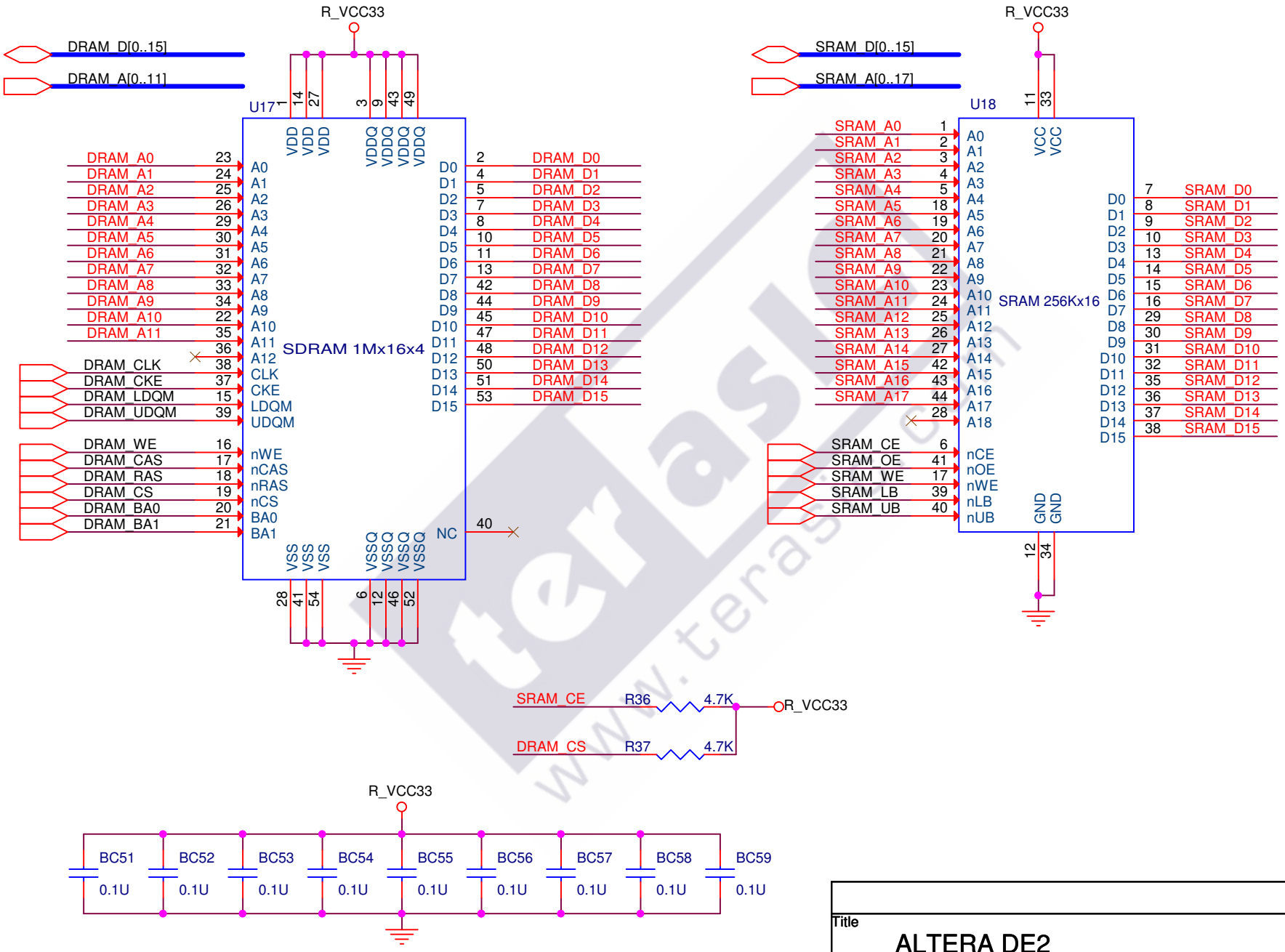
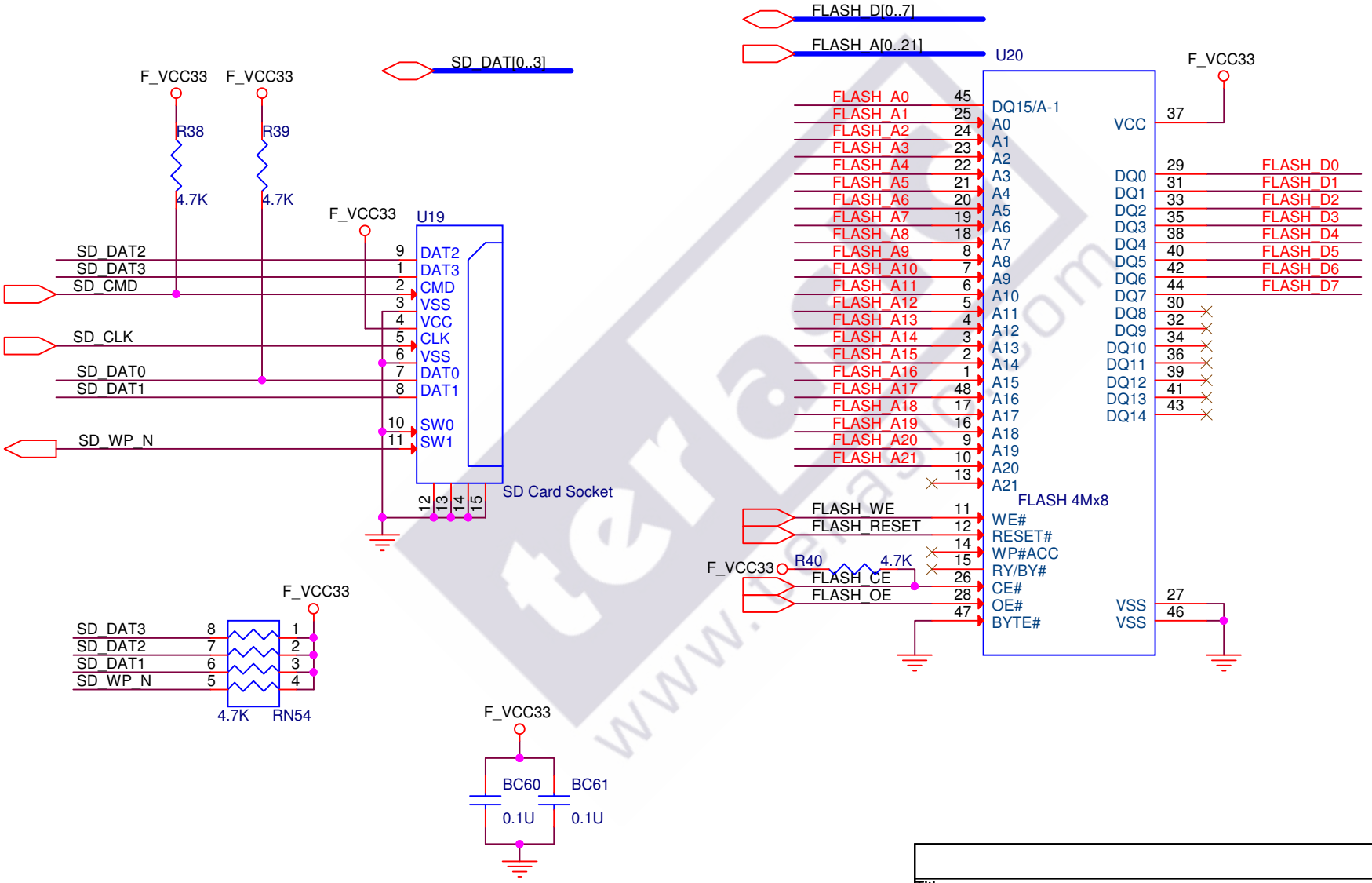
Title	ALTERA DE2	
Size B	Document Number CONNECT A	Rev 2.2
Date: Tuesday, January 31, 2012	Sheet 16 of 24	



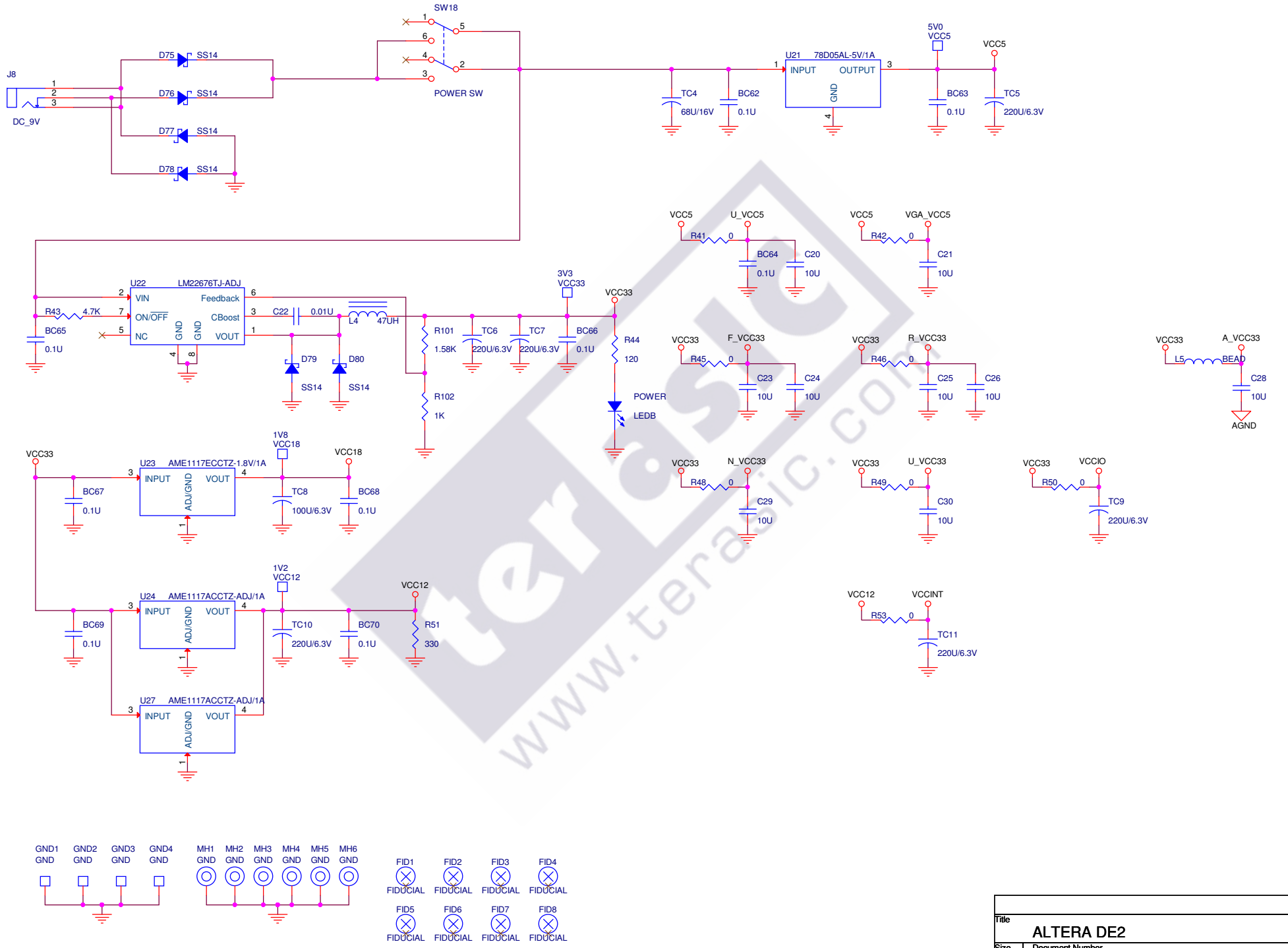
Title	ALTERA DE2	
Size B	Document Number CONNECT B	Rev 2.2
Date: Tuesday, January 31, 2012	Sheet 17 of 24	



Title	ALTERA DE2	
Size: A	Document Number: SDRAM and SRAM	Rev: 2.2
Date: Tuesday, January 31, 2012	Sheet 18 of 24	



Title	ALTERA DE2	
Size: A	Document Number: FLASH and SD Card	Rev: 2.2
Date: Wednesday, February 01, 2012	Sheet 19 of 24	



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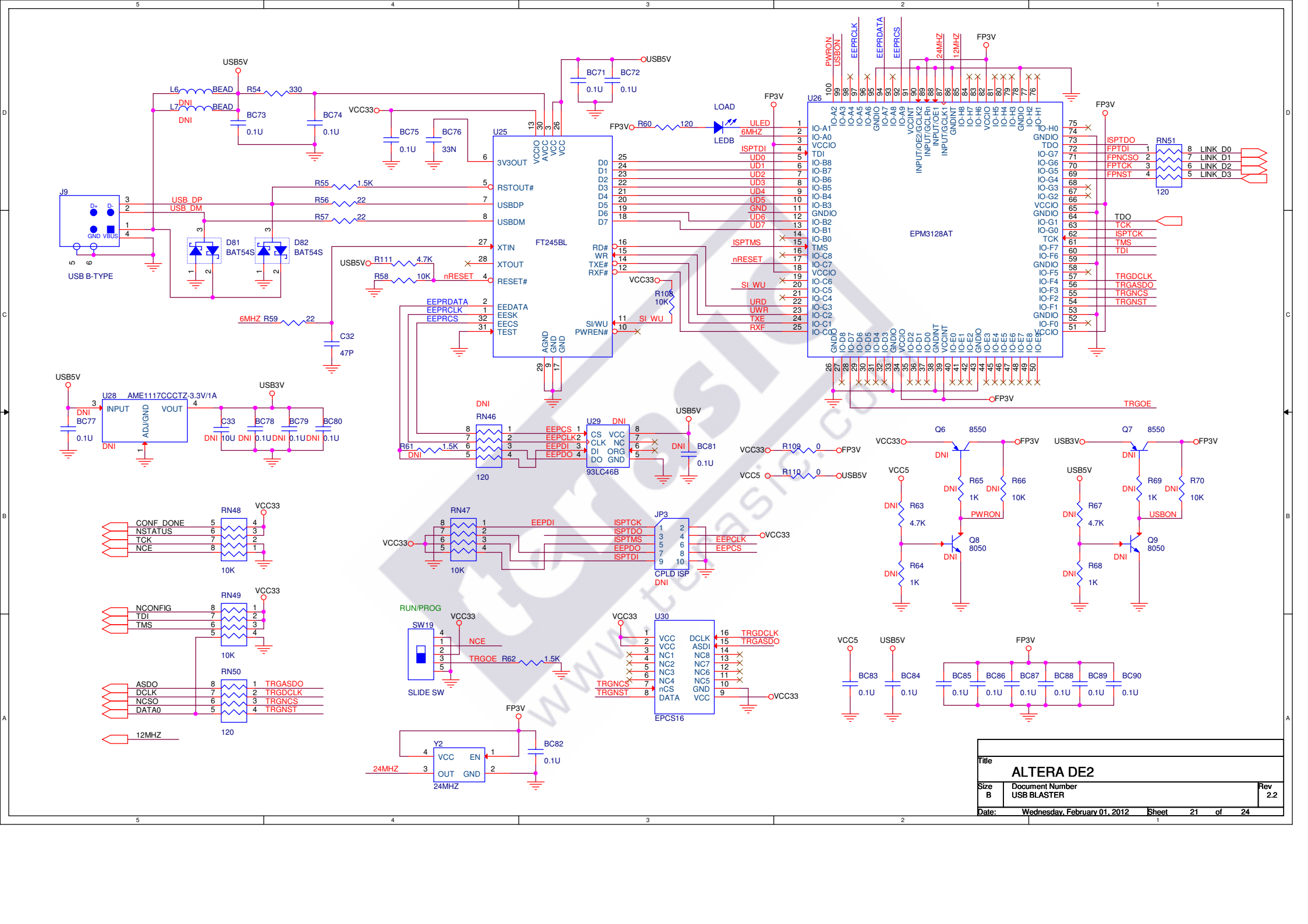
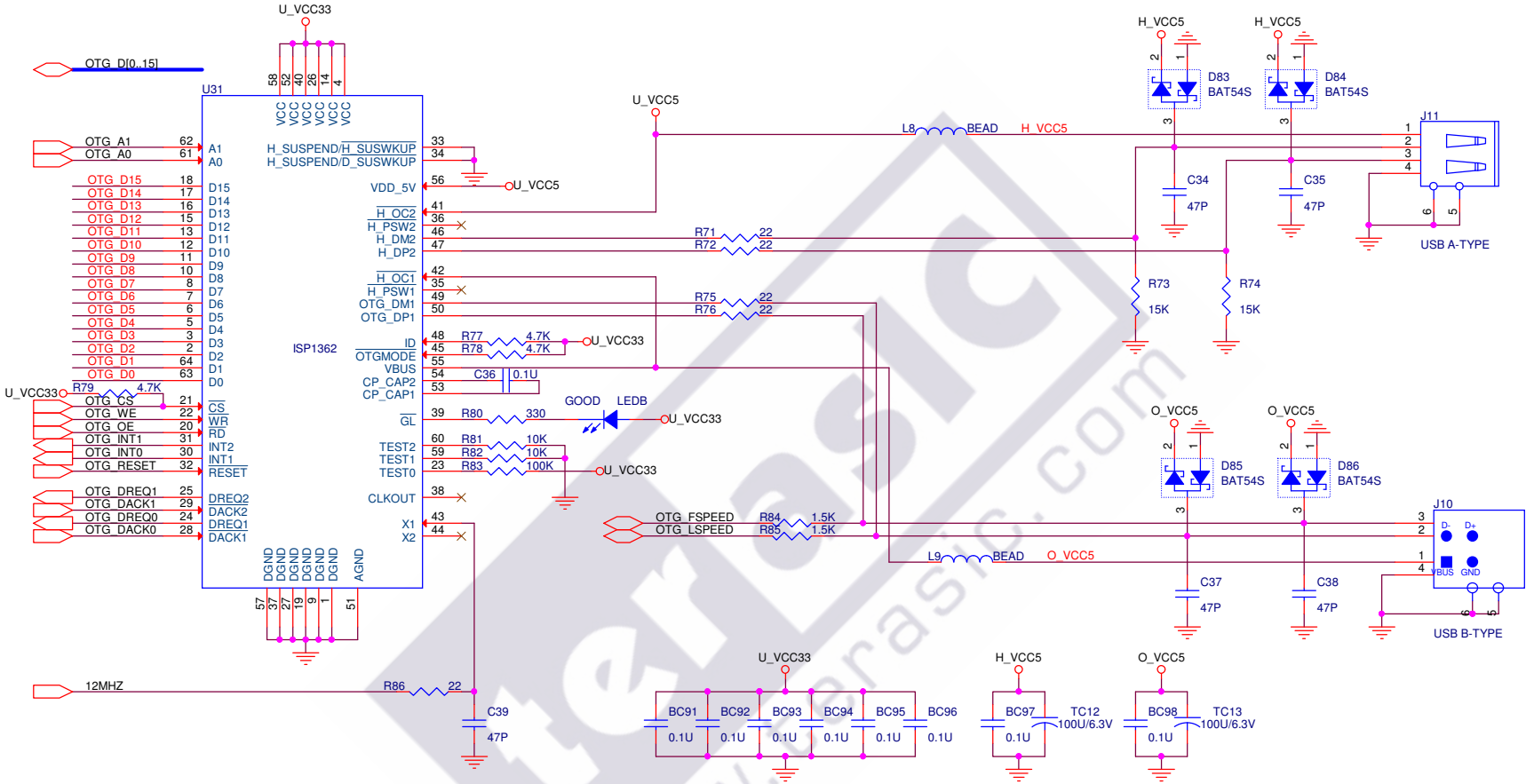
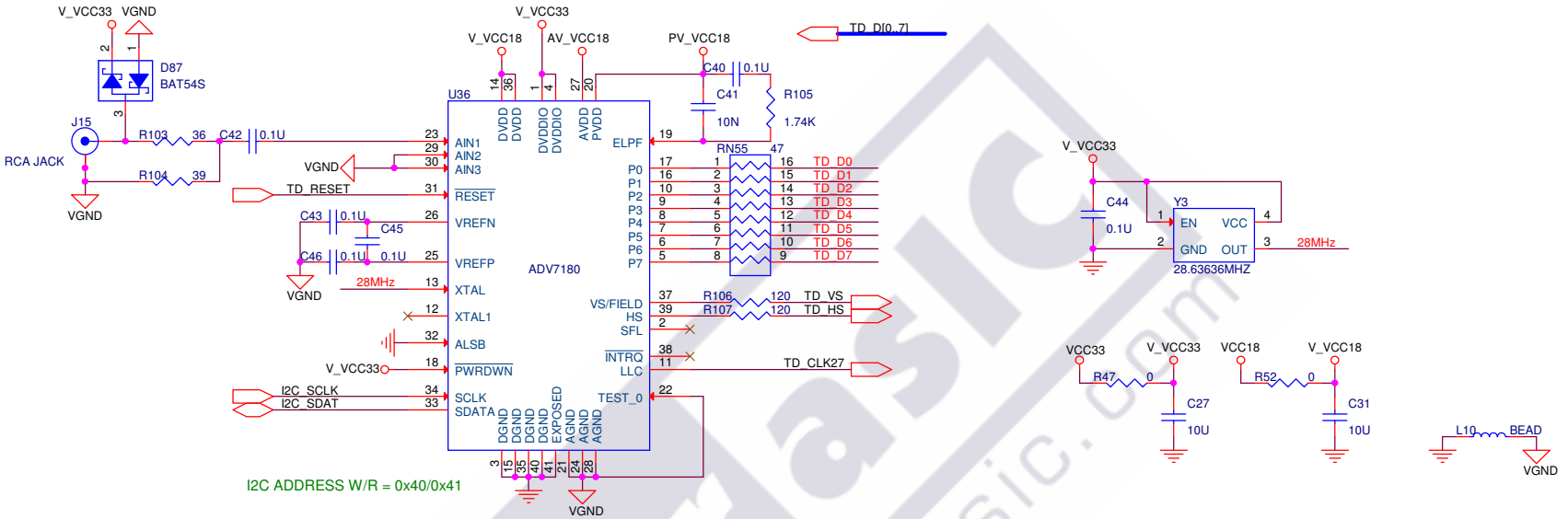
Title	ALTERA DE2	
Size	Document Number: POWER	Rev: 2.2
Date: Wednesday, February 01, 2012	Sheet 20 of 24	


USB5V
L6 BEAD
L7 DNI BEAD
DNI
R54 330
BC73 0.1U
BC74 0.1U
VCC33
BC75 0.1U
BC76 33N
BC71 BC72 0.1U 0.1U
USB5V
J9
USB B-TYPE
USB_DP
USB_DM
D81 BAT54S
D82 BAT54S
R55 1.5K
R56 22
R57 22
6MHZ R59 22
C32 47P
USB5V R111 4.7K
R58 10K nRESET
U25
3V3OUT
RSTOUT#
USBDP
USBDM
XTIN
XTOUT
RESET#
FT245BL
EEPRDATA
EEPRCLK
EEPRCS
EEDATA
EESK
EECS
TEST
AGND GND GND
VCCIO AVCC VCC VCC
D0 D1 D2 D3 D4 D5 D6 D7
RD# WR TXE# RXF#
SI/WU PWREN#
SI_WU
R108 10K
VCC33
FP3V R60 120
LOAD
LEDB
ULED 6MHZ ISPTDI UD0 UD1 UD2 UD3 UD4 UD5 GND UD6 UD7
ISPTMS nRESET SI_WU URD UWR TXE RXF
U26
EPM3128AT
IO-A1 IO-A0 VCCIO TDI IO-B8 IO-B7 IO-B6 IO-B5 IO-B4 IO-B3 GNDIO IO-B2 IO-B1 IO-B0 TMS IO-C8 IO-C7 VCCIO IO-C6 IO-C5 IO-C4 IO-C3 IO-C2 IO-C1 IO-C0
IO-H1 IO-H2 GNDIO IO-H3 IO-H4 IO-H5 VCCIO IO-H6 IO-H7 IO-H8 GNDINT IO-G4 IO-G5 IO-G6 IO-G7 TDO GNDIO IO-H0
IO-D8 IO-D7 IO-D6 IO-D5 IO-D4 IO-D3 GNDIO VCCIO IO-D2 IO-D1 IO-D0 GNDINT VCCINT IO-E0 IO-E1 IO-E2 GNDIO IO-E3 IO-E4 IO-E5 IO-E6 IO-E7 IO-E8 IO-E9 VCCIO IO-F0 GNDIO IO-F1 IO-F2 IO-F3 IO-F4 IO-F5 IO-F6 GNDIO IO-F7 TCK IO-G0 IO-G1 GNDIO IO-G2 IO-G3
PWRON USBON EEPRCLK EEPRDATA EEPRCS 24MHZ 12MHZ
INPUT/OE2/GCLK2 INPUT/GCLRn INPUT/OE1 INPUT/GCLK1 GNDINT VCCINT
FP3V
TRGOE
ISPTDO FPTDI FPNCSO FPTCK FPNST
RN51 120
LINK_D0 LINK_D1 LINK_D2 LINK_D3
TDO TCK ISPTCK TMS TDI
TRGDCLK TRGASDO TRGNCS TRGNST
USB5V U28 AME1117CCCTZ-3.3V/1A INPUT ADJ/GND VOUT
DNI BC77 0.1U
C33 10U DNI BC78 0.1U DNI BC79 0.1U DNI BC80 0.1U
USB3V
RN46 120
R61 1.5K DNI
EEPCS EEPCLK EEPDI EEPDO
U29 DNI 93LC46B
CS VCC CLK NC DI ORG DO GND
DNI BC81 0.1U
VCC33 R109 0 FP3V
VCC5 R110 0 USB5V
Q6 8550
Q7 8550
Q8 8050
Q9 8050
R63 4.7K
R64 1K
R65 1K
R66 10K
R67 4.7K
R68 1K
R69 1K
R70 10K
PWRON
USBON
RN48 10K
CONF_DONE NSTATUS TCK NCE
RN47 10K
EEPDI
ISPTCK ISPTDO ISPTMS EEPDO ISPTDI
JP3
CPLD ISP
EEPCLK EEPCS
RN49 10K
NCONFIG TDI TMS
RN50 120
ASDO DCLK NCSO DATA0
TRGASDO TRGDCLK TRGNCS TRGNST
12MHZ
RUN/PROG
SW19
SLIDE SW
NCE
TRGOE R62 1.5K
U30
EPCS16
VCC VCC NC1 NC2 NC3 NC4 nCS DATA
DCLK ASDI NC8 NC7 NC6 NC5 GND VCC
TRGNCS TRGNST
Y2
24MHZ
BC82 0.1U
BC83 BC84 BC85 BC86 BC87 BC88 BC89 BC90 0.1U
Title
ALTERA DE2
Size B
Document Number
USB BLASTER
Rev 2.2
Date: Wednesday, February 01, 2012
Sheet 21 of 24



Title	ALTERA DE2	
Size B	Document Number ISP 1362	Rev 2.2
Date: Tuesday, January 31, 2012	Sheet 22 of 24	



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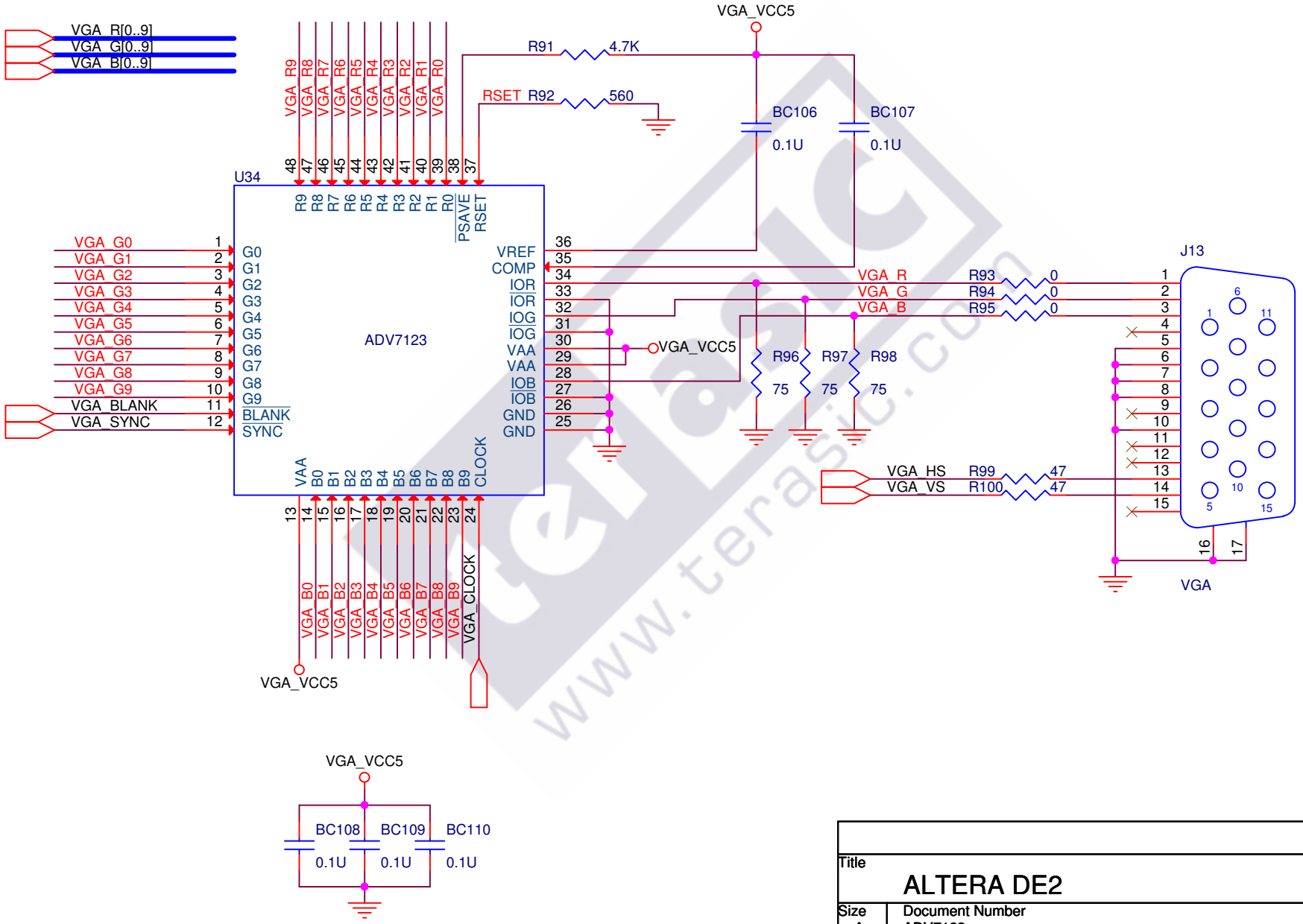
Title	ALTERA DE2	
Size B	Document Number ADV7180	Rev 2.2
Date: Tuesday, January 31, 2012	Sheet 23 of 24	


VGA_R[0..9]
VGA_G[0..9]
VGA_B[0..9]
VGA_R9
VGA_R8
VGA_R7
VGA_R6
VGA_R5
VGA_R4
VGA_R3
VGA_R2
VGA_R1
VGA_R0
R91 4.7K
RSET R92 560
VGA_VCC5
BC106 0.1U
BC107 0.1U
U34
ADV7123
R9 R8 R7 R6 R5 R4 R3 R2 R1 R0 PSAVE RSET
VGA_G0 VGA_G1 VGA_G2 VGA_G3 VGA_G4 VGA_G5 VGA_G6 VGA_G7 VGA_G8 VGA_G9
VGA_BLANK VGA_SYNC
G0 G1 G2 G3 G4 G5 G6 G7 G8 G9 BLANK SYNC
VREF COMP IOR IOR IOG IOG VAA VAA IOB IOB GND GND
VGA_VCC5
R96 75
R97 75
R98 75
VGA_R VGA_G VGA_B
R93 0
R94 0
R95 0
J13
VGA_HS VGA_VS
R99 47
R100 47
VGA
VAA B0 B1 B2 B3 B4 B5 B6 B7 B8 B9 CLOCK
VGA_B0 VGA_B1 VGA_B2 VGA_B3 VGA_B4 VGA_B5 VGA_B6 VGA_B7 VGA_B8 VGA_B9 VGA_CLOCK
VGA_VCC5
BC108 0.1U
BC109 0.1U
BC110 0.1U
Title
ALTERA DE2
Size A
Document Number ADV7123
Rev 2.2
Date: Tuesday, January 31, 2012
Sheet 24 of 24