Timer Registers

Created with Corsair v1.0.4.

Conventions

Access mode	Description
rw	Read and Write
rw1c	Read and Write 1 to Clear
rw1s	Read and Write 1 to Set
ro	Read Only
roc	Read Only to Clear
roll	Read Only / Latch Low
rolh	Read Only / Latch High
wo	Write only
wosc	Write Only / Self Clear

Register map summary

Base address: 0x00000000

Name	Address	Description
TIMER_CTRL	0x00000000	TIMER CONTROL REGISTER
TIMER0	0x00000004	Timer0 Delay Register
TIMER1	0x00000008	Timer1 Delay Register
PWM0	0x0000000c	PWM0 Delay Register
PWM1	0x00000010	PWM1 Delay Register

TIMER_CTRL

TIMER CONTROL REGISTER

Address offset: 0x00000000

Reset value: 0x00000000

31																										4	3	2	1	0
																											PWM_EN		TMR_EN	
																											rw		wosc	

Name	Bits	Mode	Reset	Description
-	31:4	-	0x0000000	Reserved
PWM_EN	3:2	rw	0x0	PWM Timer Enable
TMR_EN	1:0	wosc	0x0	Enable Timer

Back to Register map.

TIMER0

Timer0 Delay Register

Address offset: 0x00000004

Reset value: 0x00000000
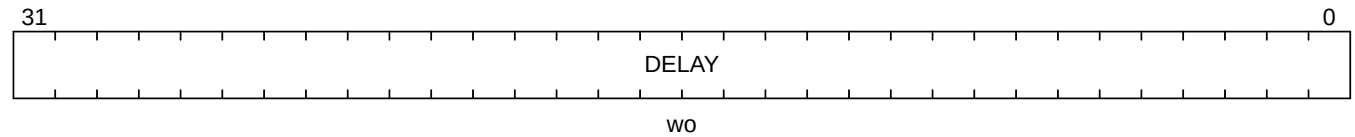
```
31                                                                    0
┌─────────────────────────────────────────────────────────────────────┐
│                              DELAY                                    │
└─────────────────────────────────────────────────────────────────────┘
                               wo
```

Name	Bits	Mode	Reset	Description
DELAY	31:0	wo	0x00000000	Timer Delay

Back to Register map.

TIMER1

Timer1 Delay Register

Address offset: 0x00000008

Reset value: 0x00000000
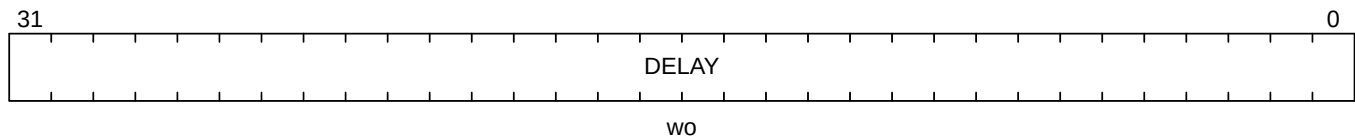
```
31                                                                    0
┌─────────────────────────────────────────────────────────────────────┐
│                              DELAY                                    │
└─────────────────────────────────────────────────────────────────────┘
                               wo
```

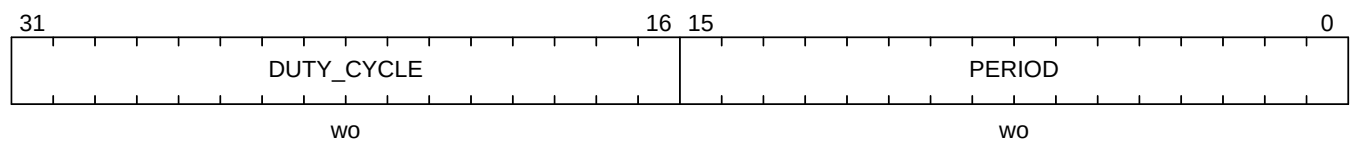| Name	Bits	Mode	Reset	Description
DELAY	31:0	wo	0x00000000	Timer Delay

Back to Register map.

PWM0

PWM0 Delay Register

Address offset: 0x0000000c

Reset value: 0x00000000

```
31                                          16 15                                      0
┌─────────────────────────────────────────────┬───────────────────────────────────────┐
│                 DUTY_CYCLE                   │                PERIOD                 │
└─────────────────────────────────────────────┴───────────────────────────────────────┘
                      wo                                          wo
```

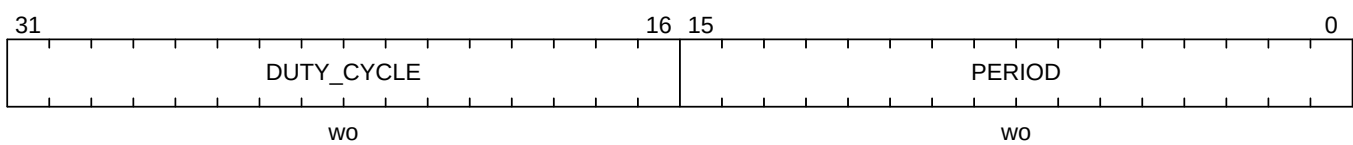| Name	Bits	Mode	Reset	Description
DUTY_CYCLE	31:16	wo	0x0000	PWM0 Duty Cycle
PERIOD	15:0	wo	0x0000	PWM0 Period

Back to Register map.

PWM1

PWM1 Delay Register

Address offset: 0x00000010

Reset value: 0x00000000

```
31                                          16 15                                      0
┌─────────────────────────────────────────────┬───────────────────────────────────────┐
│                 DUTY_CYCLE                   │                PERIOD                 │
└─────────────────────────────────────────────┴───────────────────────────────────────┘
                      wo                                          wo
```

Name	Bits	Mode	Reset	Description
DUTY_CYCLE	31:16	wo	0x0000	PWM0 Duty Cycle
PERIOD	15:0	wo	0x0000	PWM0 Period

Back to Register map.