



Elite

# NPTEL ONLINE CERTIFICATION

(Funded by the MoE, Govt. of India)



This certificate is awarded to

**YAKSH SHIRPURKAR**

for successfully completing the course

**VLSI Design Flow: RTL to GDS**

with a consolidated score of **71** %

Online Assignments	20.31/25	Proctored Exam	50.25/75
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Total number of candidates certified in this course: **2041**

**Dr. Anand Srivastava**  
Coordinator  
Continued Education Program, IIITD

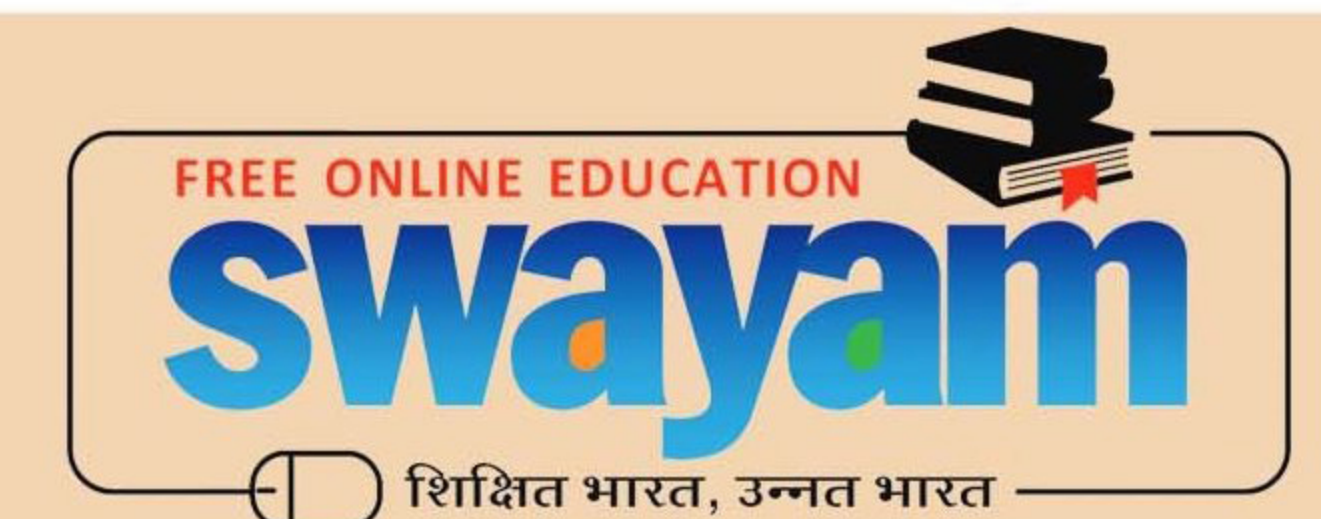
**Jul-Oct 2024**

**(12 week course)**

**Prof. Andrew Thangaraj**  
NPTEL, Coordinator  
IIT Madras



INDRAPRASTHA INSTITUTE of  
INFORMATION TECHNOLOGY **DELHI**



Roll No: NPTEL24EE102S363300406

To verify the certificate



No. of credits recommended: 3 or 4