# **Team6\_Tests**hamadeb\_yakubup\_memberea

05-Oct-2024 22:18:50

# **Table of Contents**

<u>1. l</u>	Indicator Board Requirements Tests	2
	1.1. IB_1_PropulsionSystemLightON	
	1.2. IB 2 PropulsionSystemLightOFF	
	1.3. IB 3 PropulsionSystemLightFlashing	
	1.4. IB 4 HVSystemLightON	
	1.5. IB 5 HVSystemLightOFF	15
	1.6. IB 6 HVSystemLightFlashing	
	1.7. IB 7 LongitudinalControlSystemLightON	
	1.8. IB 8 LongitudinalControlSystemLightOFF	24
	1.9. IB 9 LongitudinalControlSystemLightFLASHING	28
	1.10. IB 10 CAVLateralControlStatusON	
	1.11. IB 11 CAVLateralControlStatusOFF	36
	1.12. IB 12 CAVLateralControlStatusFLASHING	39
	1.13. IB 13 CAVV2XStatusON	
	1.14. IB 14 CAVV2XStatusOFF	47
	1.15. IB 15 CAVV2XStatusFLASHING	50

# 1. Indicator Board Requirements Tests

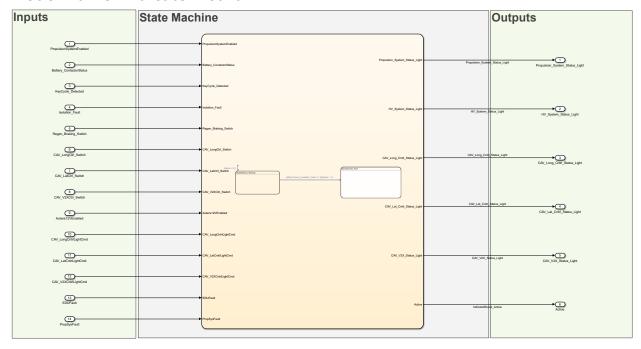
# ${\tt 1.1.}\,IB\_1\_PropulsionSystemLightON$

#### **Test Details**

Requirements
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#### **System Under Test**

#### Model Name: IndicatorBoard



## Simulation Settings Overrides

Simulation Mode	[Model Settings]
Releases	Current
Start Time	0

Stop Time	22
'	

# **External Inputs**

Name	File Path	Status
IB_in_r1_test1.m at (Active)	C:\Users\valca\Documents\ECOCAR\pcm- dev-challenge-y3\IB_in_r1_test1.mat	Successfully mapped in- puts.

## **Output Triggers**

Start Logging	On simulation start
Stop Logging	When simulation stops
Shift time to zero	True

# **Configuration Settings Overrides**

Configuration settings	Do not override model settings
comigaration settings	Do not overnae model settings

# **Logical and Temporal Assessments**

#### Assessments

Enabled	Name	Definition	Requirem ents
True	Assess- ment1	At any point of time, whenever PropulsionSystemEnabled == 1 & PropulsionSystemFault == 0 is true then, with a delay of at most 0.01 seconds, PropulsionSystemStatusLight == 1 must be true	

Symbol	Scope		Metadata
PropulsionSys-	Signal		
temEnabled		Name	From:1
		Path	IndicatorBoard/Chart/SpeedG- oat_Run/From
		Port Index	1
		Field/Element	
PropulsionSys-	Signal		
temFault		Name	From12:1
		Path	IndicatorBoard/Chart/SpeedG- oat_Run/From12
		Port Index	1
		Field/Element	
PropulsionSys-	Signal		
temStatusLight		Name	Propulsion System LED:1
		Path	IndicatorBoard/Chart/SpeedG- oat_Run/Propulsion System LED
		Port Index	1

Symbol	Scope	Metadata
		Field/Element

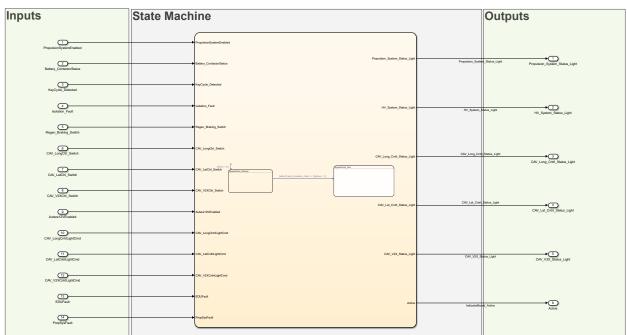
# ${\bf 1.2.}\ IB\_2\_Propulsion System Light OFF$

#### **Test Details**

Requirements
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#### **System Under Test**

#### Model Name: IndicatorBoard



## Simulation Settings Overrides

Simulation Mode	[Model Settings]
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Releases	Current
Start Time	0
Stop Time	22

# **External Inputs**

Name	File Path	Status
IB_in_r1_test1.m at (Active)	C:\Users\valca\Documents\ECOCAR\pcm- dev-challenge-y3\IB_in_r1_test1.mat	Successfully mapped in- puts.

#### **Output Triggers**

Start Logging	On simulation start
Stop Logging	When simulation stops
Shift time to zero	True

# **Configuration Settings Overrides**

Configuration settings	Do not override model settings
comigaration settings	Do not overnae moder settings

## **Logical and Temporal Assessments**

#### Assessments

Enabled	Name	Definition	Requirem ents
True	Assess- ment1	At any point of time, whenever <b>Propulsion- SystemEnabled == 0 &amp; PropulsionSystem-</b>	

Enabled	Name	Definition	Requirem ents
		Fault == 0 is true then, with no delay, PropulsionSystemStatusLight == 0 must be true	

Symbol	Scope	Metadata	
PropulsionSys-	Signal		
temEnabled		Name	From:1
		Path	IndicatorBoard/Chart/SpeedG- oat_Run/From
		Port Index	1
		Field/Element	
PropulsionSys-	Signal		
temFault		Name	From12:1
		Path	IndicatorBoard/Chart/SpeedG- oat_Run/From12
		Port Index	1
		Field/Element	
PropulsionSys-	Signal		
temStatusLight		Name	Propulsion System LED:1

Symbol	Scope	Metadata	
		Path	IndicatorBoard/Chart/SpeedG- oat_Run/Propulsion System LED
		Port Index	1
		Field/Element	

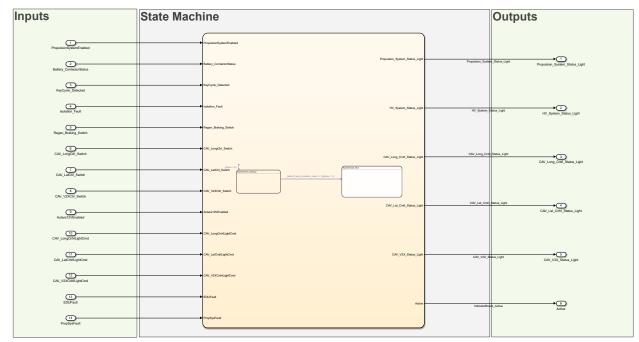
# ${\tt 1.3.} \ IB\_3\_Propulsion System Light Flashing$

#### **Test Details**

Requirements	<ul> <li>Description: IB 3 Propulsion System Status light flashing when faults are detected Document: IndicatorBoard.slreqx</li> </ul>
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#### **System Under Test**

#### Model Name: IndicatorBoard



## Simulation Settings Overrides

Simulation Mode	[Model Settings]
Releases	Current
Start Time	0
Stop Time	22

# **External Inputs**

Name	File Path	Status
IB_in_r1_test1.m at (Active)	C:\Users\valca\Documents\ECOCAR\pcm- dev-challenge-y3\IB_in_r1_test1.mat	Successfully mapped in- puts.

## **Output Triggers**

Start Logging	On simulation start
Stop Logging	When simulation stops
Shift time to zero	True

# **Configuration Settings Overrides**

Configuration settings Do not override model settings
---

## **Logical and Temporal Assessments**

#### Assessments

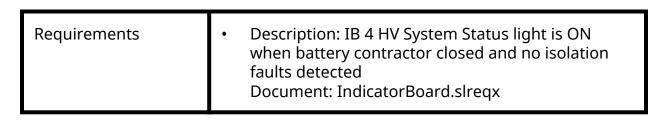
Enabled	bled Name Definition		Requireme nts
True	Assess- ment1	At any point of time, whenever <b>Propulsion-SystemFault == 1</b> is true then, with a delay of at most <b>0.01 seconds</b> , <b>PropulsionSystemStatusLight == 2</b> must be true	

Symbol	Scope	Metadata	
PropulsionSys-	Signal		
temFault		Name	From12:1
		Path	IndicatorBoard/Chart/SpeedG- oat_Run/From12
		Port Index	1

Symbol	Scope		Metadata
		Field/Element	
PropulsionSys-	Signal		
temStatusLight	3.9	Name	Propulsion System LED:1
		Path	IndicatorBoard/Chart/SpeedG- oat_Run/Propulsion System LED
		Port Index	1
		Field/Element	

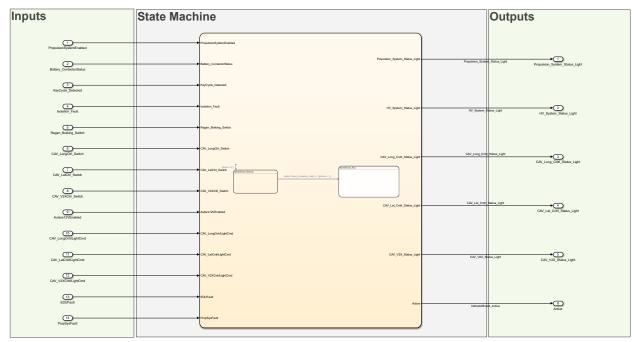
# ${\bf 1.4.}\ IB\_4\_HVSystemLightON$

#### **Test Details**



#### **System Under Test**

#### Model Name: IndicatorBoard



## Simulation Settings Overrides

Simulation Mode	[Model Settings]
Releases	Current
Start Time	0
Stop Time	22

# **External Inputs**

Name	File Path	Status
IB_in_r1_test1.m at (Active)	C:\Users\valca\Documents\ECOCAR\pcm- dev-challenge-y3\IB_in_r1_test1.mat	Successfully mapped in- puts.

## **Output Triggers**

Start Logging	On simulation start
Stop Logging	When simulation stops
Shift time to zero	True

# **Configuration Settings Overrides**

Configuration settings Do not override model settings
---

## **Logical and Temporal Assessments**

#### Assessments

Enabled	bled Name Definition		Requireme nts
True	Assess- ment1	At any point of time, whenever Battery_ContactorStatus == 2 & IsoFault == 0 is true then, with no delay, HVSystemLightStatus == 1 must be true	

Symbol	Scope		Metadata
Battery_Contac-	Signal		
torStatus	- 5	Name	From1:1
		Path	IndicatorBoard/Chart/ SpeedGoat_Run/From1
		Port Index	1

Symbol	Scope		Metadata
		Field/Element	
IsoFault	Signal	Name Path	From2:1  IndicatorBoard/Chart/ SpeedGoat_Run/From2
		Port Index Field/Element	1
HVSystemLight-	Signal		
Status	3	Name	KeyCycle_Detected:1
		Path	IndicatorBoard/Chart/ SpeedGoat_Run/KeyCycle_De- tected
		Port Index	1
		Field/Element	

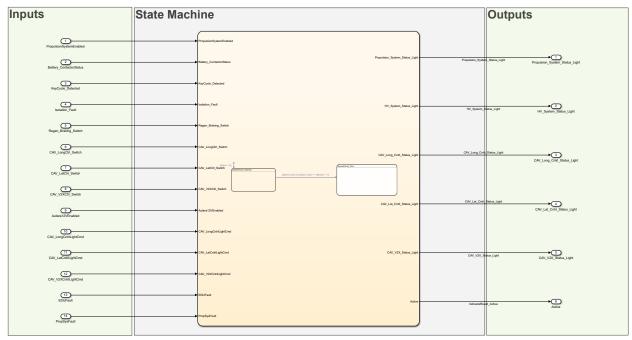
# ${\bf 1.5.}\ IB\_5\_HVSystem LightOFF$

#### **Test Details**

Requirements
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## **System Under Test**

#### Model Name: IndicatorBoard



#### Simulation Settings Overrides

Simulation Mode	[Model Settings]
Releases	Current
Start Time	0
Stop Time	22

## **External Inputs**

Name	File Path	Status
IB_in_r1_test1.m at (Active)	C:\Users\valca\Documents\ECOCAR\pcm-dev-challenge-y3\IB_in_r1_test1.mat	Successfully mapped in- puts.

## **Output Triggers**

Start Logging	On simulation start
Stop Logging	When simulation stops
Shift time to zero	True

# **Configuration Settings Overrides**

Configuration settings
------------------------

## **Logical and Temporal Assessments**

#### Assessments

Enabled	Name	Definition	Requireme nts
True	Assess- ment1	At any point of time, whenever Battery_ContactorStatus == 0 & IsoFault == 0 is true then, with no delay, HVSystemLightStatus == 0 must be true	

Symbol	Scope		Metadata
Battery_Contac- Sign			
torStatus		Name	From1:1
		Path	IndicatorBoard/Chart/ SpeedGoat_Run/From1
		Port Index	1
		Field/Element	
IsoFault	Signal		
		Name	From2:1
		Path	IndicatorBoard/Chart/ SpeedGoat_Run/From2
		Port Index	1
		Field/Element	
HVSystemLight-	Signal		
Status		Name	HV System LED:1
		Path	IndicatorBoard/Chart/ SpeedGoat_Run/HV System LED
		Port Index	1

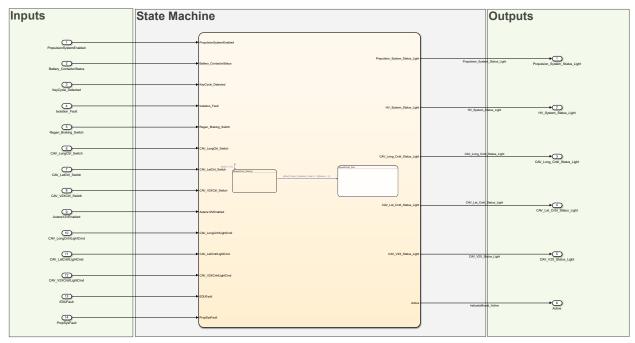
Symbol	Scope	Metadata
		Field/Element

# ${\bf 1.6.} \, IB\_{\bf 6\_HVSystemLightFlashing}$

#### **Test Details**

#### **System Under Test**

#### Model Name: IndicatorBoard



## Simulation Settings Overrides

Simulation Mode	[Model Settings]
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Releases	Current
Start Time	0
Stop Time	22

# **External Inputs**

Name	File Path	Status
IB_in_r1_test1.m at (Active)	C:\Users\valca\Documents\ECOCAR\pcm- dev-challenge-y3\IB_in_r1_test1.mat	Inputs not mapped.

## **Output Triggers**

Start Logging	On simulation start
Stop Logging	When simulation stops
Shift time to zero	True

## **Configuration Settings Overrides**

Configuration settings	Do not override model settings
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## **Logical and Temporal Assessments**

#### Assessments

Enabled	Name	Definition	Requirement s
True	Assess- ment1	At any point of time, whenever <b>IsoFault</b> == 1 is true then, with no delay, <b>HVSys-temLightStatus</b> == 2 must be true	

# Symbols

Symbol	Scope		Metadata
IsoFault	Signal		
		Name	From2:1
		Path	IndicatorBoard/Chart/SpeedG- oat_Run/From2
		Port Index	1
		Field/Element	
HVSystem-	Signal		
LightStatus		Name	HV System LED:1
		Path	IndicatorBoard/Chart/SpeedG- oat_Run/HV System LED
		Port Index	1
		Field/Element	

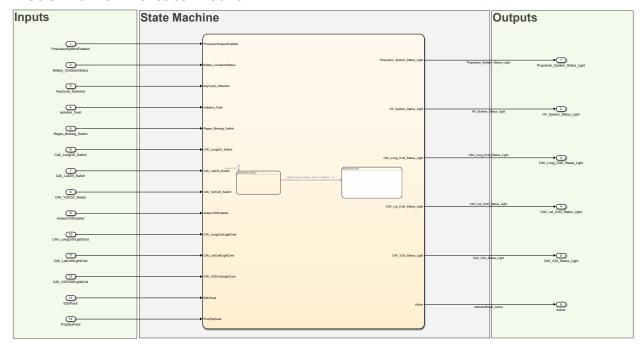
# ${\tt 1.7.\,IB\_7\_LongitudinalControlSystemLightON}$

#### **Test Details**

ON when switch is ON and CAV supervisory con- troller is enabled Document: IndicatorBoard.slreqx
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#### **System Under Test**

#### Model Name: IndicatorBoard



## Simulation Settings Overrides

Simulation Mode	[Model Settings]
Releases	Current
Start Time	0
Stop Time	22

#### **External Inputs**

Name	File Path	Status
IB_in_r1_test1.m at (Active)	C:\Users\valca\Documents\ECOCAR\pcm- dev-challenge-y3\IB_in_r1_test1.mat	Successfully mapped in- puts.

## **Output Triggers**

Start Logging	On simulation start
Stop Logging	When simulation stops
Shift time to zero	True

# **Configuration Settings Overrides**

Configuration settings Do not override model settings
---

## **Logical and Temporal Assessments**

#### Assessments

Enabled	Name	Definition	Require ments
True	Assess- ment1	At any point of time, whenever Longitudinal-ControlLightCommand == 1 & Longitudinal-ControlSystemSwitch == 1 & Autera == 1 is true then, with a delay of at most 0.01 seconds, LongitudinalControlSystemLight == 1 must be true	

Symbol	Scope		Metadata
Longitudinal-	Signal		
ControlSys- temLight	3	Name	CAV_Long_Cntrl_Status_Light
		Path	IndicatorBoard/Chart

Symbol	Scope		Metadata
		Port Index	3
		Field/Element	
Longitudinal-	Signal		
ControlSys- temSwitch		Name	CAV_LongCtrl_Switch:1
		Path	Indicator- Board/CAV_LongCtrl_Switch
		Port Index	1
		Field/Element	
Longitudinal-	Signal		
ControlLight- Command		Name	CAV_LongCntrlLightCmd:1
		Path	IndicatorBoard/CAV_Long- CntrlLightCmd
		Port Index	1
		Field/Element	
Autera	Signal		
		Name	Autera12VEnabled:1

Symbol	Scope	Metadata	
		Path	IndicatorBoard/Autera12VEna- bled
		Port Index	1
		Field/Element	

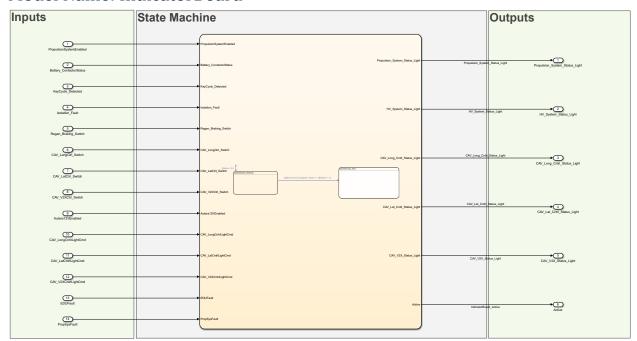
# ${\bf 1.8.} \ IB\_8\_Longitudinal Control System Light OFF$

#### **Test Details**

Requirements	Description: IB 8 CAV Longitudinal Status light is OFF when switch is OFF Document: IndicatorBoard.slreqx
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#### **System Under Test**

#### Model Name: IndicatorBoard



# Simulation Settings Overrides

Simulation Mode	[Model Settings]
Releases	Current
Start Time	0
Stop Time	22

## **External Inputs**

Name	File Path	Status
IB_in_r1_test1.m at (Active)	C:\Users\valca\Documents\ECOCAR\pcm- dev-challenge-y3\IB_in_r1_test1.mat	Successfully mapped in- puts.

# **Output Triggers**

Start Logging	On simulation start
Stop Logging	When simulation stops
Shift time to zero	True

# **Configuration Settings Overrides**

Configuration settings	Do not override model settings
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# **Logical and Temporal Assessments**

#### Assessments

Enabled	Name	Definition	Requir ement s
True	Assess- ment1	At any point of time, whenever Autera12VEnbld == 0   CAV_LongCntrlLightCmd == 0   CAV_Long_Cntrl_Switch == 0 is true then, with a delay of at most 0.01 seconds, LongitudinalControlSystemLight == 0 must be true	

Symbol	Scope		Metadata											
Autera12VEnbld	Sig-	Ι,												
	nal		Name	From5:1										
													Path	Indicator- Board/Chart/ SpeedGoat_Run/ From5
			Port Index	1										
			Field/Element											
CAV_LongCntrlLightCmd	Sig-	١,												
	nal		Name	From7:1										
			Path	Indicator- Board/Chart/										

Symbol	Scope		Metadata	
			SpeedGoa From7	it_Run/
		Port Index	1	
		Field/Eleme	t	
			•	•
CAV_Long_Cntrl_Switch	Sig-		1	
	nal	Name	From3:1	
		Path	Indicator- Board/Cha SpeedGoa From3	
		Port Index	1	
		Field/Eleme	t	
			-	_
LongitudinalControlSys-	Sig-			
temLight	nal	Name CAV Longitudin trol Status Ligh		
		Path	IndicatorBoar SpeedGoat_R Longitudinal ( Status Light	un/CAV
		Port In- dex	1	

Symbol	Scope	Metadata		
		Field/ Element		

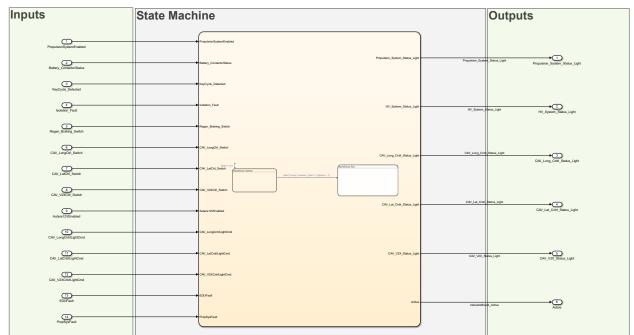
# ${\it 1.9.}\,IB\_9\_Longitudinal Control System Light FLASHING$

#### **Test Details**

Requirements	Description: IB 9 CAV Longitudinal Status light is flashing when faults detected Document: IndicatorBoard.slreqx
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#### **System Under Test**

#### Model Name: IndicatorBoard



# Simulation Settings Overrides

Simulation Mode	[Model Settings]
Releases	Current
Start Time	0
Stop Time	22

## **External Inputs**

Name	File Path	Status
IB_in_r1_test1.m at (Active)	C:\Users\valca\Documents\ECOCAR\pcm- dev-challenge-y3\IB_in_r1_test1.mat	Successfully mapped in- puts.

# **Output Triggers**

Start Logging	On simulation start
Stop Logging	When simulation stops
Shift time to zero	True

# **Configuration Settings Overrides**

Configuration settings
------------------------

# **Logical and Temporal Assessments**

#### Assessments

Enabled	Name	Definition	Requir ement s
True	Assess- ment1	At any point of time, whenever Autera12VEnbld == 1 & CAV_Long_Cntrl_Switch == 1 & CAV_Long-CntrlLightCmd == 2 is true then, with a delay of at most 0.1 seconds, LongitudinalControlSystemLight == 2 must be true	

Symbol	Scope	Metadata			
Autera12VEnbld	Sig-				
	nal	Name	From5:1		
		Path	Indicator- Board/Chart/ SpeedGoat_Run/ From5		
			Port Index	1	
		Field/Element			
			_		
CAV_Long_Cntrl_Switch Sig-					
	nal	Name	From3:1		
		Path	Indicator- Board/Chart/		

Symbol	Scope	Metadata		
			SpeedGo From3	oat_Run/
		Port Index	1	
		Field/Elemer	t	
				_
CAV_LongCntrlLightCmd	Sig- nal		1	
	Hai	Name	From7:1	
		Path	Indicato Board/C SpeedGo From7	
		Port Index	1	
		Field/Elemer	t	
			7	•
LongitudinalControlSys-	Sig-	 		
temLight	nal	Name	CAV Longitu trol Status L	
		Path	SpeedGoat_Run/CAV Longitudinal Control Status Light ort In- 1	
		Port In- dex		

Symbol	Scope	Metadata		
		Field/ Element		

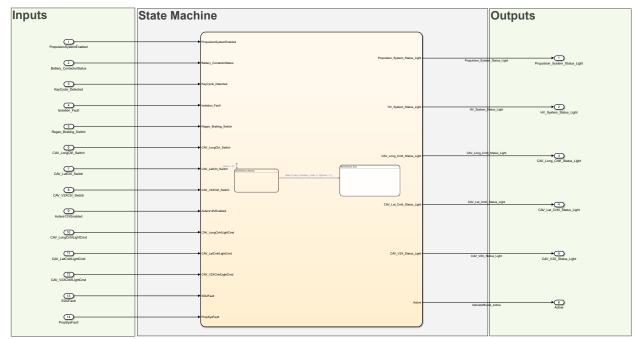
# ${\tt 1.10.}\,IB\_{\tt 10\_CAVLateralControlStatusON}$

#### **Test Details**

Requirements	Description: IB 10 CAV Lateral Control Status light ON when switch is ON and CAV supervisory control is enabled Document: IndicatorBoard.slreqx
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## **System Under Test**

#### Model Name: IndicatorBoard



# Simulation Settings Overrides

Simulation Mode	[Model Settings]
Releases	Current
Start Time	0
Stop Time	22

## **External Inputs**

Name	File Path	Status
IB_in_r1_test1.m at (Active)	C:\Users\valca\Documents\ECOCAR\pcm- dev-challenge-y3\IB_in_r1_test1.mat	Successfully mapped in- puts.

# **Output Triggers**

Start Logging	On simulation start
Stop Logging	When simulation stops
Shift time to zero	True

# **Configuration Settings Overrides**

Configuration settings
------------------------

# **Logical and Temporal Assessments**

#### Assessments

Enabled	Name	Definition	Require ments
True	Assess- ment1	At any point of time, whenever Autera12VEnbld == 1 & CAV_Lat_Cntrl_Switch == 1 & CAV_LatCntrlLightCmd == 1 is true then, with a delay of at most 0.1 seconds, CAVLateralControlStatusON == 1 must be true	

Symbol	Scope	Metadata			
Autera12VEnbld	Signal				
		Name	From6:1		
		Path	IndicatorBoard/Chart/ SpeedGoat_Run/ From6		
		Port Index	1		
		Field/Element			
CAV_LatCntrlLightCmd	CAV_LatCntrlLightCmd Signal				
_		Name	From8:1		
		Path	IndicatorBoard/Chart/ SpeedGoat_Run/ From8		

Symbol	Scope		Metadata			
			Port Index		1	
			Field/Elemen	t		
CAV_Lat_Cntrl_Switch	Signal					
			Name Path		From4:1	
					IndicatorBoard/Chart/ SpeedGoat_Run/ From4	
			Port Index		1	
			Field/Elemen	ıt		
CAVLateralControlStatu-	Signal					
sON			Name		CAV Lateral Control Sta- tus Light:1	
			Path	IndicatorBoard/Chart/ SpeedGoat_Run/CAV Lateral Control Status Light		
			Port Index	1		
			Field/ Element			

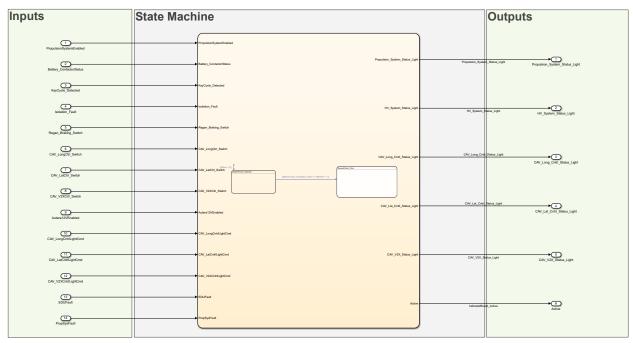
### ${\tt 1.11.}\,IB\_{\tt 11\_CAVLateralControlStatusOFF}$

#### **Test Details**

Requirements
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#### **System Under Test**

#### Model Name: IndicatorBoard



#### Simulation Settings Overrides

Simulation Mode	[Model Settings]	
Releases	Current	
Start Time	0	
Stop Time	22	

### **External Inputs**

Name	File Path	Status
IB_in_r1_test1.m at (Active)	C:\Users\valca\Documents\ECOCAR\pcm-dev-challenge-y3\IB_in_r1_test1.mat	Successfully mapped in- puts.

### **Output Triggers**

Start Logging	On simulation start
Stop Logging	When simulation stops
Shift time to zero	True

### **Configuration Settings Overrides**

Configuration settings	Do not override model settings
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### **Logical and Temporal Assessments**

#### Assessments

Enabled	Name	Definition	Require ments
True	Assess- ment1	At any point of time, whenever Autera12VEnbld == 0   CAV_LatCntrlLightCmd == 0   CAV_Lat_Cntrl_Switch == 0 is true then, with a delay of at most 0.1 seconds, CAVLateralControlStatusOFF == 0 must be true	

Symbol	Scope		Metadata	
Autera12VEnbld	Signal			
		Name	From6:1	
		Path	IndicatorBoard/Chart/ SpeedGoat_Run/ From6	
		Port Index	1	
		Field/Element		
CAV_LatCntrlLightCmd	Signal			
		Name	From8:1	
			Path	IndicatorBoard/Chart/ SpeedGoat_Run/ From8
		Port Index	1	
		Field/Element		
CAV_Lat_Cntrl_Switch	Signal			
			Name	From4:1
	Path	IndicatorBoard/Chart/ SpeedGoat_Run/ From4		

Symbol	Scope			Metadata
		⊩	Port Index Field/Elemen	1 t
CAVLateralControlSta- tusOFF	Signal	L	Name Path	CAV Lateral Control Status Light:1  IndicatorBoard/Chart/ SpeedGoat_Run/CAV Lateral Control Status Light
				Port Index Field/ Element

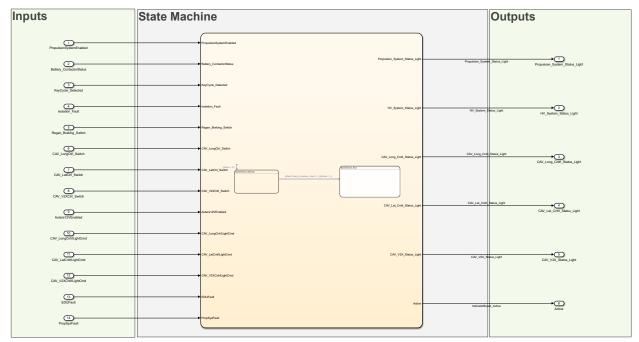
# ${\tt 1.12.}\,IB\_12\_CAVLateral Control Status FLASHING$

#### **Test Details**

Requirements	•	Description: IB 12 CAV Lateral Control Status light flashing when faults detected Document: IndicatorBoard.slreqx
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#### **System Under Test**

#### Model Name: IndicatorBoard



### Simulation Settings Overrides

Simulation Mode	[Model Settings]	
Releases	Current	
Start Time	0	
Stop Time	22	

### **External Inputs**

Name	File Path	Status
IB_in_r1_test1.m at (Active)	C:\Users\valca\Documents\ECOCAR\pcm- dev-challenge-y3\IB_in_r1_test1.mat	Successfully mapped in- puts.

### **Output Triggers**

Start Logging	On simulation start
Stop Logging	When simulation stops
Shift time to zero	True

### **Configuration Settings Overrides**

Configuration settings Do not override model settings
---

### **Logical and Temporal Assessments**

#### Assessments

Enabled	Name	Definition	Require ments
True	Assess- ment1	At any point of time, whenever Autera12VEnbld == 1 & CAV_Lat_Cntrl_Switch == 1 & CAV_LatCntrlLightCmd == 2 is true then, with a delay of at most 0.1 seconds, CAVLateralControlStatusOFF == 2 must be true	

Symbol	Scope	Metadata	
Autera12VEnbld	Signal		
	3	Name	From6:1
		Path	IndicatorBoard/Chart/ SpeedGoat_Run/ From6

Symbol	Scope	Metadata	
		Port Index Field/Element	1
CAV_Lat_Cntrl_Switch	Signal	Name Path  Port Index Field/Element	From4:1  IndicatorBoard/Chart/ SpeedGoat_Run/ From4  1
CAV_LatCntrlLightCmd	Signal	Name Path Port Index Field/Element	From8:1  IndicatorBoard/Chart/ SpeedGoat_Run/ From8  1

Symbol	Scope		Metadata
CAVLateralControlSta- tusOFF	Signal		
tusoff		Name	CAV Lateral Control Sta- tus Light:1
		Path	IndicatorBoard/Chart/ SpeedGoat_Run/CAV Lateral Control Status Light
		Port Index	1
		Field/ Element	

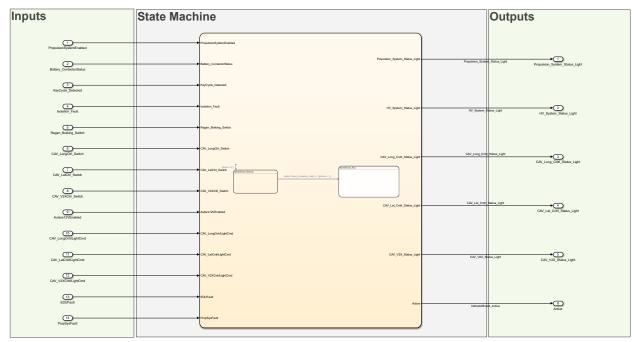
# ${\tt 1.13.}\,IB\_{\tt 13\_CAVV2XStatusON}$

#### **Test Details**

Requirements	Description: IB 13 CAV V2X Status light ON when V2X data can be utilized Document: IndicatorBoard.slreqx
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#### **System Under Test**

#### Model Name: IndicatorBoard



### Simulation Settings Overrides

Simulation Mode	[Model Settings]
Releases	Current
Start Time	0
Stop Time	22

### **External Inputs**

Name	File Path	Status
IB_in_r1_test1.m at (Active)	C:\Users\valca\Documents\ECOCAR\pcm- dev-challenge-y3\IB_in_r1_test1.mat	Successfully mapped in- puts.

### **Output Triggers**

Start Logging	On simulation start	
Stop Logging	When simulation stops	
Shift time to zero	True	

### **Configuration Settings Overrides**

Configuration settings Do not override model settings
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### **Logical and Temporal Assessments**

#### Assessments

Enabled	Name	Definition	Requirem ents
True	Assess- ment1	At any point of time, whenever V2XSwitch == 1 & Autera == 1 & ControlLightCmd == 1 is true then, with a delay of at most 0.01 seconds, V2XStatusLight == 1 must be true	

Symbol	Scope	Metadata		
V2XSwitch	Signal			
	3	Name	CAV_V2XCtrl_Switch:1	
		Path	Indicator- Board/CAV_V2XCtrl_Switch	
		Port Index	1	

Symbol	Scope	Metadata		
		Field/Element		
V2XStatus- Light	Signal	Name		CAV_V2X_Status_Light
		Path		IndicatorBoard/Chart
		Port Index		5
		Field/Element		
Autera	Signal			
		Name	Aut	tera12VEnabled:1
		Path	Ind ble	licatorBoard/Autera12VEna- d
		Port Index	1	
		Field/Element		
				_
Control-	Signal			
LightCmd		Name	CAV_V2XCntrlLightCmd:1	
		Path		licator- ard/CAV_V2XCntrlLightCmd
		Port Index	1	

Symbol	Scope	Metadata
		Field/Element

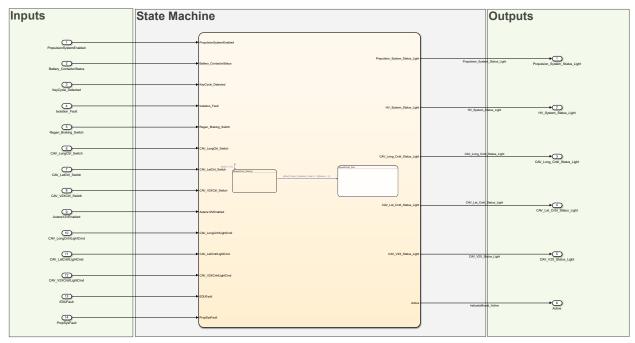
### ${\tt 1.14.}\,IB\_{\tt 14\_CAVV2XStatusOFF}$

#### **Test Details**

Requirements
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#### **System Under Test**

#### Model Name: IndicatorBoard



### Simulation Settings Overrides

Simulation Mode	[Model Settings]
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Releases	Current
Start Time	0
Stop Time	22

### **External Inputs**

Name	File Path	Status
IB_in_r1_test1.m at (Active)	C:\Users\valca\Documents\ECOCAR\pcm- dev-challenge-y3\IB_in_r1_test1.mat	Successfully mapped in- puts.

#### **Output Triggers**

Start Logging	On simulation start
Stop Logging	When simulation stops
Shift time to zero	True

### **Configuration Settings Overrides**

Configuration settings	Do not override model settings
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### **Logical and Temporal Assessments**

#### Assessments

Enabled	Name	Definition	Requireme nts
True	Assess- ment1	At any point of time, whenever <b>V2XSwitch</b> == <b>0</b> is true then, with a delay of at most	

Enabled	Name	Definition	Requireme nts
		<b>0.01 seconds</b> , <b>V2XStatusLight == 0</b> must be true	

Symbol	Scope		Metadata
V2XSwitch	Signal		
		Name	CAV_V2XCtrl_Switch:1
		Path	Indicator- Board/CAV_V2XCtrl_Switch
		Port Index	1
		Field/Element	
V2XStatus-	Signal		
Light		Name	CAV V2X Control Status Light:1
		Path	IndicatorBoard/Chart/SpeedG- oat_Run/CAV V2X Control Sta- tus Light
		Port Index	1
		Field/Element	

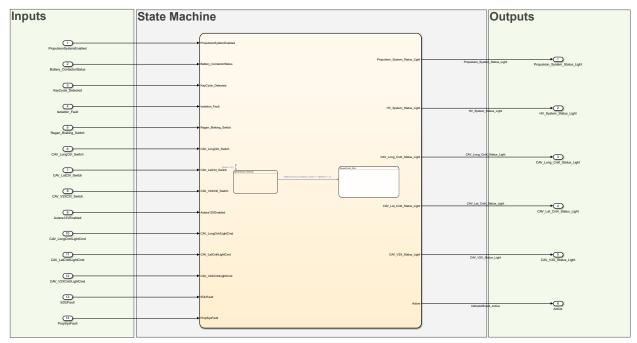
### 1.15. IB\_15\_CAVV2XStatusFLASHING

#### **Test Details**

Requirements	Description: IB 15 CAV V2X Status light flashing when faults detected Document: IndicatorBoard.slreqx
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### **System Under Test**

#### Model Name: IndicatorBoard



#### Simulation Settings Overrides

Simulation Mode	[Model Settings]
Releases	Current
Start Time	0
Stop Time	22

### **External Inputs**

Name	File Path	Status
IB_in_r1_test1.m at (Active)	C:\Users\valca\Documents\ECOCAR\pcm- dev-challenge-y3\IB_in_r1_test1.mat	Successfully mapped in- puts.

### **Output Triggers**

Start Logging	On simulation start		
Stop Logging	When simulation stops		
Shift time to zero	True		

### **Configuration Settings Overrides**

Configuration settings
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### **Logical and Temporal Assessments**

#### Assessments

Enabled	Name	Definition	Requirem ents
True	Assess- ment1	At any point of time, whenever Autera == 1 & V2XSwitch == 1 & V2XControlLightCmd == 2 is true then, with a delay of at most 0.01 seconds, V2XStatusLight == 2 must be true	

Symbol	Scope	Metadata			
Autera	Autera Signal				
		Name Au		Autera12VEnabled:1	
		Path	Ind ble	licatorBoard/Autera12VEna- d	
		Port Index	1		
		Field/Element			
V2XStatus-	V2XStatus- Signal				
Light		Name		CAV_V2X_Status_Light	
		Path		IndicatorBoard/Chart	
		Port Index		5	
		Field/Element			
V2XSwitch	Signal				
		Name	CA	V_V2XCtrl_Switch:1	
		Path	Indicator- Board/CAV_V2XCtrl_Switch		
		Port Index	1		
		Field/Element			

#### 1. Indicator Board Requirements Tests

Symbol	Scope	Metadata		
V2XControl-	Signal			
LightCmd		Name	CAV_V2XCntrlLightCmd:1	
		Path	Indicator- Board/CAV_V2XCntrlLightCmd	
		Port Index	1	
		Field/Element		