

# **Team6\_Tests**

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# 1. Indicator Board Requirements Tests

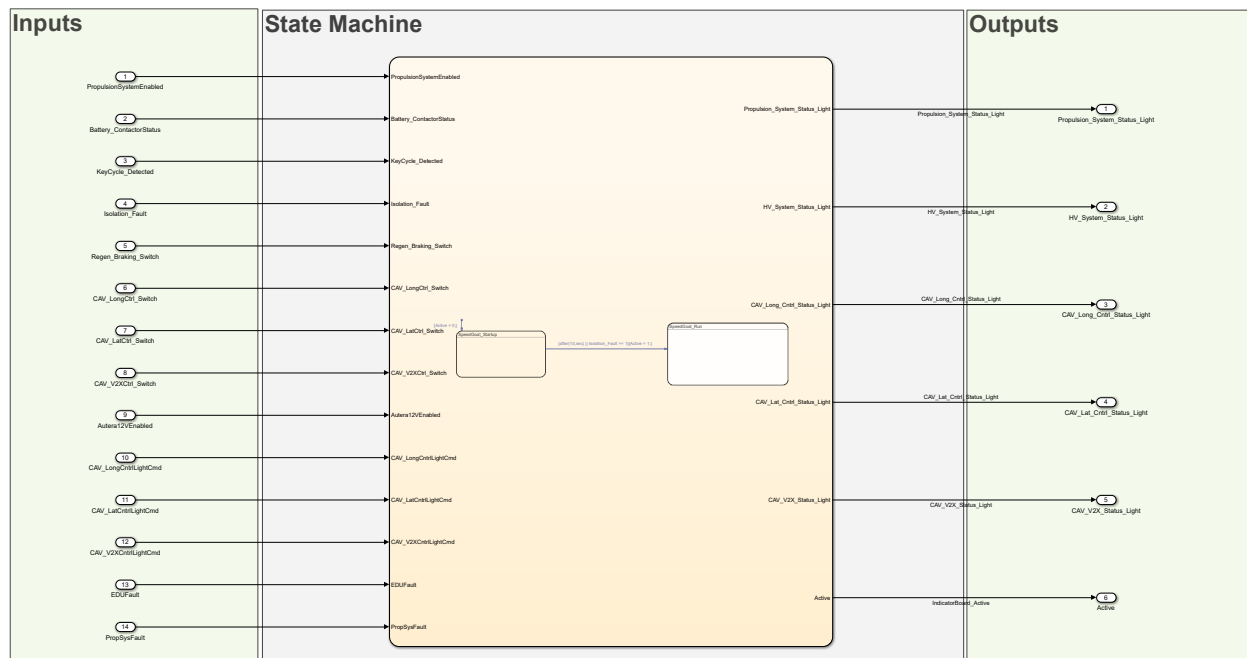
## 1.1. IB\_1\_PropulsionSystemLightON

### Test Details

Requirements	<ul style="list-style-type: none"><li>Description: IB 1 Propulsion System Status light ON when enabled and no faults are detected</li></ul> Document: IndicatorBoard.slsreqx
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### System Under Test

Model Name: IndicatorBoard



### Simulation Settings Overrides

Simulation Mode	[Model Settings]
Releases	Current
Start Time	0

## 1. Indicator Board Requirements Tests

Stop Time	22
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### External Inputs

Name	File Path	Status
IB_in_r1_test1.mat (Active)	C:\Users\yakub\Desktop\PE-TER\Projects\pcm-dev-challenge-y3\IB_in_r1_test1.mat	Successfully mapped inputs.

### Output Triggers

Start Logging	On simulation start
Stop Logging	When simulation stops
Shift time to zero	True

### Configuration Settings Overrides

Configuration settings	Do not override model settings
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### Logical and Temporal Assessments

#### Assessments

Enabled	Name	Definition	Requirements
True	Assessment1	At any point of time, whenever <b>PropulsionSystemEnabled == 1 &amp; PropulsionSystemFault == 0</b> is true then, with a delay of at most <b>0.01 seconds</b> , <b>PropulsionSystemStatusLight == 1</b> must be true	

## 1. Indicator Board Requirements Tests

### Symbols

Symbol	Scope	Metadata	
PropulsionSystemEnabled	Signal		
		Name	From:1
		Path	IndicatorBoard/Chart/SpeedGoat_Run/From
		Port Index	1
		Field/Element	
PropulsionSystemFault	Signal		
		Name	From12:1
		Path	IndicatorBoard/Chart/SpeedGoat_Run/From12
		Port Index	1
		Field/Element	
PropulsionSystemStatusLight	Signal		
		Name	Propulsion System LED:1
		Path	IndicatorBoard/Chart/SpeedGoat_Run/Propulsion System LED
		Port Index	1

## 1. Indicator Board Requirements Tests

Symbol	Scope	Metadata	
		Field/Element	

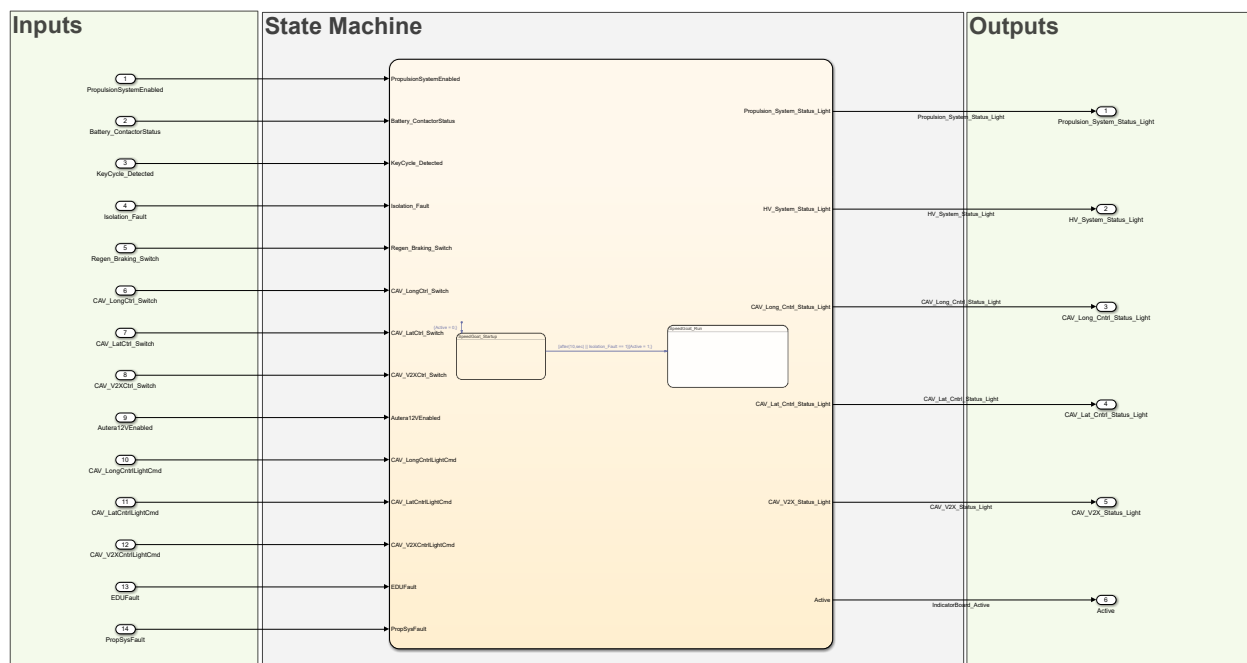
### 1.2. IB\_2\_PropulsionSystemLightOFF

#### Test Details

Requirements	<ul style="list-style-type: none"> <li>Description: IB 2 Propulsion System Status light OFF when disabled</li> <li>Document: IndicatorBoard.slreqx</li> </ul>
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#### System Under Test

Model Name: IndicatorBoard



#### Simulation Settings Overrides

Simulation Mode	[Model Settings]
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## 1. Indicator Board Requirements Tests

Releases	Current
Start Time	0
Stop Time	22

### External Inputs

Name	File Path	Status
IB_in_r1_test1.mat (Active)	C:\Users\yakub\Desktop\PE-TER\Projects\pcm-dev-challenge-y3\IB_in_r1_test1.mat	Successfully mapped inputs.

### Output Triggers

Start Logging	On simulation start
Stop Logging	When simulation stops
Shift time to zero	True

### Configuration Settings Overrides

Configuration settings	Do not override model settings
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### Logical and Temporal Assessments

#### Assessments

Enabled	Name	Definition	Requirements
True	Assessment1	At any point of time, whenever <b>PropulsionSystemEnabled == 0 &amp; PropulsionSystem-</b>	

## 1. Indicator Board Requirements Tests

Enabled	Name	Definition	Requirements
		<b>Fault == 0</b> is true then, with no delay, <b>PropulsionSystemStatusLight == 0</b> must be true	

### Symbols

Symbol	Scope	Metadata	
PropulsionSystemEnabled	Signal		
		Name	From:1
		Path	IndicatorBoard/Chart/SpeedGoat_Run/From
		Port Index	1
		Field/Element	
PropulsionSystemFault	Signal		
		Name	From12:1
		Path	IndicatorBoard/Chart/SpeedGoat_Run/From12
		Port Index	1
		Field/Element	
PropulsionSystemStatusLight	Signal		
		Name	Propulsion System LED:1



## 1. Indicator Board Requirements Tests

Symbol	Scope	Metadata	
		Path	IndicatorBoard/Chart/SpeedGoat_Run/Propulsion System LED
		Port Index	1
		Field/Element	

### 1.3. IB\_3\_PropulsionSystemLightFlashing

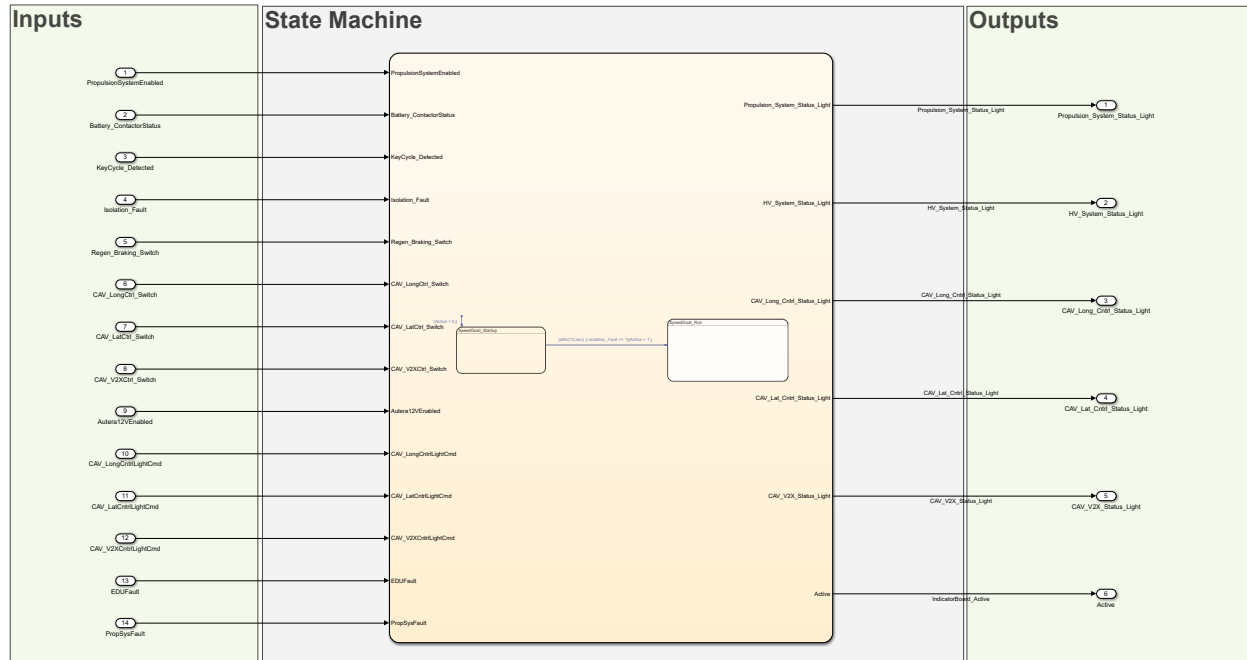
#### Test Details

Requirements	<ul style="list-style-type: none"><li>Description: IB 3 Propulsion System Status light flashing when faults are detected Document: IndicatorBoard.slreqx</li></ul>
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## 1. Indicator Board Requirements Tests

### System Under Test

Model Name: IndicatorBoard



### Simulation Settings Overrides

Simulation Mode	[Model Settings]
Releases	Current
Start Time	0
Stop Time	22

### External Inputs

Name	File Path	Status
IB_in_r1_test1.mat (Active)	C:\Users\yakub\Desktop\PE-TER\Projects\pcm-dev-challenge-y3\IB_in_r1_test1.mat	Successfully mapped inputs.

## Output Triggers

Start Logging	On simulation start
Stop Logging	When simulation stops
Shift time to zero	True

## Configuration Settings Overrides

Configuration settings	Do not override model settings
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## Logical and Temporal Assessments

### Assessments

Enabled	Name	Definition	Requirements
True	Assessment1	At any point of time, whenever <b>PropulsionSystemFault == 1</b> is true then, with a delay of at most <b>0.01 seconds</b> , <b>PropulsionSystemStatusLight == 2</b> must be true	

### Symbols

Symbol	Scope	Metadata	
PropulsionSystemFault	Signal	Name	From12:1
		Path	IndicatorBoard/Chart/SpeedGoat_Run/From12
		Port Index	1

## 1. Indicator Board Requirements Tests

Symbol	Scope	Metadata	
		Field/Element	
PropulsionSystemStatusLight	Signal	Name	Propulsion System LED:1
		Path	IndicatorBoard/Chart/SpeedGoat_Run/Propulsion System LED
		Port Index	1
		Field/Element	

### 1.4. IB\_4\_HVSystemLightON

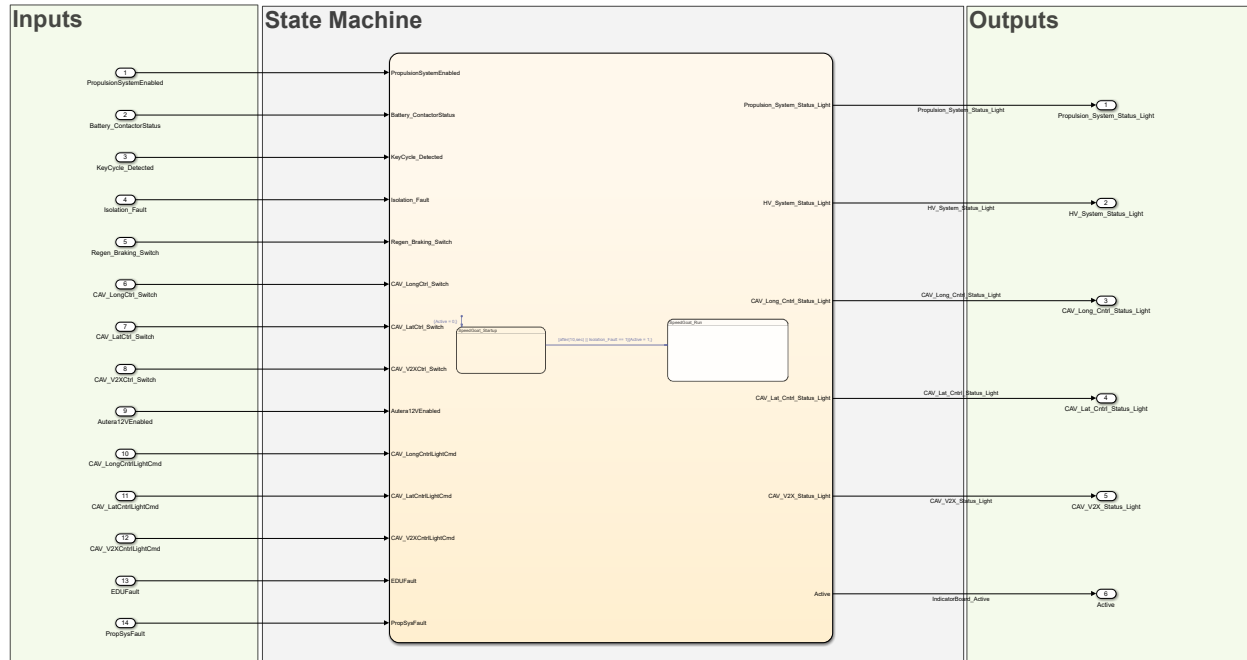
#### Test Details

Requirements	<ul style="list-style-type: none"><li>Description: IB 4 HV System Status light is ON when battery contractor closed and no isolation faults detected Document: IndicatorBoard.slreqx</li></ul>
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## 1. Indicator Board Requirements Tests

### System Under Test

Model Name: IndicatorBoard



### Simulation Settings Overrides

Simulation Mode	[Model Settings]
Releases	Current
Start Time	0
Stop Time	22

### External Inputs

Name	File Path	Status
IB_in_r1_test1.mat (Active)	C:\Users\yakub\Desktop\PE-TER\Projects\pcm-dev-challenge-y3\IB_in_r1_test1.mat	Successfully mapped inputs.

## Output Triggers

Start Logging	On simulation start
Stop Logging	When simulation stops
Shift time to zero	True

## Configuration Settings Overrides

Configuration settings	Do not override model settings
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## Logical and Temporal Assessments

### Assessments

Enabled	Name	Definition	Requirements
True	Assessment1	At any point of time, whenever <b>Battery_ContactorStatus == 2 &amp; IsoFault == 0</b> is true then, with no delay, <b>HVSystemLightStatus == 1</b> must be true	

### Symbols

Symbol	Scope	Metadata	
Battery_ContactorStatus	Signal		
		Name	From1:1
		Path	IndicatorBoard/Chart/SpeedGoat_Run/From1
		Port Index	1

## 1. Indicator Board Requirements Tests

Symbol	Scope	Metadata	
		Field/Element	
IsoFault	Signal	Name	From2:1
		Path	IndicatorBoard/Chart/ SpeedGoat_Run/From2
		Port Index	1
		Field/Element	
HVSystemLight- Status	Signal	Name	KeyCycle_Detected:1
		Path	IndicatorBoard/Chart/ SpeedGoat_Run/KeyCycle_De- tected
		Port Index	1
		Field/Element	

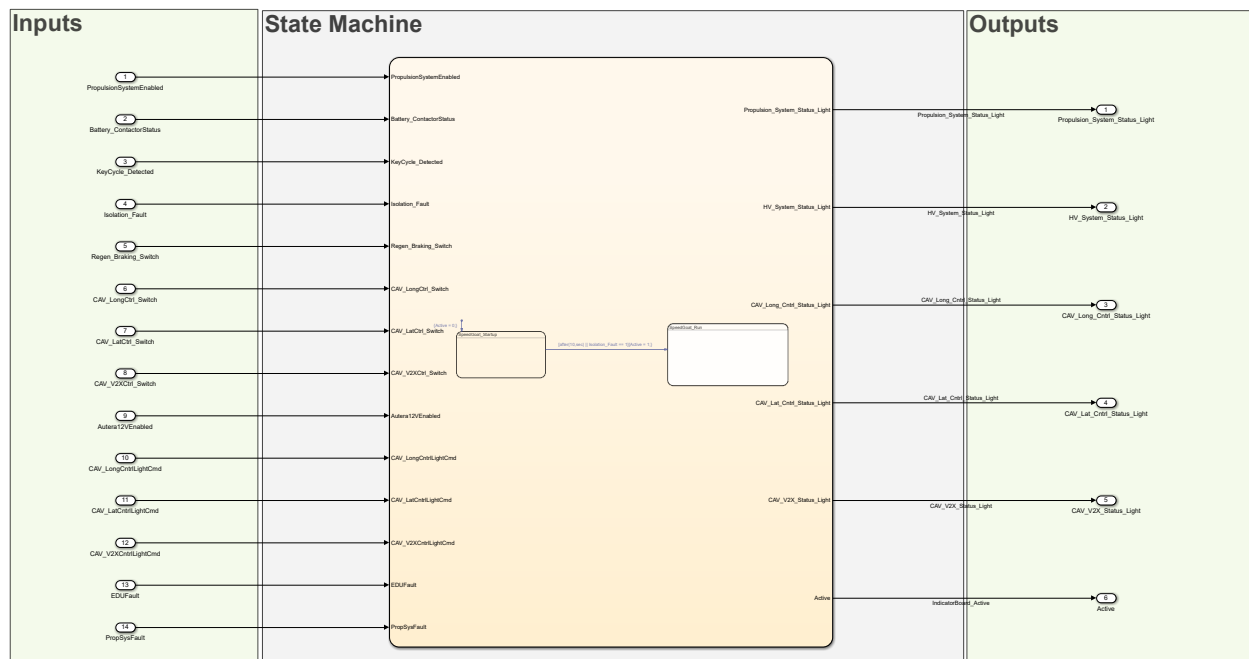
## 1.5. IB\_5\_HVSystemLightOFF

### Test Details

Requirements	<ul style="list-style-type: none"> <li>Description: IB 5 HV System Status light OFF when battery contactors are open</li> <li>Document: IndicatorBoard.slreqx</li> </ul>
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### System Under Test

Model Name: IndicatorBoard



### Simulation Settings Overrides

Simulation Mode	[Model Settings]
Releases	Current
Start Time	0
Stop Time	22



## External Inputs

Name	File Path	Status
IB_in_r1_test1.mat (Active)	C:\Users\yakub\Desktop\PE-TER\Projects\pcm-dev-challenge-y3\IB_in_r1_test1.mat	Successfully mapped inputs.

## Output Triggers

Start Logging	On simulation start
Stop Logging	When simulation stops
Shift time to zero	True

## Configuration Settings Overrides

Configuration settings	Do not override model settings
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## Logical and Temporal Assessments

### Assessments

Enabled	Name	Definition	Requirements
True	Assessment1	At any point of time, whenever <b>Battery_ContactStatus == 0 &amp; IsoFault == 0</b> is true then, with no delay, <b>HVSystemLightStatus == 0</b> must be true	

## 1. Indicator Board Requirements Tests

### Symbols

Symbol	Scope	Metadata	
Battery_Contac- torStatus	Signal		
		Name	From1:1
		Path	IndicatorBoard/Chart/ SpeedGoat_Run/From1
		Port Index	1
		Field/Element	
IsoFault	Signal		
		Name	From2:1
		Path	IndicatorBoard/Chart/ SpeedGoat_Run/From2
		Port Index	1
		Field/Element	
HVSystemLight- Status	Signal		
		Name	HV System LED:1
		Path	IndicatorBoard/Chart/ SpeedGoat_Run/HV System LED
		Port Index	1

## 1. Indicator Board Requirements Tests

Symbol	Scope	Metadata	
		Field/Element	

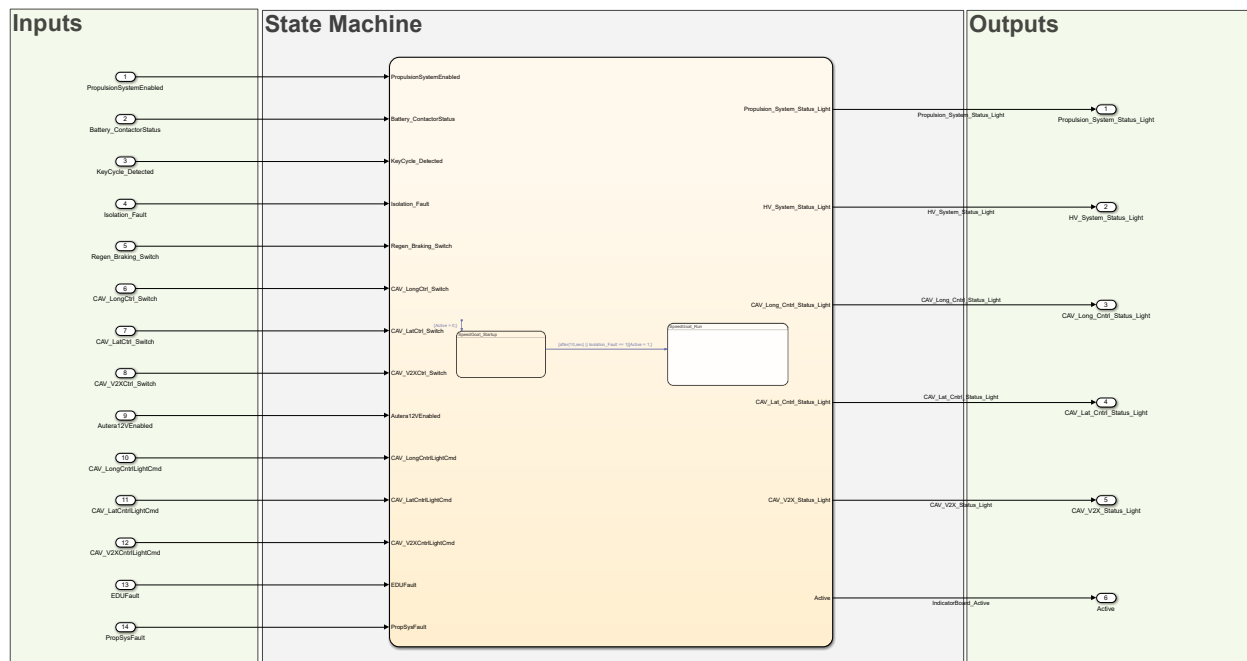
### 1.6. IB\_6\_HVSystemLightFlashing

#### Test Details

Requirements	<ul style="list-style-type: none"> <li>Description: IB 6 HV System Status light flashing when ground fault detected</li> <li>Document: IndicatorBoard.slreqx</li> </ul>
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#### System Under Test

Model Name: IndicatorBoard



#### Simulation Settings Overrides

Simulation Mode	[Model Settings]
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## 1. Indicator Board Requirements Tests

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Releases	Current
Start Time	0
Stop Time	22

### External Inputs

Name	File Path	Status
IB_in_r1_test1.mat (Active)	C:\Users\yakub\Desktop\PE-TER\Projects\pcm-dev-challenge-y3\IB_in_r1_test1.mat	Inputs not mapped.

### Output Triggers

Start Logging	On simulation start
Stop Logging	When simulation stops
Shift time to zero	True

### Configuration Settings Overrides

Configuration settings	Do not override model settings
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## Logical and Temporal Assessments

### Assessments

Enabled	Name	Definition	Requirements
True	Assessment1	At any point of time, whenever <b>IsoFault == 1</b> is true then, with no delay, <b>HVSystemLightStatus == 2</b> must be true	

### Symbols

Symbol	Scope	Metadata	
IsoFault	Signal	Name	From2:1
		Path	IndicatorBoard/Chart/SpeedGoat_Run/From2
		Port Index	1
		Field/Element	
HVSystem-LightStatus	Signal	Name	HV System LED:1
		Path	IndicatorBoard/Chart/SpeedGoat_Run/HV System LED
		Port Index	1
		Field/Element	

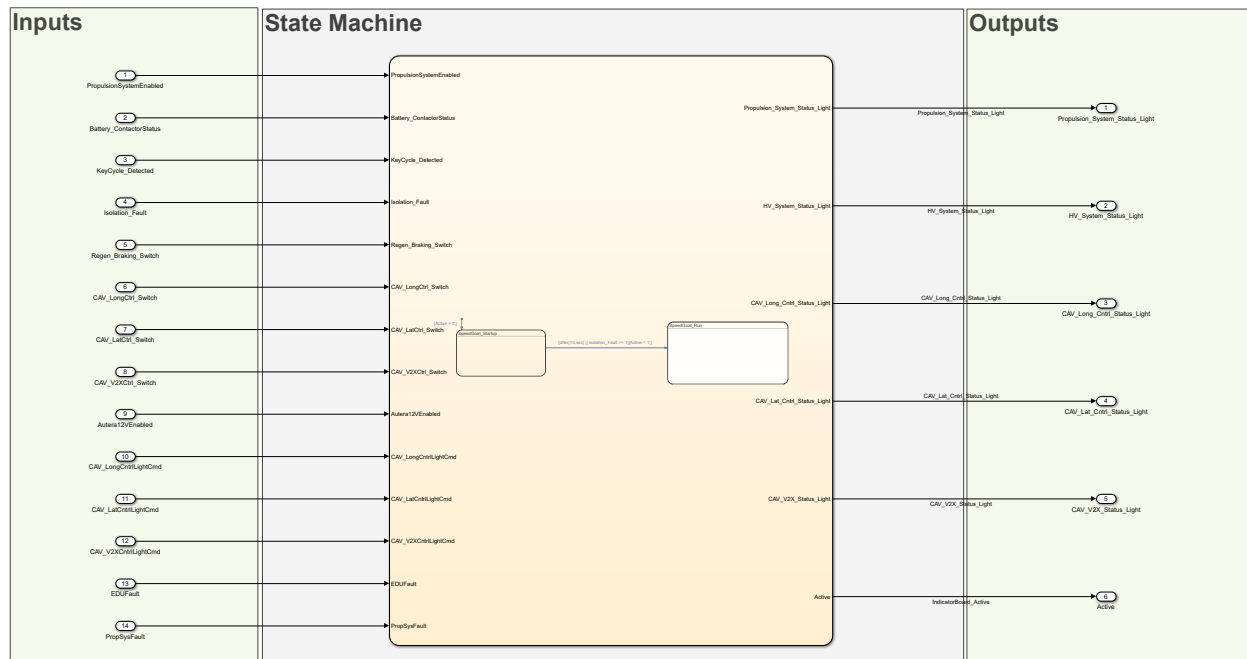
## 1.7. IB\_7\_LongitudinalControlSystemLightON

### Test Details

Requirements	<ul style="list-style-type: none"> <li>Description: IB 7 CAV Longitudinal Status light is ON when switch is ON and CAV supervisory controller is enabled Document: IndicatorBoard.slreqx</li> </ul>
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### System Under Test

Model Name: IndicatorBoard



### Simulation Settings Overrides

Simulation Mode	[Model Settings]
Releases	Current
Start Time	0
Stop Time	22

## External Inputs

Name	File Path	Status
IB_in_r1_test1.mat (Active)	C:\Users\yakub\Desktop\PE-TER\Projects\pcm-dev-challenge-y3\IB_in_r1_test1.mat	Successfully mapped inputs.

## Output Triggers

Start Logging	On simulation start
Stop Logging	When simulation stops
Shift time to zero	True

## Configuration Settings Overrides

Configuration settings	Do not override model settings
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## Logical and Temporal Assessments

### Assessments

Enabled	Name	Definition	Requirements
True	Assessment1	At any point of time, whenever <b>LongitudinalControlLightCommand == 1 &amp; LongitudinalControlSystemSwitch == 1 &amp; Autera == 1</b> is true then, with a delay of at most <b>0.01 seconds</b> , <b>LongitudinalControlSystemLight == 1</b> must be true	

## Symbols

Symbol	Scope	Metadata	
Longitudinal-ControlSystemLight	Signal		
		Name	CAV_Long_Cntrl_Status_Light
		Path	IndicatorBoard/Chart
		Port Index	3
		Field/Element	
Longitudinal-ControlSystemSwitch	Signal		
		Name	CAV_LongCtrl_Switch:1
		Path	Indicator-Board/CAV_LongCtrl_Switch
		Port Index	1
		Field/Element	
Longitudinal-ControlLight-Command	Signal		
		Name	CAV_LongCntrlLightCmd:1
		Path	IndicatorBoard/CAV_Long-CntrlLightCmd
		Port Index	1
		Field/Element	



## 1. Indicator Board Requirements Tests

Symbol	Scope	Metadata	
Autera	Signal	Name	Autera12VEnabled:1
		Path	IndicatorBoard/Autera12VEnabled
		Port Index	1
		Field/Element	

### 1.8. IB\_8\_LongitudinalControlSystemLightOFF

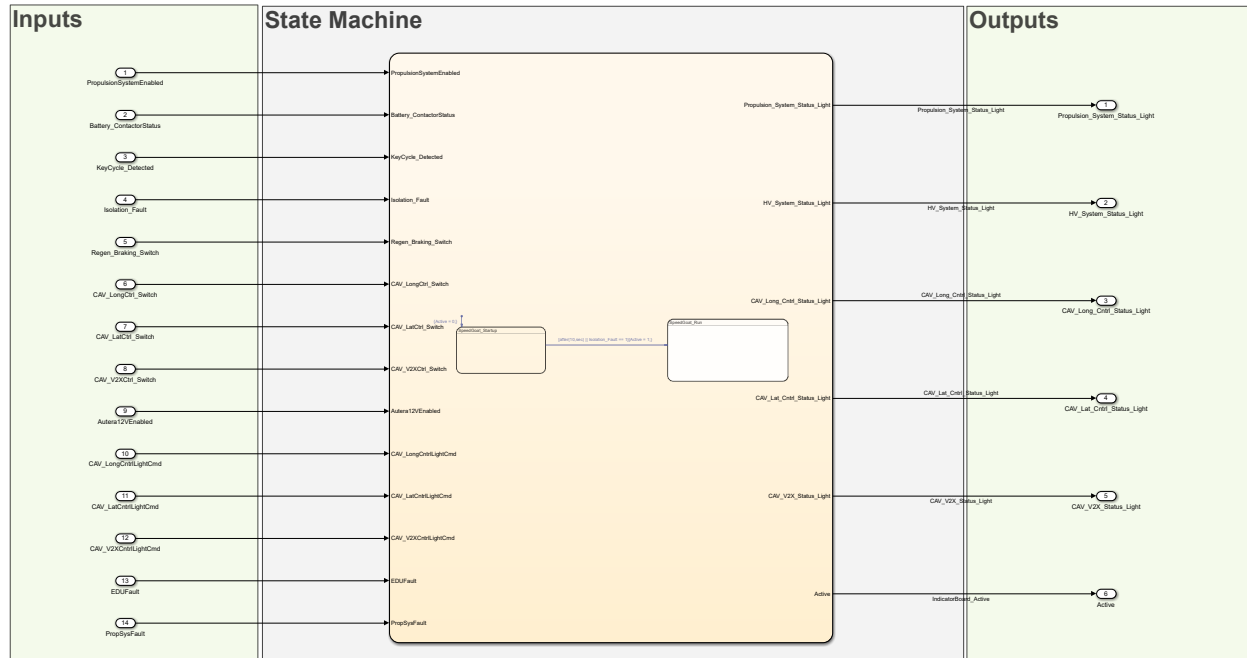
#### Test Details

Requirements	<ul style="list-style-type: none"><li>Description: IB 8 CAV Longitudinal Status light is OFF when switch is OFF Document: IndicatorBoard.slreqx</li></ul>
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## 1. Indicator Board Requirements Tests

### System Under Test

Model Name: IndicatorBoard



### Simulation Settings Overrides

Simulation Mode	[Model Settings]
Releases	Current
Start Time	0
Stop Time	22

### External Inputs

Name	File Path	Status
IB_in_r1_test1.mat at (Active)	C:\Users\yakub\Desktop\PE-TER\Projects\pcm-dev-challenge-y3\IB_in_r1_test1.mat	Successfully mapped inputs.

## 1. Indicator Board Requirements Tests

### Output Triggers

Start Logging	On simulation start
Stop Logging	When simulation stops
Shift time to zero	True

### Configuration Settings Overrides

Configuration settings	Do not override model settings
------------------------	--------------------------------

### Logical and Temporal Assessments

#### Assessments

Enabled	Name	Definition	Requirements
True	Assessment1	At any point of time, whenever <b>Autera12VEnbld == 0   CAV_LongCntrlLightCmd == 0   CAV_Long_Cntrl_Switch == 0</b> is true then, with a delay of at most <b>0.01 seconds</b> , <b>LongitudinalControlSystemLight == 0</b> must be true	

#### Symbols

Symbol	Scope	Metadata	
Autera12VEnbld	Signal		
		Name	From5:1
		Path	Indicator-Board/Chart/

## 1. Indicator Board Requirements Tests

Symbol	Scope	Metadata	
			SpeedGoat_Run/ From5
		Port Index	1
		Field/Element	
CAV_LongCntrlLightCmd	Signal	Name	From7:1
		Path	Indicator- Board/Chart/ SpeedGoat_Run/ From7
		Port Index	1
		Field/Element	
CAV_Long_Cntrl_Switch	Signal	Name	From3:1
		Path	Indicator- Board/Chart/ SpeedGoat_Run/ From3
		Port Index	1
		Field/Element	

## 1. Indicator Board Requirements Tests

Symbol	Scope	Metadata	
LongitudinalControlSystemLight	Signal	Name	CAV Longitudinal Control Status Light:1
		Path	IndicatorBoard/Chart/SpeedGoat_Run/CAV Longitudinal Control Status Light
		Port Index	1
		Field/Element	

### 1.9. IB\_9\_LongitudinalControlSystemLightFLASHING

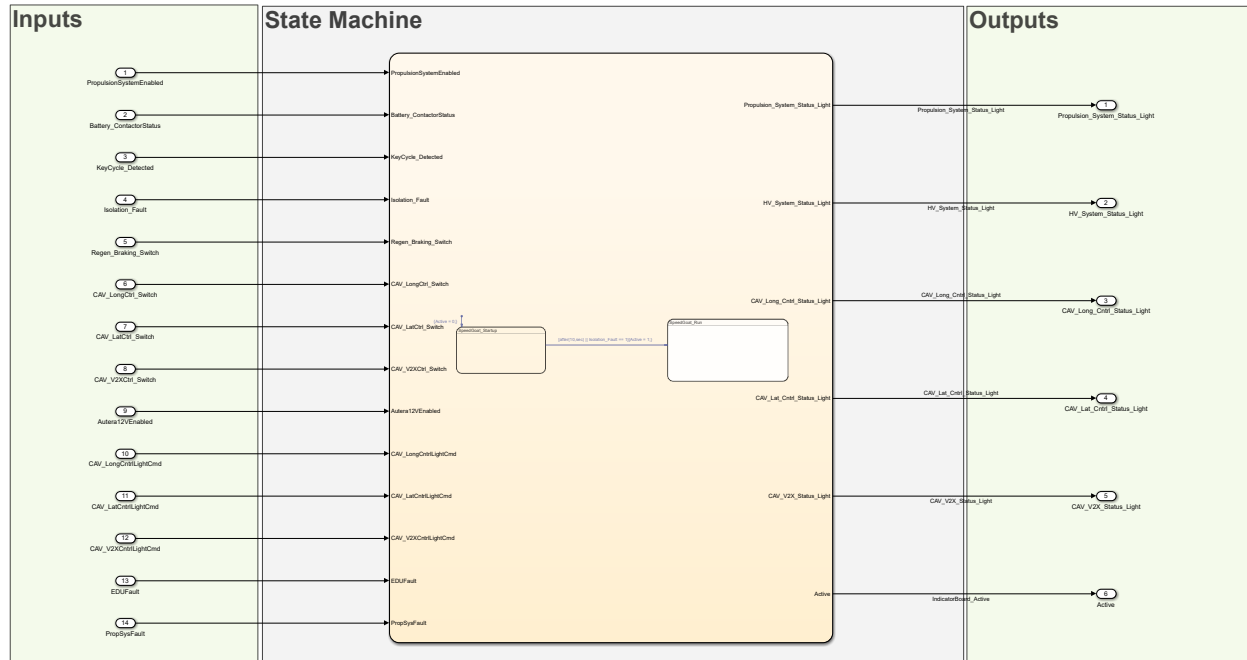
#### Test Details

Requirements	<ul style="list-style-type: none"><li>Description: IB 9 CAV Longitudinal Status light is flashing when faults detected Document: IndicatorBoard.slreqx</li></ul>
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## 1. Indicator Board Requirements Tests

### System Under Test

Model Name: IndicatorBoard



### Simulation Settings Overrides

Simulation Mode	[Model Settings]
Releases	Current
Start Time	0
Stop Time	22

### External Inputs

Name	File Path	Status
IB_in_r1_test1.mat (Active)	C:\Users\yakub\Desktop\PE-TER\Projects\pcm-dev-challenge-y3\IB_in_r1_test1.mat	Successfully mapped inputs.

## Output Triggers

Start Logging	On simulation start
Stop Logging	When simulation stops
Shift time to zero	True

## Configuration Settings Overrides

Configuration settings	Do not override model settings
------------------------	--------------------------------

## Logical and Temporal Assessments

### Assessments

Enabled	Name	Definition	Requirements
True	Assessment1	At any point of time, whenever <b>Autera12VEnbld == 1 &amp; CAV_Long_Cntrl_Switch == 1 &amp; CAV_Long_CntrlLightCmd == 2</b> is true then, with a delay of at most <b>0.1 seconds</b> , <b>LongitudinalControlSystemLight == 2</b> must be true	

### Symbols

Symbol	Scope	Metadata	
Autera12VEnbld	Signal	Name	From5:1
		Path	Indicator-Board/Chart/

## 1. Indicator Board Requirements Tests

Symbol	Scope	Metadata	
			SpeedGoat_Run/ From5
		Port Index	1
		Field/Element	
CAV_Long_Cntrl_Switch	Signal	Name	From3:1
		Path	Indicator- Board/Chart/ SpeedGoat_Run/ From3
		Port Index	1
		Field/Element	
CAV_LongCntrlLightCmd	Signal	Name	From7:1
		Path	Indicator- Board/Chart/ SpeedGoat_Run/ From7
		Port Index	1
		Field/Element	



## 1. Indicator Board Requirements Tests

Symbol	Scope	Metadata	
LongitudinalControlSystemLight	Signal	Name	CAV Longitudinal Control Status Light:1
		Path	IndicatorBoard/Chart/SpeedGoat_Run/CAV Longitudinal Control Status Light
		Port Index	1
		Field/Element	

### 1.10. IB\_10\_CAVLateralControlStatusON

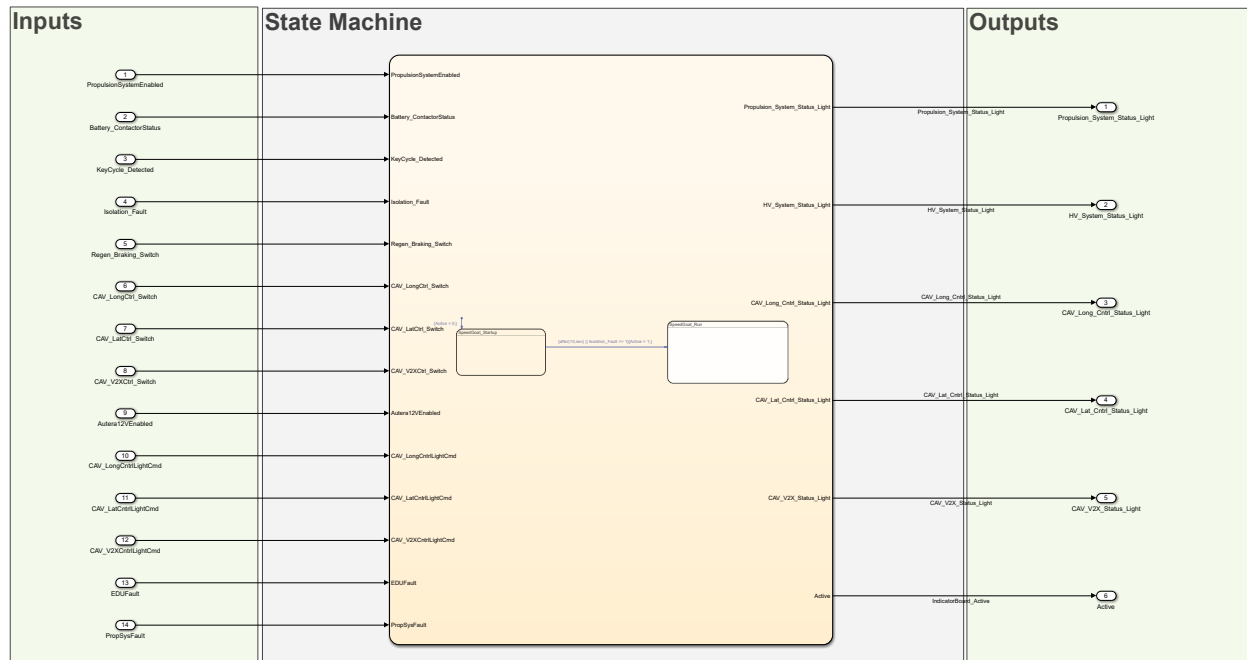
#### Test Details

Requirements	<ul style="list-style-type: none"><li>Description: IB 10 CAV Lateral Control Status light ON when switch is ON and CAV supervisory control is enabled Document: IndicatorBoard.slreqx</li></ul>
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## 1. Indicator Board Requirements Tests

### System Under Test

Model Name: IndicatorBoard



### Simulation Settings Overrides

Simulation Mode	[Model Settings]
Releases	Current
Start Time	0
Stop Time	22

### External Inputs

Name	File Path	Status
IB_in_r1_test1.mat (Active)	C:\Users\yakub\Desktop\PE-TER\Projects\pcm-dev-challenge-y3\IB_in_r1_test1.mat	Successfully mapped inputs.

## Output Triggers

Start Logging	On simulation start
Stop Logging	When simulation stops
Shift time to zero	True

## Configuration Settings Overrides

Configuration settings	Do not override model settings
------------------------	--------------------------------

## Logical and Temporal Assessments

### Assessments

Enabled	Name	Definition	Requirements
True	Assessment1	At any point of time, whenever <b>Autera12VEnbld == 1 &amp; CAV_Lat_Cntrl_Switch == 1 &amp; CAV_LatCntrlLightCmd == 1</b> is true then, with a delay of at most <b>0.1 seconds</b> , <b>CAVLateralControlStatusON == 1</b> must be true	

### Symbols

Symbol	Scope	Metadata				
Autera12VEnbld	Signal	<table><tr><td>Name</td><td>From6:1</td></tr><tr><td>Path</td><td>IndicatorBoard/Chart/SpeedGoat_Run/From6</td></tr></table>	Name	From6:1	Path	IndicatorBoard/Chart/SpeedGoat_Run/From6
Name	From6:1					
Path	IndicatorBoard/Chart/SpeedGoat_Run/From6					

## 1. Indicator Board Requirements Tests

Symbol	Scope	Metadata								
		<table><tr><td>Port Index</td><td>1</td></tr><tr><td>Field/Element</td><td></td></tr></table>	Port Index	1	Field/Element					
Port Index	1									
Field/Element										
CAV_LatCntrlLightCmd	Signal	<table><tr><td>Name</td><td>From8:1</td></tr><tr><td>Path</td><td>IndicatorBoard/Chart/ SpeedGoat_Run/ From8</td></tr><tr><td>Port Index</td><td>1</td></tr><tr><td>Field/Element</td><td></td></tr></table>	Name	From8:1	Path	IndicatorBoard/Chart/ SpeedGoat_Run/ From8	Port Index	1	Field/Element	
Name	From8:1									
Path	IndicatorBoard/Chart/ SpeedGoat_Run/ From8									
Port Index	1									
Field/Element										
CAV_Lat_Cntrl_Switch	Signal	<table><tr><td>Name</td><td>From4:1</td></tr><tr><td>Path</td><td>IndicatorBoard/Chart/ SpeedGoat_Run/ From4</td></tr><tr><td>Port Index</td><td>1</td></tr><tr><td>Field/Element</td><td></td></tr></table>	Name	From4:1	Path	IndicatorBoard/Chart/ SpeedGoat_Run/ From4	Port Index	1	Field/Element	
Name	From4:1									
Path	IndicatorBoard/Chart/ SpeedGoat_Run/ From4									
Port Index	1									
Field/Element										

## 1. Indicator Board Requirements Tests

Symbol	Scope	Metadata	
CAVLateralControlStatusON	Signal	Name	CAV Lateral Control Status Light:1
		Path	IndicatorBoard/Chart/SpeedGoat_Run/CAV Lateral Control Status Light
		Port Index	1
		Field/Element	

### 1.11. IB\_11\_CAVLateralControlStatusOFF

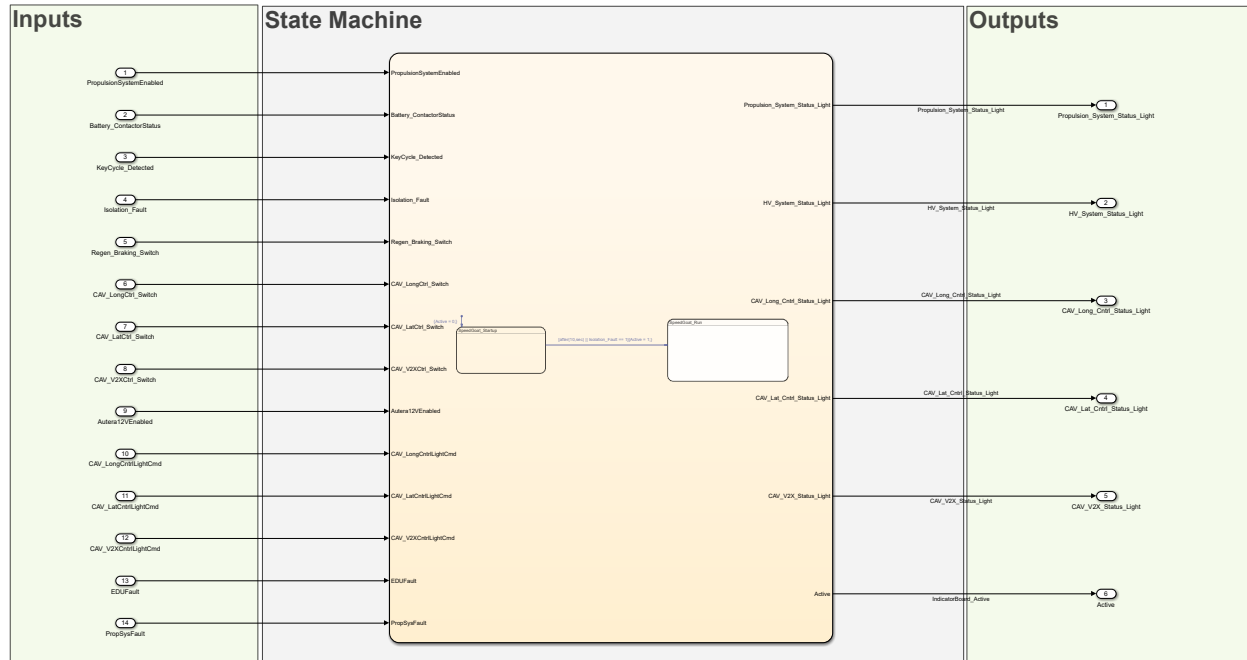
#### Test Details

Requirements	<ul style="list-style-type: none"><li>Description: IB 11 CAV Lateral Control Status light is OFF when the switch is OFF Document: IndicatorBoard.slreqx</li></ul>
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## 1. Indicator Board Requirements Tests

### System Under Test

Model Name: IndicatorBoard



### Simulation Settings Overrides

Simulation Mode	[Model Settings]
Releases	Current
Start Time	0
Stop Time	22

### External Inputs

Name	File Path	Status
IB_in_r1_test1.mat (Active)	C:\Users\yakub\Desktop\PE-TER\Projects\pcm-dev-challenge-y3\IB_in_r1_test1.mat	Successfully mapped inputs.

## Output Triggers

Start Logging	On simulation start
Stop Logging	When simulation stops
Shift time to zero	True

## Configuration Settings Overrides

Configuration settings	Do not override model settings
------------------------	--------------------------------

## Logical and Temporal Assessments

### Assessments

Enabled	Name	Definition	Requirements
True	Assessment1	At any point of time, whenever <b>Autera12VEnbld == 0   CAV_LatCntrlLightCmd == 0   CAV_Lat_Cntrl_Switch == 0</b> is true then, with a delay of at most <b>0.1 seconds</b> , <b>CAVLateralControlStatusOFF == 0</b> must be true	

### Symbols

Symbol	Scope	Metadata				
Autera12VEnbld	Signal	<table><tr><td>Name</td><td>From6:1</td></tr><tr><td>Path</td><td>IndicatorBoard/Chart/SpeedGoat_Run/From6</td></tr></table>	Name	From6:1	Path	IndicatorBoard/Chart/SpeedGoat_Run/From6
Name	From6:1					
Path	IndicatorBoard/Chart/SpeedGoat_Run/From6					

## 1. Indicator Board Requirements Tests

Symbol	Scope	Metadata									
		<table><tr><td>Port Index</td><td>1</td></tr><tr><td>Field/Element</td><td></td></tr></table>	Port Index	1	Field/Element						
Port Index	1										
Field/Element											
CAV_LatCntrlLightCmd	Signal	<table><tr><td>Name</td><td>From8:1</td></tr><tr><td>Path</td><td>IndicatorBoard/Chart/ SpeedGoat_Run/ From8</td></tr><tr><td>Port Index</td><td>1</td></tr><tr><td>Field/Element</td><td></td></tr></table>		Name	From8:1	Path	IndicatorBoard/Chart/ SpeedGoat_Run/ From8	Port Index	1	Field/Element	
Name	From8:1										
Path	IndicatorBoard/Chart/ SpeedGoat_Run/ From8										
Port Index	1										
Field/Element											
CAV_Lat_Cntrl_Switch	Signal	<table><tr><td>Name</td><td>From4:1</td></tr><tr><td>Path</td><td>IndicatorBoard/Chart/ SpeedGoat_Run/ From4</td></tr><tr><td>Port Index</td><td>1</td></tr><tr><td>Field/Element</td><td></td></tr></table>		Name	From4:1	Path	IndicatorBoard/Chart/ SpeedGoat_Run/ From4	Port Index	1	Field/Element	
Name	From4:1										
Path	IndicatorBoard/Chart/ SpeedGoat_Run/ From4										
Port Index	1										
Field/Element											



## 1. Indicator Board Requirements Tests

Symbol	Scope	Metadata	
CAVLateralControlStatusOFF	Signal	Name	CAV Lateral Control Status Light:1
		Path	IndicatorBoard/Chart/SpeedGoat_Run/CAV Lateral Control Status Light
		Port Index	1
		Field/Element	

### 1.12. IB\_12\_CAVLateralControlStatusFLASHING

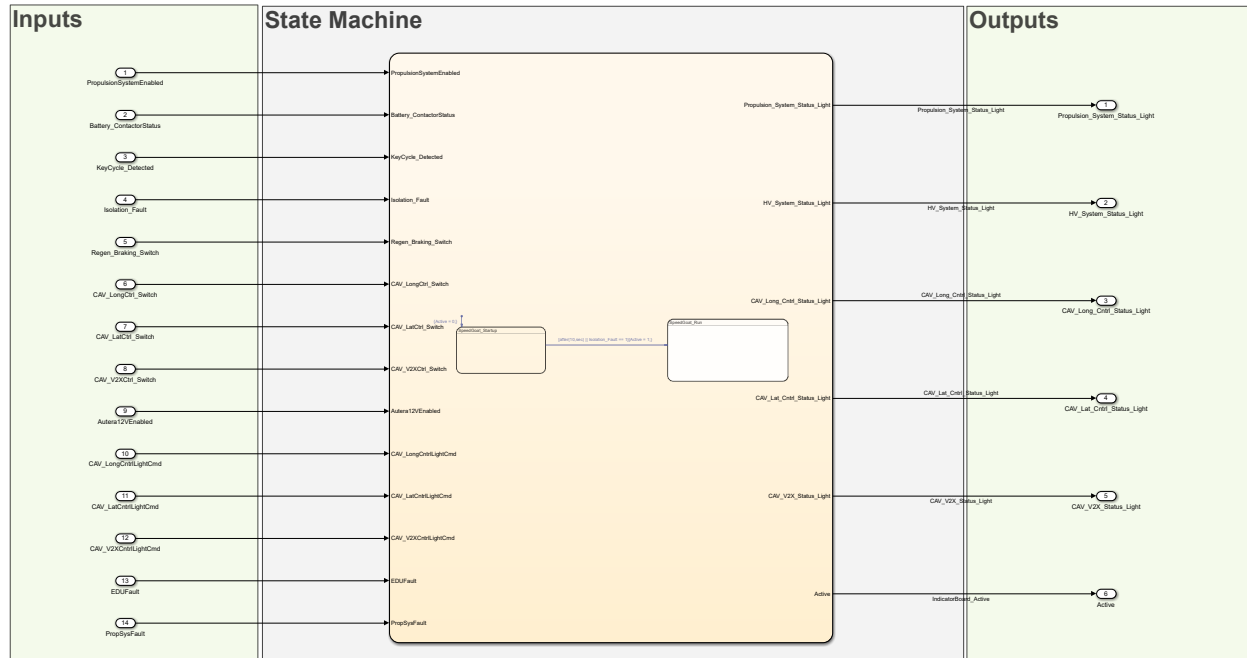
#### Test Details

Requirements	<ul style="list-style-type: none"><li>Description: IB 12 CAV Lateral Control Status light flashing when faults detected Document: IndicatorBoard.slreqx</li></ul>
--------------	---

## 1. Indicator Board Requirements Tests

### System Under Test

Model Name: IndicatorBoard



### Simulation Settings Overrides

Simulation Mode	[Model Settings]
Releases	Current
Start Time	0
Stop Time	22

### External Inputs

Name	File Path	Status
IB_in_r1_test1.mat (Active)	C:\Users\yakub\Desktop\PE-TER\Projects\pcm-dev-challenge-y3\IB_in_r1_test1.mat	Successfully mapped inputs.

## Output Triggers

Start Logging	On simulation start
Stop Logging	When simulation stops
Shift time to zero	True

## Configuration Settings Overrides

Configuration settings	Do not override model settings
------------------------	--------------------------------

## Logical and Temporal Assessments

### Assessments

Enabled	Name	Definition	Requirements
True	Assessment1	At any point of time, whenever <b>Autera12VEnbld == 1 &amp; CAV_Lat_Cntrl_Switch == 1 &amp; CAV_LatCntrlLightCmd == 2</b> is true then, with a delay of at most <b>0.1 seconds</b> , <b>CAVLateralControlStatusOFF == 2</b> must be true	

### Symbols

Symbol	Scope	Metadata				
Autera12VEnbld	Signal	<table><tr><td>Name</td><td>From6:1</td></tr><tr><td>Path</td><td>IndicatorBoard/Chart/SpeedGoat_Run/From6</td></tr></table>	Name	From6:1	Path	IndicatorBoard/Chart/SpeedGoat_Run/From6
Name	From6:1					
Path	IndicatorBoard/Chart/SpeedGoat_Run/From6					

## 1. Indicator Board Requirements Tests

Symbol	Scope	Metadata									
		<table><tr><td>Port Index</td><td>1</td></tr><tr><td>Field/Element</td><td></td></tr></table>	Port Index	1	Field/Element						
Port Index	1										
Field/Element											
CAV_Lat_Cntrl_Switch	Signal	<table><tr><td>Name</td><td>From4:1</td></tr><tr><td>Path</td><td>IndicatorBoard/Chart/ SpeedGoat_Run/ From4</td></tr><tr><td>Port Index</td><td>1</td></tr><tr><td>Field/Element</td><td></td></tr></table>		Name	From4:1	Path	IndicatorBoard/Chart/ SpeedGoat_Run/ From4	Port Index	1	Field/Element	
Name	From4:1										
Path	IndicatorBoard/Chart/ SpeedGoat_Run/ From4										
Port Index	1										
Field/Element											
CAV_LatCntrlLightCmd	Signal	<table><tr><td>Name</td><td>From8:1</td></tr><tr><td>Path</td><td>IndicatorBoard/Chart/ SpeedGoat_Run/ From8</td></tr><tr><td>Port Index</td><td>1</td></tr><tr><td>Field/Element</td><td></td></tr></table>		Name	From8:1	Path	IndicatorBoard/Chart/ SpeedGoat_Run/ From8	Port Index	1	Field/Element	
Name	From8:1										
Path	IndicatorBoard/Chart/ SpeedGoat_Run/ From8										
Port Index	1										
Field/Element											

## 1. Indicator Board Requirements Tests

Symbol	Scope	Metadata	
CAVLateralControlStatusOFF	Signal	Name	CAV Lateral Control Status Light:1
		Path	IndicatorBoard/Chart/SpeedGoat_Run/CAV Lateral Control Status Light
		Port Index	1
		Field/Element	

### 1.13. IB\_13\_CAVV2XStatusON

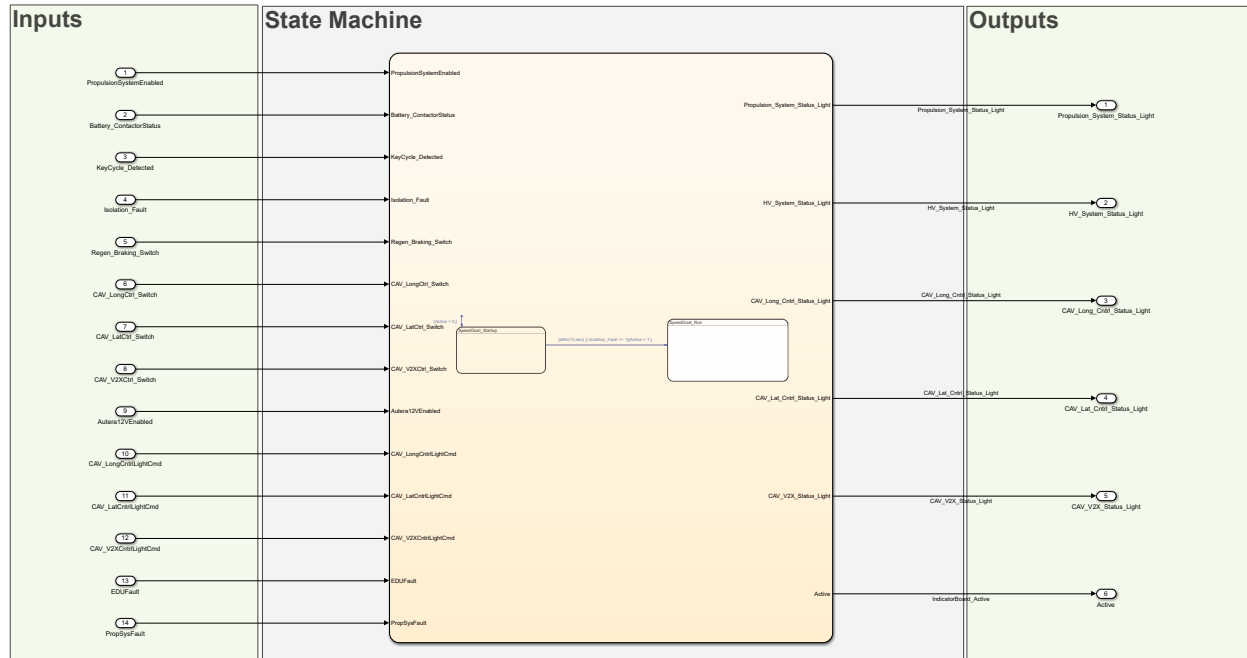
#### Test Details

Requirements	<ul style="list-style-type: none"><li>Description: IB 13 CAV V2X Status light ON when V2X data can be utilized Document: IndicatorBoard.slreqx</li></ul>
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## 1. Indicator Board Requirements Tests

### System Under Test

Model Name: IndicatorBoard



### Simulation Settings Overrides

Simulation Mode	[Model Settings]
Releases	Current
Start Time	0
Stop Time	22

### External Inputs

Name	File Path	Status
IB_in_r1_test1.mat (Active)	C:\Users\yakub\Desktop\PE-TER\Projects\pcm-dev-challenge-y3\IB_in_r1_test1.mat	Successfully mapped inputs.

## Output Triggers

Start Logging	On simulation start
Stop Logging	When simulation stops
Shift time to zero	True

## Configuration Settings Overrides

Configuration settings	Do not override model settings
------------------------	--------------------------------

## Logical and Temporal Assessments

### Assessments

Enabled	Name	Definition	Requirements
True	Assessment1	At any point of time, whenever <b>V2XSwitch == 1 &amp; Autera == 1 &amp; ControllLightCmd == 1</b> is true then, with a delay of at most <b>0.01 seconds</b> , <b>V2XStatusLight == 1</b> must be true	

### Symbols

Symbol	Scope	Metadata	
V2XSwitch	Signal		
		Name	CAV_V2XCtrl_Switch:1
		Path	Indicator-Board/CAV_V2XCtrl_Switch
		Port Index	1

## 1. Indicator Board Requirements Tests

Symbol	Scope	Metadata									
		<table><tr><td>Field/Element</td><td></td></tr></table>		Field/Element							
Field/Element											
V2XStatus-Light	Signal	<table><tr><td>Name</td><td>CAV_V2X_Status_Light</td></tr><tr><td>Path</td><td>IndicatorBoard/Chart</td></tr><tr><td>Port Index</td><td>5</td></tr><tr><td>Field/Element</td><td></td></tr></table>		Name	CAV_V2X_Status_Light	Path	IndicatorBoard/Chart	Port Index	5	Field/Element	
Name	CAV_V2X_Status_Light										
Path	IndicatorBoard/Chart										
Port Index	5										
Field/Element											
Autera	Signal	<table><tr><td>Name</td><td>Autera12VEnabled:1</td></tr><tr><td>Path</td><td>IndicatorBoard/Autera12VEnabled</td></tr><tr><td>Port Index</td><td>1</td></tr><tr><td>Field/Element</td><td></td></tr></table>		Name	Autera12VEnabled:1	Path	IndicatorBoard/Autera12VEnabled	Port Index	1	Field/Element	
Name	Autera12VEnabled:1										
Path	IndicatorBoard/Autera12VEnabled										
Port Index	1										
Field/Element											
Control-LightCmd	Signal	<table><tr><td>Name</td><td>CAV_V2XCntrlLightCmd:1</td></tr><tr><td>Path</td><td>Indicator-Board/CAV_V2XCntrlLightCmd</td></tr><tr><td>Port Index</td><td>1</td></tr></table>		Name	CAV_V2XCntrlLightCmd:1	Path	Indicator-Board/CAV_V2XCntrlLightCmd	Port Index	1		
Name	CAV_V2XCntrlLightCmd:1										
Path	Indicator-Board/CAV_V2XCntrlLightCmd										
Port Index	1										



## 1. Indicator Board Requirements Tests

Symbol	Scope	Metadata	
		Field/Element	

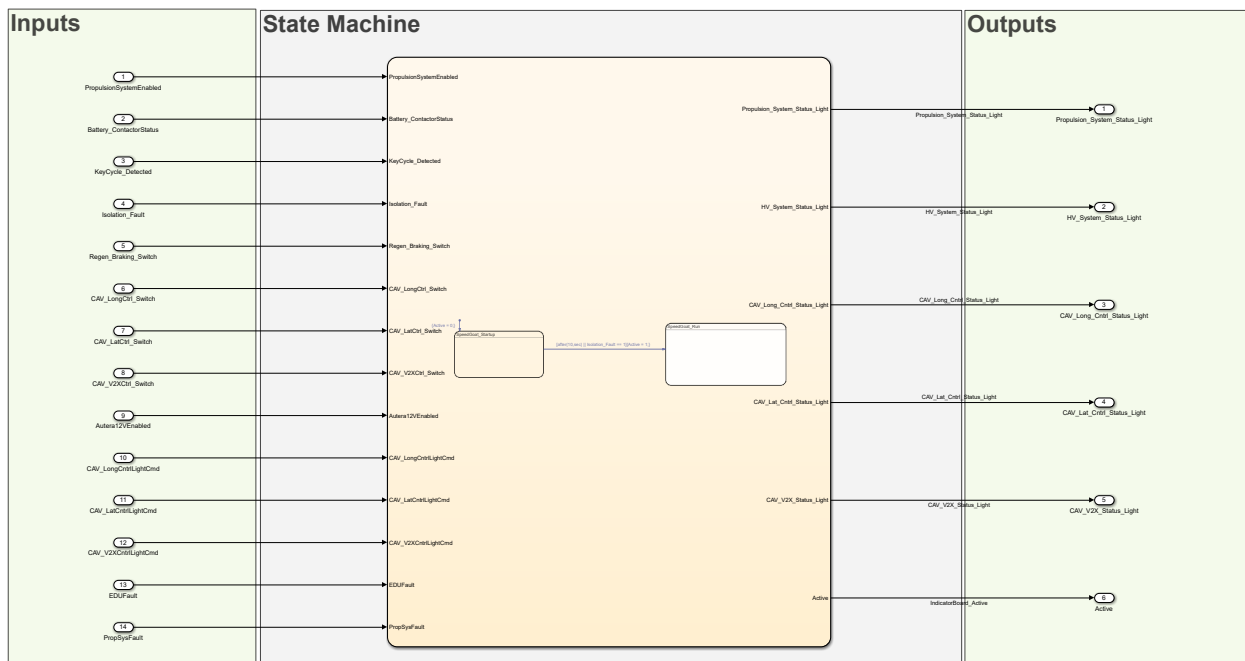
### 1.14. IB\_14\_CAVV2XStatusOFF

#### Test Details

Requirements	<ul style="list-style-type: none"> <li>Description: IB 14 CAV V2X Status light OFF when switch is OFF</li> <li>Document: IndicatorBoard.slreqx</li> </ul>
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#### System Under Test

Model Name: IndicatorBoard



#### Simulation Settings Overrides

Simulation Mode	[Model Settings]
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## 1. Indicator Board Requirements Tests

Releases	Current
Start Time	0
Stop Time	22

### External Inputs

Name	File Path	Status
IB_in_r1_test1.mat (Active)	C:\Users\yakub\Desktop\PE-TER\Projects\pcm-dev-challenge-y3\IB_in_r1_test1.mat	Successfully mapped inputs.

### Output Triggers

Start Logging	On simulation start
Stop Logging	When simulation stops
Shift time to zero	True

### Configuration Settings Overrides

Configuration settings	Do not override model settings
------------------------	--------------------------------

### Logical and Temporal Assessments

#### Assessments

Enabled	Name	Definition	Requirements
True	Assessment1	At any point of time, whenever <b>V2XSwitch == 0</b> is true then, with a delay of at most	

## 1. Indicator Board Requirements Tests

Enabled	Name	Definition	Requirements
		<b>0.01 seconds, V2XStatusLight == 0</b> must be true	

### Symbols

Symbol	Scope	Metadata	
V2XSwitch	Signal	Name	CAV_V2XCtrl_Switch:1
		Path	Indicator-Board/CAV_V2XCtrl_Switch
		Port Index	1
		Field/Element	
V2XStatus-Light	Signal	Name	CAV V2X Control Status Light:1
		Path	IndicatorBoard/Chart/SpeedGoat_Run/CAV V2X Control Status Light
		Port Index	1
		Field/Element	

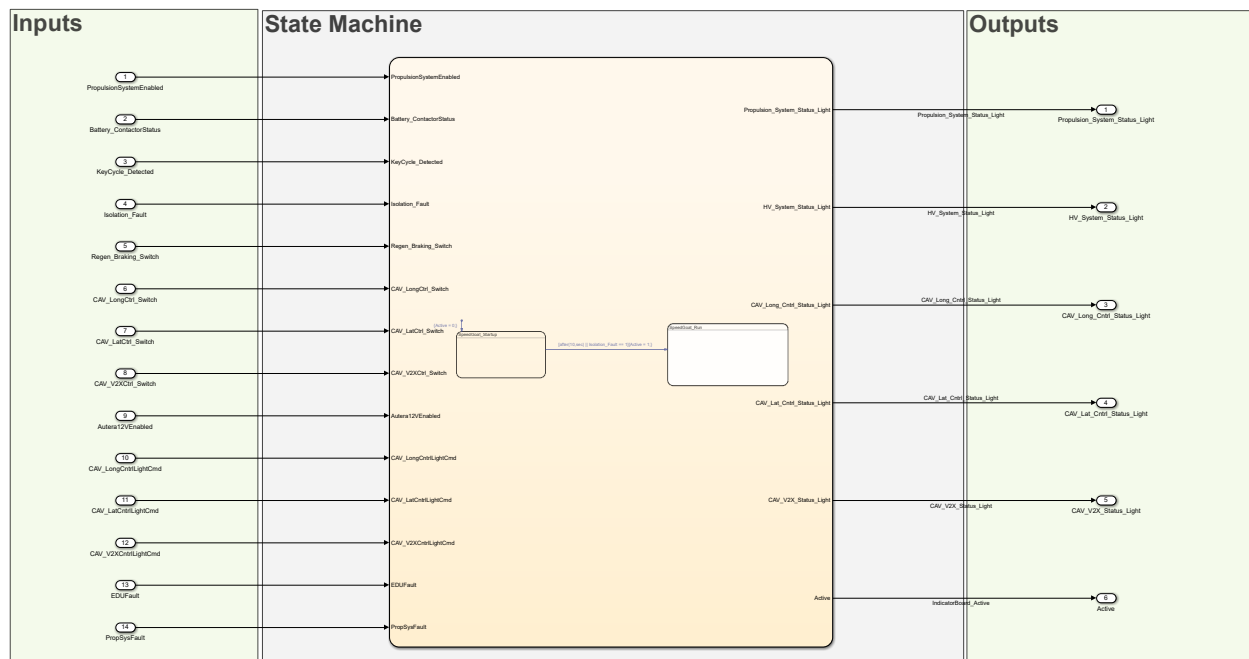
## 1.15. IB\_15\_CAVV2XStatusFLASHING

### Test Details

Requirements	<ul style="list-style-type: none"> <li>Description: IB 15 CAV V2X Status light flashing when faults detected</li> <li>Document: IndicatorBoard.slreqx</li> </ul>
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### System Under Test

Model Name: IndicatorBoard



### Simulation Settings Overrides

Simulation Mode	[Model Settings]
Releases	Current
Start Time	0
Stop Time	22

## External Inputs

Name	File Path	Status
IB_in_r1_test1.mat (Active)	C:\Users\yakub\Desktop\PE-TER\Projects\pcm-dev-challenge-y3\IB_in_r1_test1.mat	Successfully mapped inputs.

## Output Triggers

Start Logging	On simulation start
Stop Logging	When simulation stops
Shift time to zero	True

## Configuration Settings Overrides

Configuration settings	Do not override model settings
------------------------	--------------------------------

## Logical and Temporal Assessments

### Assessments

Enabled	Name	Definition	Requirements
True	Assessment1	At any point of time, whenever <b>Autera == 1 &amp; V2XSwitch == 1 &amp; V2XControlLightCmd == 2</b> is true then, with a delay of at most <b>0.01 seconds</b> , <b>V2XStatusLight == 2</b> must be true	

## Symbols

Symbol	Scope	Metadata								
Autera	Signal	<table><tr><td>Name</td><td>Autera12VEnabled:1</td></tr><tr><td>Path</td><td>IndicatorBoard/Autera12VEnabled</td></tr><tr><td>Port Index</td><td>1</td></tr><tr><td>Field/Element</td><td></td></tr></table>	Name	Autera12VEnabled:1	Path	IndicatorBoard/Autera12VEnabled	Port Index	1	Field/Element	
Name	Autera12VEnabled:1									
Path	IndicatorBoard/Autera12VEnabled									
Port Index	1									
Field/Element										
V2XStatus-Light	Signal	<table><tr><td>Name</td><td>CAV_V2X_Status_Light</td></tr><tr><td>Path</td><td>IndicatorBoard/Chart</td></tr><tr><td>Port Index</td><td>5</td></tr><tr><td>Field/Element</td><td></td></tr></table>	Name	CAV_V2X_Status_Light	Path	IndicatorBoard/Chart	Port Index	5	Field/Element	
Name	CAV_V2X_Status_Light									
Path	IndicatorBoard/Chart									
Port Index	5									
Field/Element										
V2XSwitch	Signal	<table><tr><td>Name</td><td>CAV_V2XCtrl_Switch:1</td></tr><tr><td>Path</td><td>Indicator-Board/CAV_V2XCtrl_Switch</td></tr><tr><td>Port Index</td><td>1</td></tr><tr><td>Field/Element</td><td></td></tr></table>	Name	CAV_V2XCtrl_Switch:1	Path	Indicator-Board/CAV_V2XCtrl_Switch	Port Index	1	Field/Element	
Name	CAV_V2XCtrl_Switch:1									
Path	Indicator-Board/CAV_V2XCtrl_Switch									
Port Index	1									
Field/Element										

## 1. Indicator Board Requirements Tests

Symbol	Scope	Metadata	
V2XControl-LightCmd	Signal	Name	CAV_V2XCntrlLightCmd:1
		Path	Indicator-Board/CAV_V2XCntrlLightCmd
		Port Index	1
		Field/Element	