Yamin Yee

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Objective

Seeking a career of hardware design engineer or FPGA design engineer/ RTL design engineer in a progressive and inclusive organization that provides an opportunity to capitalize my deep technical skills. I am adept in project management and track the record of finishing projects and achieving results. I have led organisations in the university and as the president of the Engineering Student Council in California State University Long Beach. I am eager to learn new skills and more experienced to develop my technical and professional skills.

Education

California State University, Long Beach **May 2020(Spring 2020)**
Bachelor of Science in Computer Engineering

Skills

Technical skills: Programming, Operating systems designing, Project management, Event management, PR management, Human resource management
Programming Languages: FPGA Prototyping, VHDL, Verilog, Assembly Language, RTL design, RISC-V, C, C++, Embedded C, Python
Operating Systems: Xilinx, Vivado, Raspberry Pi, Linux Ubuntu, Arduino, Keil, Visual Studio Code, Microsoft Windows OS
Languages: English(Bilingual), Burmese(Bilingual), Spanish(Intermediate)

Experience

Summer Scholar and Intern, The Hewlett-Packard Company(Remote) **June 2020 – July 2020**

- Managed how a global business operates and how business decisions are made
- Explored technology in core business areas including gaming, 3D, Print, PCs, data analytics, and sales
- Developed personal skills such as interviewing, building a personal brand, storytelling, creating a compelling résumé, giving and receiving feedback, and practicing a growth mindset
- Identified the behaviors and mindsets required to take risks and innovate for the future

Projects

Blue Jay – Food Spy Project (Senior Design Project) **September 2019 – May 2020**

- IoT Integrated Fridge in which the user can track the status of food in the fridge and keep the user aware of its status
- Built the fridge's foundation, integrate components with Fridge, detail design of fridge
- Hardware implementation of the sensors with the Fridge and Raspberry Pi
- Implemented central computer – RasberryPi3 to connect with Pi camera

Single Cycle MIPS Processor (Course Project) **February 2020 – May 2020**

- Used Verilog language to design RISC-V Single Cycle Processor
- Mainly designed the flip flops, instruction memory, adder, register files, ALU, data memory and mux
- Designed testbench to check the simulation to test the modules

System on Chip Specification (Course Project) **January 2019 – May 2019**

- Created UART module to a 16-bit processor which was programmed in assembly
- Simulated Pico Blaze / Use the testing terminal, develop test fixture
- Created the transmitter and receiver of the UART and using terminal to talk to it and its memory

Pong Game VGA Synchronization (Course Project) **October 2018 –December 2018**

- Used Verilog Language to change the color of background, wall, ball, paddle with three-bit VGA color combinations and speed of ball and paddle
- Used VGA synchronization for Borders of the game and display on the screen

Leadership & Volunteering Experiences

Associated Engineering Students Body (AESB), President ` **May 2019 – May 2020**

- AESB is the Engineering Student Council at CSULB

Society of Women Engineers (SWE), Vice President **May 2019- May 2020**
ASI Student Government, Senator of College of Engineering **August 2018-2019**
ASI Student Government, USU Board of Trustees **January 2019 - May 2019**
Society of Asian Scientists and Engineers (SASE), Public Relation Chair **August 2018 – December 2018**

Honors & Awards

Outstanding Leadership and Service Award Class of 2020, College of Engineering CSULB **June 2020**
Certified Achievement by Dr. Robert Garcia Mayor, City of Long Beach, California **May 2019**
Senator of The Week, ASI Student Government **February 6, 2019**
Eta Kappa Nu (HKN) **April 2018**