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| --- | --- | --- | --- |
| **Name:** | **Lee Yan Cheng** | **Lab Group#:** | **B10** |
| **Student Id:** | |  |  |  |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | --- | --- | --- | | **A** | **0** | **1** | **9** | **9** | **1** | **4** | **1** | **B** | |  | |

**Task 2**

|  |  |
| --- | --- |
| 1. Base address of **arrayA** is | **0x10010000 [1pt]** |
| 1. Base address of **count** is at | **0x10010014**  **[1pt]** |
| 1. Assembler Directive for array of size 8: | **arrayA:.word 1,0,3,0,3,7,8,9**  **[1pt]** |
| 1. To map **arrayA**: | **la $t0, arrayA [2pts]** |
| 1. To map **count** : | **la $t8, count**  **lw $t8, 0($t8)**  **[2pts]** |

MIPS translation: **[2pts]**

|  |  |
| --- | --- |
| if ( $t4 <= $t5 )  $t8 = $t8+1 | **slt $t2, $t5, $t4**  **bne $t2, $zero, skip**  **addi $t8, $t8, 1**  **skip:** |

**Task 3**

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| **inputArrayCount.asm Code and Test: [3pts]** |