# XIAOLING,YI

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HomePage: https://xiaolingyi.github.io

## Education

**Fudan University**                                                                 Shanghai, China
*M.S. in Microelectronics and Solid State Electronics*                    *Sep 2020 – Jun 2023*
- GPA: 3.58/4: (Rank: NA)
- Research interests: DNN Accelerators, DNN Accelerators High-level Modeling, Computer Architecture Design Space Exploration, Electronic Design Automation (EDA)

**Fudan University**                                                                 Shanghai, China
*B.E. in Microelectronic Science and Engineering*                         *Sep 2016 – Jun 2020*
- GPA: 3.67/4.0 (Rank: 10/120)
- Relevant coursework: Computer Architecture, Design of Integrated Circuits, Principles and Applications of FPGA, Mathematical Statistics and Stochastic Process, Signal and System, Data Structure and Algorithm Design

## PUBLICATIONS

- **Xiaoling Yi**, Jiangnan Yu, Zheng Wu, Xiankui Xiong, Dong Xu, Chixiao Chen, Jun Tao, Fan Yang, "NNASIM: an Efficient Event-Driven Simulator for DNN Accelerators with Accurate Timing and Area Models," IEEE International Symposium on Circuits and Systems (ISCAS), 2022.
- Zheng Wu, Wuzhen Xie, **Xiaoling Yi**, haotao yang, ruiyao pu, Xiankui Xiong, Dong Xu, Chixiao Chen, Jun Tao, Fan Yang, "An Automated Compiler for RISC-V Based DNN Accelerator," IEEE International Symposium on Circuits and Systems (ISCAS), 2022.

## RESEARCH EXPERIENCE

**Fudan University (State Key Laboratory of ASIC & System)**                Shanghai, China
*Research Assistant to Prof. Fan Yang*                                        *July 2020 – Present*
- AI architecture design based on reconfigurable PE engine and dataflow
- DNN Accelerator functional modeling with accurate timing and area models
- Rocket Custom Coprocessor design and its FPGA prototype implementation

**University of Alberta**                                                            Alberta, Canada
*Exchange student*                                                            *July 2018 - August 2018*
- Acdemic writing and public speaking

## TEACHING

**Fudan University**                                                                 Shanghai, China
*Teaching Assistant*                                                        *January 2021 – June 2021*
- Performed TA duties for Fundamentals of Computer Software(instructor: Prof. Changhao Yan)

## SELECTED AWARDS AND HONORS

- First Prize Scholarship, Fudan University (top 5 %)                                       2021
- National 2nd Prize, China Post-Graduate Mathematical Contest in Modeling (top 15 %)      2021
- National 1st Prize, Integrated Circuit EDA Elite Challenge (top 5 %)                      2021
- Outstanding Graduates of Shanghai (top 5 %)                                               2020
- National 3rd Prize, National College Students Electronic Design Competition (top 20 %)    2019
- First Prize Scholarship, Fudan University (top 5 %)                                       2019
- Outstanding Student of Fudan University (top 5 %)                                         2019
- Second Prize Scholarship, Fudan University (top 15 %)                                2017,2018

## TECHNICAL SKILLS

**Computer and Language Skills**

- **Programming languages** & **Software**: C/C++, Chisel, MATLAB, Python, Shell, Verilog Kits, Haskell, Tcl, TVM, Vivado, Modesim, Design, Compiler, ICC

- **Languages**: Mandarin Chinese (native), English (proficient, IELTS: )

## ADDITIONAL INFORMATION

**Personal Interest**

- psychology, badminton