Experiment 3+4: CMOS Operational Amplifie

Department: 電機三

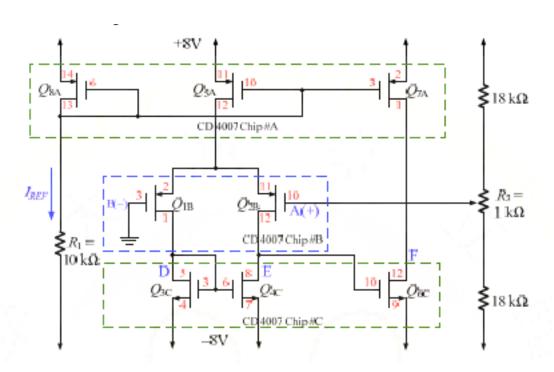
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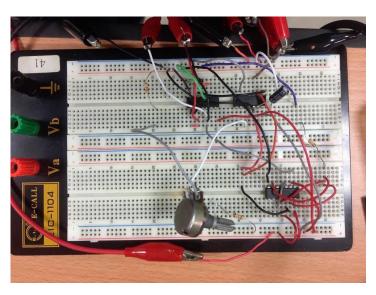
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1. Experiment Objectives

1.1.To make sure all the MOS of the circuit are able to enter saturation region so that the circuit can applied to be an operational amplifier.

2. Experiment Setups

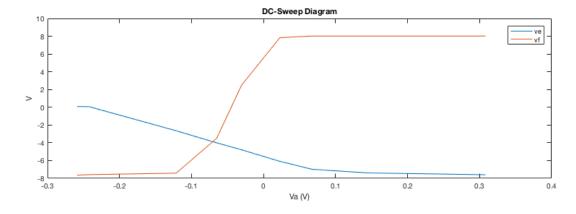




3. Labs Work

3.1. DC-Sweep Diagram

Ve (V)	Vf (V)
0.0847	-7.638
0.0742	-7.583
-2.6423	-7.412
-4.002	-3.4934
-4.784	2.4915
-6.088	7.84
-6.980	8.022
-7.387	8.022
-7.594	8.022
	0.0847 0.0742 -2.6423 -4.002 -4.784 -6.088 -6.980 -7.387



3.2.

