

Xi Yang

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School of Computer Science

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yangxi.github.io

Australian National University

Goal

Craft platforms with which humans can control the future of computing.

Research Interests

My research interests have been focusing on programming languages, computer architecture, and operating systems, especially micro-architecture aware optimizations.

Now, I am focusing on the data management medium.

Education

- **Australian National University** Canberra, Australia
Ph.D. Computer Science *Oct. 2011 - Date*
 - Supervisors: Prof. Steve Blackburn, Prof. Kathryn McKinley
- **Australian National University** Canberra, Australia
Mphil Computer Science *May 2009 - March 2011*
 - Thesis: Locality Aware Zeroing: Exploiting Both Hardware and Software Semantics
 - Supervisors: Prof. Steve Blackburn, Prof. Kathryn McKinley
- **University of Electronic Science and Technology of China** Chengdu, China
B.S. Computer Science *Sept. 2004 - July 2008*
 - Thesis: RTEMS on L4 Microkernel
 - Supervisor: Prof. Kevin Elphinstone
 - I did the project at NICTA's ERTOS group, University of New South Wales as a visitor student.

Honors and Awards

- Paper recognized as Honorable Mention in **IEEE Micro Top Picks from the 2015 Computer Architecture Conferences**. *"Computer Performance Microscopy with SHIM"*
- **2012 Google Australia PhD Fellowship in Energy Aware Computing**: One year fellowship, one of three awarded in Australia and 40 worldwide.
- Paper selected for **Communications of the ACM Research Highlights**. *"Looking Back and Looking Forward: Power, Performance, and Upheaval"*
- Paper selected for **IEEE Micro Top Picks from the 2011 Computer Architecture Conferences**. *"What Is Happening to Power, Performance, and Software?"*

Publications

- **X. Yang**, S. M. Blackburn, and K. S. McKinley, "**Elfen Scheduling: Fine-Grain Principled Borrowing from Latency-Critical Workloads using Simultaneous Multithreading**", in Proceedings of the 2016 USENIX Annual Technical Conference (**USENIX ATC**) , Denver, CO, June 22-24, 2016.
- **X. Yang**, S. M. Blackburn, and K. S. McKinley, "**Computer Performance Microscopy with SHIM**", in Proceedings of the 42nd International Symposium on Computer Architecture (**ISCA**) , Portland, OR, June 13-17, 2015.
- R. Shahriyar, S. M. Blackburn, **X. Yang**, and K. M. McKinley, "**Taking Off the Gloves with Reference Counting Immix**", in Proceedings of the 2013 ACM SIGPLAN Conference on Object-Oriented Programming, Systems, Languages, and Applications (**OOPSLA**), Indianapolis, IN, October 26-31, 2013.
- **X. Yang**, D. Frampton, S. M. Blackburn, and A. L. Hosking, "**Barriers Reconsidered, Friendlier Still!**", in Proceedings of the 2012 International Symposium on Memory Management (**ISMM**) , Beijing, China, June 15-16, 2012.
- H. Esmailzadeh, T. Cao, **X. Yang**, S. M. Blackburn, and K. S. McKinley, "**What is Happening to Power, Performance, and Software?,**", **IEEE Micro**, vol. 32, pp. 110-121, 2012.
- H. Esmailzadeh, T. Cao, **X. Yang**, S. M. Blackburn, and K. S. McKinley, "**Looking Back and Looking Forward: Power, Performance, and Upheaval,**", **Communications of the ACM**, vol. 55, iss. 7, pp. 105-114, 2012.
- **X. Yang**, S. M. Blackburn, D. Frampton, J. B. Sartor, and K. S. McKinley, "**Why Nothing Matters: The Impact of Zeroing**", in Proceedings of the 2011 ACM SIGPLAN Conference on Object-Oriented Programming, Systems, Languages, and Applications (**OOPSLA**), Portland, OR, October 22-27, 2011.
- H. Esmailzadeh, S. M. Blackburn, T. Cao, **X. Yang**, and K. S. McKinley, "**Looking Back on the Language and Hardware Revolution: Measured Power, Performance, and Scaling**", in Proceedings of the 16th International Conference on Architectural Support for Programming Languages and Operating Systems (**ASPLOS**), Newport Beach, CA, USA, March 5-11, 2011.

Research Experience

- **Australian National University** Canberra, Australia
Ph.D. Computer Science *Oct. 2011 - Date*
 - Designed and implemented Elfen, a scheduler for improving the datacenter CPU utilization by co-running latency critical workloads and batch workloads on SMT CPUs. **USENIX ATC 2016**
 - Designed and implemented SHIM, a continuous profiler that samples at resolutions as fine as 15 cycles; three to five orders of magnitude finer than current continuous profilers. SHIM is an open source project. **ISCA 2015**
 - Evaluated overheads of a range of useful barriers used by managed language runtime systems on a range of modern hardware and workloads. **ISMM 2012**

- **Australian National University**

Canberra, Australia

- *Mphil Computer Science*

May 2009 - March 2011

- Analyzed the overhead of zero initialization, discovered the cost is surprisingly high, and proposed three better designs to reduce the direct and indirect zeroing costs simultaneously. The three new zeroing approaches are in Jikes RVM now. **OOPSLA 2011**
- Analyzed measured chip power and performance on five process technology generations executing 61 diverse benchmarks with a rigorous methodology. **ASPLOS 2011**

- **University of Electronic Science and Technology of China**

Chengdu, China

- *B.S. Computer Science*

Sept. 2004 - July 2008

- Ported the RTEMS, a single address space RTOS, to the L4 Microkernel, and evaluated the performance. Parts of the project, PXA255 and Gumstix BSP, were contributed to the RTEMS.

Working Experience

- **Terrain Data**

Sydney, Australia

- *Engineer*

2017 - Date

- Help to create the first graphical algorithm management system, with which non-tech users can manage, annotate, and search their structural data.

Selected Open Source Projects

- **Elefen**

<https://github.com/yangxi/elfen>

- Elefen is scheduler for co-running batch jobs with latency critical jobs. I designed, implemented, and maintain the project.

- **SHIM**

<https://github.com/yangxi/SHIM>

- SHIM is an open source high frequency continuous profiler. I designed, implemented, and maintain the project.

- **RTEMS on the Cortex-M3 CPU of OMAP4 SoC** <https://github.com/yangxi/omap4m3>

- The OMAP4 SoC is an interesting platform for researchers who are interested in heterogeneous systems because it has two big Cortex-A9 cores and two small Cortex-M3 cores. However, because there is no open source RTOS running on the small cores, it is hard to prototype ideas on the platform. This project ported the RTEMS to the small M3 cores. The RTEMS is loaded by and communicates with the Linux kernel running on the big A9 cores.

Community Services

- **Program Committee Member**

- OOPSLA'13 AEC
- PLDI'16 ERC
- OOPSLA'16 AEC