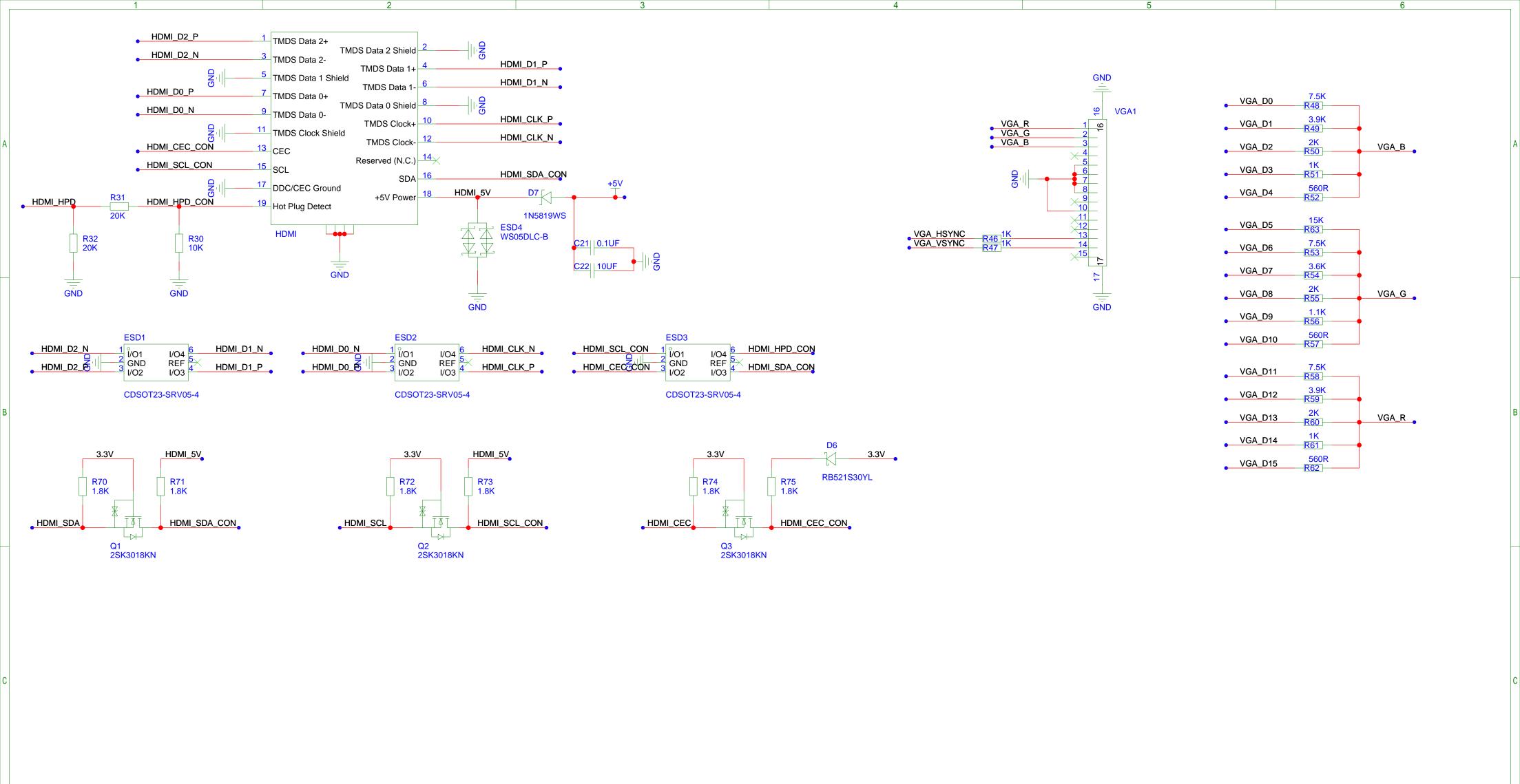


U10.12 SDRAM_CLK SDRAM_DM1 IO,DIFFIO_L5p,(DQ1L)/(_) IO,DIFFIO_L5n,(DQ1L)/(_) SDRAM_D15 J6 K6 SD_D1 SD_CD IO,DIFFIO_L6p IO,DIFFIO_L6n IO,DIFFIO_L7p SDRAM_A6 SDRAM_A8 IO,DIFFIO_L7n,(DQ1L)/(_) SDRAM_A4 SIO,DIFFIO_L8p,(DQS1L/CQ1L#,DPCLK1)/(DQS1L/CQ1L#,DPCLK1) SDRAM_A5 IO,DIFFIO_L8n,(DQ1L)/(_) SD_D0 SD_D2 SD_D3 IO,VREFB2N0 IO,DIFFIO_L9p,(DQ1L)/(_) IO,DIFFIO_L9n,(DQ1L)/(_) T_PAD SD_CLK SMG_STCP IO,RUP1,(DQ1L)/(_) IO,RDN1,(DQ1L)/(_) IO,(DQS3L/CQ3L#)/(DQS3L/CQ3L#) RX IO,DIFFIO_L10p,(DQ1L)/(_)
IO,DIFFIO_L10n,(DM1L/BWS#1L)/(_)

<u>U10.3</u> HDMI_CLK_N HDMI_CLK_P IO,DIFFIO_R4n,(INIT_DONE) IO,DIFFIO_R4p,(CRC_ERROR) IO_F13 HDMI_D0_N IO,DIFFIO_R3n,(nCEO)
IO,DIFFIO_R3p,(CLKUSR) HDMI_D0_P IO_B16 IO,(DQS0R/CQ1R,DPCLK5)/(DQS0R/CQ1R,DPCLK5) IO F14 IO,VREFB6N0 IO,DIFFIO_R2n HDMI_D1_N HDMI_D1_P IO,DIFFIO_R2p G11 IO_G11 IO,DIFFIO_R1n,(DQS2R/CQ3R)/(DQS2R/CQ3R)
IO,DIFFIO_R1p

U10.2 N13 VGA_D2 M12 D4_B IO IO L12 VGA_D6 K12 VGA_D10 IO N14 VGA_D3 IO,RUP3,(DM1R/BWS#1R)/(_) iO,RDN3,(DQ1R)/(_) D3_B IO,DIFFIO_R11n,(DQS3R/CQ3R#)/(DQS3R/CQ3R#) R16 D3_R IO,DIFFIO_R11p,(DQ1R)/(_) K11 VGA_D8 N16 D4_G N15 D4_R IO,DIFFIO_R10n,(DQ1R)/(_) IO,DIFFIO_R10p,(DQ1R)/(_) L14 VGA_D9 <u>____</u> VGA_D7 IO,(DQ1R)/(_) HDMI_SDA IO,DIFFIO_R9n,(DQ1R)/(_) RST IO,DIFFIO_R9p VGA_D11 VGA_D13 IO,DIFFIO_R8n,(DQ1R)/(_)
IO,DIFFIO_R8p,(DQS1R/CQ1R#,DPCLK4)/(DQS1R/CQ1R#,DPCLK4) J16 HDMI_SCL IO,DIFFIO_R7n,(DEV_OE) J15 HDMI_CEC IO,DIFFIO_R7p,(DEV_CLRn) IO,DIFFIO_R6n,(DQ1R)/(_) IO,DIFFIO_R6p IO,(DQ1R)/(_)

Schematic	ΔΙ	Altera A406 KFB			odate D	ate				
Scriematic	Al		IXI D	С	reate D	ate				
Page				P	art Num	ber				
Drawed										
Reviewed		A406_KFB								
		VER	SIZE	P	AGE	2	OF	4		
思妙电	上子									
4										



Cohomotic		Altera A406	Update	Date						
Schematic		Allera A400	Create	Create Date						
Page				Part Nu	ımber					
Drawed					·					
Reviewed			A406_KFB							
		VER	SIZE	PAGE	3	OF	4			
思妙	电子									
4		1	5			6				

