

**Birkbeck  
(University of London)**

**MSc Examination**

**Department of Computer Science and Information Systems**

**COMPUTER SYSTEMS (COIY060H7)**

**CREDIT VALUE: 15 credits**

**Date of examination: 2/6/2016**

**Duration of paper: 14:30–16:30**

There are five questions in this paper; each of them is compulsory and worth 20 marks.  
The paper is not prior-disclosed.  
The use of electronic calculators is not permitted.

1. Consider the computation  $(m1 + [r1]) * (m2 * r2)$  where  $m1, m2$  denote the contents of memory locations,  $[r1]$  denotes the content of a memory location whose address is in the register and  $r2$  denotes the content of the register.
  - (a) Write assembly code typical of RISC machines for this computation. Use at most two operands for each instruction (e.g., `ADD r1 r2` for `r1 <- r1 + r2`) and use at most three registers altogether. (8 marks)
  - (b) Identify the dependencies (both true and false) in your code. (4 marks)
  - (c) List the various addressing modes in your code. (4 marks)
  - (d) Describe an addressing mode suitable for supporting VM (virtual memory). (4 marks)

(Subtotal: 20 marks)
  
2. Consider a superscalar processor with two functional units for each of the five pipeline stages: fetch, decode, register read, execute, write back.
  - (a) Show the pipeline activity when your code for the previous question is executed using the in-order-issue/in-order-completion policy. State explicitly any additional assumptions you made about processing the instructions. (10 marks)
  - (b) Remove the false dependencies from your code by using the register renaming technique. (4 marks)
  - (c) Show the pipeline activity when the modified code is executed by using the out-of-order issue/out-of-order completion policy. (6 marks)

(Subtotal: 20 marks)
  
3.
  - (a) Explain the main idea of multiprogramming. (5 marks)
  - (b) Describe the overhead related to process switching. (5 marks)
  - (c) Consider a computing centre (where there are dozens of hard disks). There are 100 jobs and each of them needs 2 seconds CPU time and each spends 1 second in I/O wait (for reading data from one of the hard disks). Compute the optimal overall runtime for the 100 jobs if they run
    - i. uniprogrammed, (3 marks)
    - ii. multiprogrammed. (7 marks)

You can assume that the jobs are independent of each other.

(Subtotal: 20 marks)
  
4. (a) Explain the concepts of race condition, critical section (or region) and mutual exclusion in the context of interprocess communication. (6 marks)

- (b) Briefly describe the technique called ‘strict alternation’ and discuss whether it provides a satisfactory solution to the critical section problem. (6 marks)
- (c) Consider the following pseudo-code:

```
shared boolean flag[2];
flag[0] = flag[1] = FALSE;
proc(int i) {
    while (TRUE) {
        while (flag[(i+1) mod 2] == TRUE);    // loop till FALSE
        flag[i] = TRUE;
        critical_section;
        flag[i] = FALSE;
    }
}
```

Explain whether this code solves the critical section problem for two processes. (8 marks)

(Subtotal: 20 marks)

5. (a) Define deadlock and describe three methods for dealing with deadlock. (8 marks)
- (b) A system has four processes  $p_1, p_2, p_3, p_4$  and three types of dedicated resources  $R_1, R_2, R_3$ . The existence vector is  $E = (3, 2, 2)$ .
- Process  $p_1$  holds one unit of  $R_1$  and requests one unit of  $R_2$ ;
  - Process  $p_2$  holds two units of  $R_2$  and requests two units of  $R_1$  and one unit of  $R_3$ ;
  - Process  $p_3$  holds one unit of  $R_1$  and requests one unit of  $R_2$ ;
  - Process  $p_4$  holds two units of  $R_3$  and requests one unit of  $R_1$ .
- i. Compute the availability vector. (2 marks)
  - ii. Explain whether the system is deadlocked. (4 marks)
  - iii. Determine whether this state of the system is safe. (6 marks)

(Subtotal: 20 marks)