

File Edit Tools Syntax Buffers Window Help



// Created by prof. Mingu Kang @VVIP Lab in UCSD ECE department

// Please do not spread this code without permission

module sfp (out, in, thres, acc, relu, clk, reset);

parameter bw = 8;

parameter psum_bw = 16;

input clk;

input acc;

input relu;

input reset;

input signed [bw-1:0] in;

input signed [psum_bw-1:0] thres;

output signed [psum_bw-1:0] out;

reg signed [psum_bw-1:0] psum_q;

// Your code goes here

always @ (posedge clk) begin

if (reset) begin

psum_q <= 16'b0000000000000000;

end

else if (acc == 1'b1) begin

psum_q <= psum_q + in;

end

else if (relu == 1'b1 && (psum_q < thres)) begin

psum_q <= 16'b0000000000000000;

end

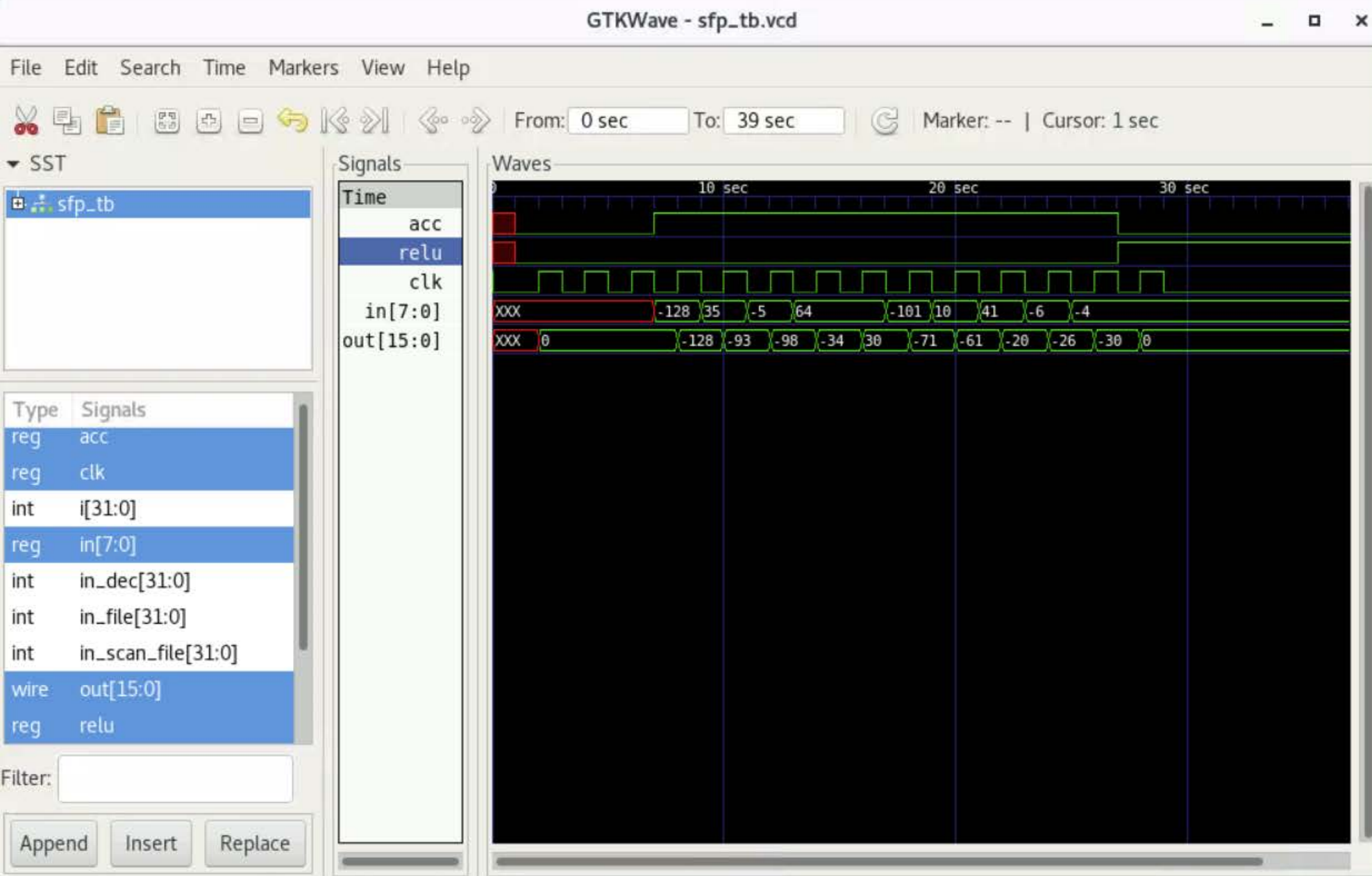
end

assign out = psum_q;

endmodule

~

~



7
CENTOS