

# **Lecture 15: MOSFET Review**

VE311 Electronic Circuits

Xuyang Lu 2023 Summer



# **Recap of Last Lecture**



• Review for the midterm

### **Topics to Be Covered**

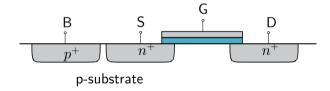


- Review for the midterm (MOSFET part)
- Source Follower

### **NMOS FET**

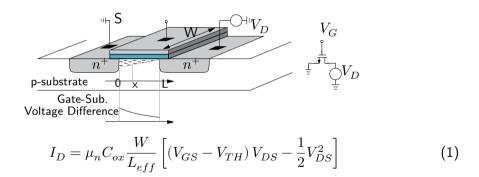


Reading: Razavi Chapter 2.



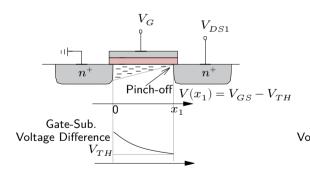
# NMOS I-V Characteristics (Triode)

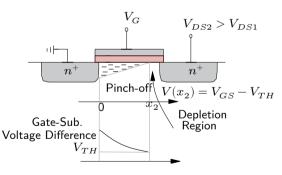




### **Saturation Region**







### **Saturation Region**



$$I_{D} = \frac{1}{2} \mu_{n} C_{ox} \frac{W}{L'} \left( V_{GS} - V_{TH} \right)^{2}$$
 (2)

- ullet  $I_D$ : constant along channel
- L': the point at which  $Q_d$  drops to zero
- ullet  $V_{GS}$   $\!\!\!\!-V_{TH}$ : the overdrive voltage
- Electron velocity  $(v=I_D/Q_d)$  becomes tremendously high at the pinch off point  $(Q_d \to 0)$ , such that electrons shoot through the depletion region and arrive at the drain terminal.

# **Channel-Length Modulation**



$$r_o = \frac{\partial V_{DS}}{\partial I_D} = 1 / \frac{\partial I_D}{\partial V_{DS}} \tag{3}$$

$$= \frac{1}{\frac{1}{2}\mu_n C_{ox} \frac{W}{L} (V_{GS} - V_{TH})^2 \cdot \lambda}$$
 (4)

$$pprox rac{1}{I_D \cdot \lambda}$$
 (5)

### **Body Effect**



$$V_{TH} = V_{TH0} + \gamma(\sqrt{|2\Phi_F + V_{SB}|} - \sqrt{|2\Phi_F|})$$
 (6)

$$\Phi_F = \frac{kT}{q} \ln \frac{N_{sub}}{n_i} \tag{7}$$

$$\gamma = \frac{\sqrt{2q\epsilon_{Si}N_{sub}}}{C_{ox}} \tag{8}$$

$$I_D = \frac{1}{2} \mu_n C_{ox} \frac{W}{L'} \left( V_{GS} - V_{TH} \right)^2 \tag{9}$$

I will ask you how the body shall be connected.

#### **NMOS Formula Table**



NMOS Transistor Mathematical Model Summary

The following equations represent the complete model for the i-v behavior of the NMOS transistor.

For all regions,

$$K_n = K'_n \frac{W}{L} \quad K'_n = \mu_n C''_{ox} \quad i_G = 0 \quad i_B = 0$$
 (10)

Cut off region,

$$i_D = 0 \quad \text{for } v_{GS} \le V_{TN} \tag{11}$$

Triode region,

$$i_D = K_n \left( v_{GS} - V_{TN} - \frac{v_{DS}}{2} \right) v_{DS} \quad \text{ for } v_{GS} - V_{TN} \ge v_{DS} \ge 0$$
 (12)

#### **NMOS Formula Table**



Saturation region,

$$i_D = \frac{K_n}{2} (v_{GS} - V_{TN})^2 (1 + \lambda v_{DS})$$
 for  $v_{DS} \ge (v_{GS} - V_{TN}) \ge 0$  (13)

Threshold voltage,

$$V_{TN} = V_{TO} + \gamma \left( \sqrt{v_{SB} + 2\phi_F} - \sqrt{2\phi_F} \right) \tag{14}$$

 $V_{TN}>0$  for enhancement-mode NMOS transistors.Depletion-mode NMOS devices can also be fabricated,and  $I_{TN}\leq 0$  for these transistors.

#### **PMOS Formula Table**



NMOS Transistor Mathematical Model Summary

The following equations represent the complete model for the i-v behavior of the NMOS transistor.

For all regions,

$$K_p = K_p' \frac{W}{L} \quad K_p' = \mu_p C_{ox}^{"} \quad i_G = 0 \quad i_B = 0$$
 (15)

Cut off region,

$$i_D = 0 \quad \text{for } V_{GS} \ge V_{TP} \tag{16}$$

Triode region,

$$i_D = K_p \left( v_{GS} - V_{TP} - \frac{v_{DS}}{2} \right) v_{DS} \quad \text{for } 0 \le |v_{DS}| \le |v_{GS} - V_{TP}|$$
 (17)

### **PMOS Formula Table**



Saturation region,

$$i_{D} = \frac{K_{p}}{2} \left( v_{GS} - V_{TP} \right)^{2} \left( 1 + \lambda |v_{DS}| \right) \quad \text{ for } |v_{DS}| \ge |v_{GS} - V_{TP}| \ge 0 \qquad \text{(18)}$$

Threshold voltage,

$$V_{TN} = V_{TO} + \gamma \left( \sqrt{v_{SB} + 2\phi_F} - \sqrt{2\phi_F} \right) \tag{19}$$

For the enhancement-mode PMOS transistor,  $V_{TP} < 0$ . Depletion-mode PMOS devices can also be fabricated;  $I_{TP} \geq 0$  for these devices.

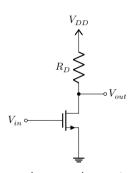
# **Small Signal Model**

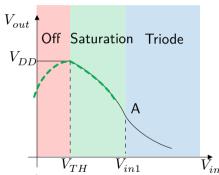


$$g_m = \mu_n C_{ox} \frac{W}{L} \left( V_{GS} - V_{TH} \right) \left( 1 + \lambda V_{DS} \right) \qquad \text{(20)}$$
 
$$r_o = \frac{\partial V_{DS}}{\partial I_D} = 1 / \frac{\partial I_D}{\partial V_{DS}} \approx \frac{1}{I_D \cdot \lambda} \qquad \text{(21)}$$
 
$$v_{in} \circ \cdots \qquad \qquad v_{gm} \cdot v_{gs} > r_o \qquad g_{mb} \cdot v_{sb}$$

#### **Common-Source**



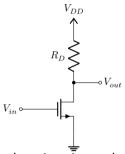




I will ask you how to determine the DC operation region.

For example, What is the value of  ${\cal V}_{in1}$ 

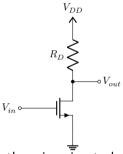




- Gain, bias, and small-signal model
- Input and output impedance
- Advantages:
  - Large input impedance
  - Large gain
  - Widely used
  - No current through Gate, easily biased.

Of course, there is going to be a question related to a common source amplifier and its derivations.



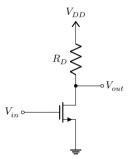


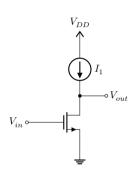
#### • Cons:

- ullet  $R_D$  and  $M_1$  process variation
- (Operating condition?) Swing limited by  ${\cal R}_D$
- Output DC voltage cannot be chosen arbitrarily
- Output impedance and gain tradeoff

Of course, there is going to be a question related to a common source amplifier and its derivations.







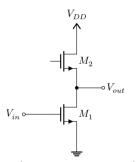
- Current Source, provide high impedance
- Problem: there is no ideal current source in the world.

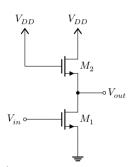




- DC biased NMOS Load: Good approximation of current source.
- Pros: Less process variation.
- Cons: Additional Bias to be generated, Body effect and therefore Linearity problem.

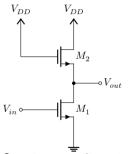


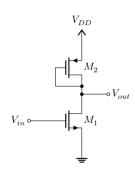




- $\bullet$  No need to generate an additional voltage.  $M_2$  is always in saturation.
- Still, output impedance and linearity

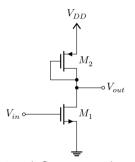


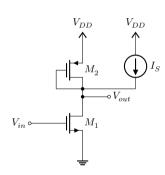




- PMOS to improve linearity
- No more body effect.
- Cons: Now how is the DC defined? Limited output swing.
- Large input capacitance.

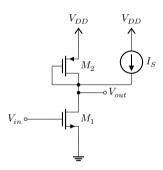


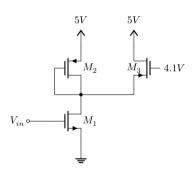




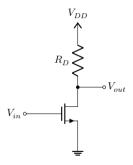
- Additional Current path
- Pros: Improve voltage swing, reduce input cap size.





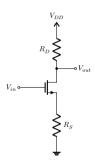






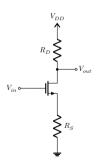
• Pros: Linearity

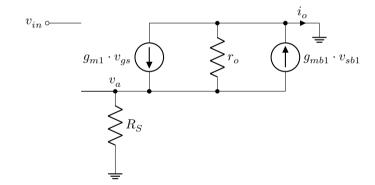
• Cons: Gain



$$Gain = G_m \cdot R_{out} \tag{22}$$









$$i_o = \frac{-v_a}{R_S} \tag{23}$$

$$(v_{in} - v_a) g m_1 + i_o = \frac{v_a}{r_{o1}} + v_a g m b_1$$
 (24)

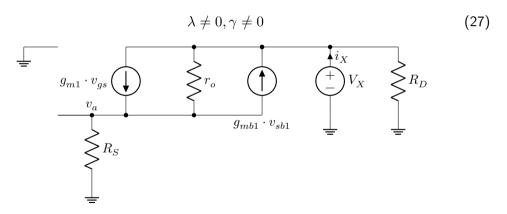
$$G_m = \frac{i_o}{v_{in}} = \frac{-gm_1r_{o1}}{R_S + r_{o1} + (g_{m_1} + g_{mb_1})r_{o1}R_S} \approx -\frac{1}{R_S}$$
(25)

If.

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$$(gm_1 + gmb_1) r_{01}R_S \gg r_{01} \text{ and } R_S$$
 (26)





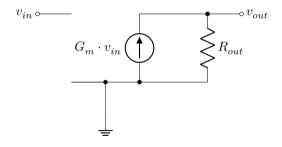


$$i_x = \frac{v_a}{R_S} \tag{28}$$

$$v_a g m_1 + v_a g m b_1 + \frac{v_a - v_x}{r_o} + i_x = 0 {29}$$

$$R_{\rm out} \ = R_{\rm out1\ 1} \parallel R_D = \left[ R_S + r_{o1} + \left( g m_1 + g m b_1 \right) r_{o1} R_S \right] \parallel R_D \approx R_D \eqno(30)$$







$$A_v = \frac{v_{\text{out}}}{v_{\text{in}}} = G_m R_{\text{out}} \tag{31}$$

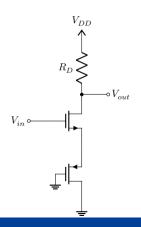
$$= \frac{-gm_1r_{o1}}{R_S + r_{o1} + (gm_1 + gmb_1)r_{o1}R_S} \cdot \frac{\left[R_S + r_{o1} + (gm_1 + gmb_1)r_{o1}R_S\right]R_D}{\left[R_S + r_{o1} + (gm_1 + gmb_1)r_{o1}R_S\right] + R_D}$$
(32)

$$\approx -\frac{R_D}{R_S} \quad \text{If } \left(gm_1 + gmb_1\right)r_{01} \text{, the intrinsic gain, is large.} \tag{33}$$

### **Example**



Assuming  $\lambda = \gamma = 0$ , calculate the small signal voltage gain of the circuit below.



$$G_m = -\frac{1}{\frac{1}{gm_1} + \frac{1}{gm_2}} \tag{34}$$

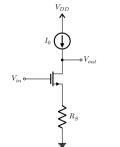
$$R_{\text{out}} = R_D$$
 (35)  
 $A_v = G_m R_{out}$  (36)

$$A_v = G_m R_{out} (36)$$

### **E**xample



Calculate the small signal voltage gain of the circuit below.



$$G_m = \frac{-gm_1r_{01}}{r_{01} + R_S + (gm_1 + gmb_1)r_{01}R_S}$$
(37)

$$R_{out} = r_{o1} + R_S + \left(g m_1 + g m b_1\right) r_{o1} R_S \tag{38} \label{eq:38}$$

$$A_v = G_m R_{out} = -g m_1 r_{o1} (39)$$

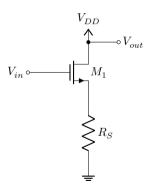
- $I_0$  is ideal current source o Voltage across  $R_S$  is constant o  $M_1$  source shorted to ground
- $R_D$  replaced by current source o Nonlinearity issue arises again again o

### **Ideal Amplifier**



• For driving a low impedance load, source follower, as a buffer, provides no gain but large input impedance and low output impedance.





$$V_{DD} - V_{\text{out}} = V_{\text{in}1} - V_{\text{out}} - V_{TH}$$
 (40)  
 $\rightarrow V_{in1} = V_{DD} + V_{TH}$  (41)



$$\mathbf{V}_{\mathsf{in}} < \mathbf{V}_{TH} o M_1 \; \mathsf{Off}$$
 (42)

$$V_{out} = 0 (43)$$

$$V_{\text{in }1} > V_{\text{in }} > V_{TH} \rightarrow M_1 \text{ in Saturation}$$
 (44)

$$R_{S} \frac{1}{2} \mu_{n} C_{ox} \frac{W}{L} \left( V_{in} - V_{out} - V_{TH} \right)^{2} = V_{out}$$
 (45)

$$V_{\mathsf{in}} > V_{\mathsf{in} \ 1} \to M_1 \ \mathsf{in} \ \mathsf{Triode}$$
 (46)

$$R_{S}\mu_{n}C_{\text{ox}} \frac{W}{L} \left[ \left( V_{\text{in}} - V_{\text{out}} - V_{TH} \right) \left( V_{DD} - V_{\text{out}} \right) - \frac{1}{2} \left( V_{DD} - V_{\text{out}} \right)^{2} \right] = V_{\text{out}} \quad (47)$$



$$V_{\mathsf{in}1} > V_{\mathsf{in}} > V_{TH} \to M_1 \text{ in Saturation}$$
 (48)

$$R_{S} \frac{1}{2} \mu_{n} C_{ox} \frac{W}{L} \left( V_{\text{in}} - V_{\text{out}} - V_{TH} \right)^{2} = V_{\text{out}}$$
 (49)

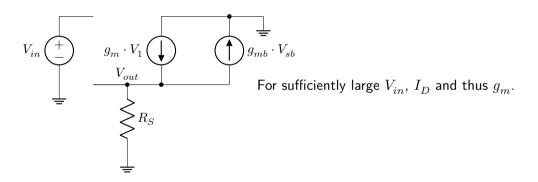
$$R_{S} \frac{1}{2} \mu_{n} C_{ox} \frac{W}{L} 2 \left( V_{in} - V_{out} - V_{TH} \right) \left( 1 - \frac{\partial V_{out}}{\partial V_{in}} - \frac{\partial V_{TH}}{\partial V_{in}} \right) = \frac{\partial V_{out}}{\partial V_{in}}$$
 (50)

$$R_{S}\mu_{n}C_{ox}\frac{W}{L}\left(V_{\mathsf{in}}-V_{\mathsf{out}}-V_{TH}\right)\left(1-\frac{\partial V_{\mathsf{out}}}{\partial V_{\mathsf{in}}}-\frac{\partial V_{TH}}{\partial V_{\mathsf{out}}}\frac{\partial V_{\mathsf{out}}}{\partial V_{\mathsf{in}}}\right)=\frac{\partial V_{\mathsf{out}}}{\partial V_{\mathsf{in}}}\tag{51}$$

$$A_v = \frac{g_m R_S}{1 + g_m R_S (1 + \eta)} = \frac{g_m R_S}{1 + (g_m + g_{mb}) R_S} \approx \frac{1}{1 + \eta}$$
 (52)









$$G_m = g_m \tag{53}$$

$$R_{out} = R_S \parallel \left(\frac{1}{gm + g_{mb}}\right) \tag{54}$$

$$A_v = \frac{gmR_S}{1 + (gm + g_{mb})R_S} \approx \frac{1}{1 + \eta}$$
 (55) If  $(g_m + g_{mb})R_S \gg 1$ 

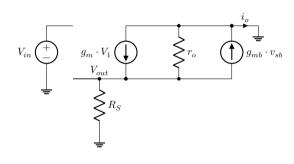


$$\lambda \neq 0, \gamma \neq 0 \tag{56}$$

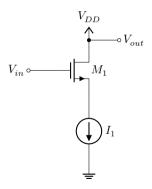
$$G_m = g_m \tag{57}$$

$$R_{out} = r_o \parallel R_S \parallel \left(\frac{1}{gm + g_{mh}}\right) \quad (58)$$

$$A_V = \frac{g_m r_o R_S}{r_o + R_S + (g_m + g_{mb}) r_o R_S} \approx \frac{1}{1 + \eta} \tag{59}$$
 If  $(g_m + g_{mb}) r_0 R_S \gg r_0$  and  $R_S$ 







$$A_v = \frac{1}{1+\eta} \tag{60} \label{eq:force_approx}$$
 If  $\gamma = 0, A_v = 1$ 



$$\frac{1}{2}\mu_n C_{ox} \frac{W}{L} \left( V_{\text{in}} - V_{\text{out}} - V_{TH} \right)^2 = I_1$$
 (61)

$$\frac{1}{2}\mu_{n}C_{\text{ox}}\frac{W}{L}2\left(V_{\text{in}}-V_{\text{out}}-V_{TH}\right)\left(1-\frac{\partial V_{\text{out}}}{\partial V_{\text{in}}}-\frac{\partial V_{TH}}{\partial V_{\text{in}}}\right)=0\tag{62}$$

$$\mu_n C_{\text{ox}} \frac{W}{L} \left( V_{\text{in}} - V_{\text{out}} - V_{TH} \right) \left( 1 - \frac{\partial V_{\text{out}}}{\partial V_{\text{in}}} - \frac{\partial V_{TH}}{\partial V_{\text{out}}} \frac{\partial V_{\text{out}}}{\partial V_{\text{in}}} \right) = 0 \tag{63}$$

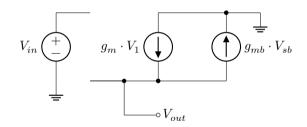




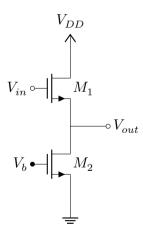
$$G_m = g_m \tag{64}$$

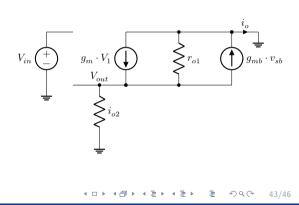
$$R_{out} = \frac{1}{g_m + g_{mb}} \tag{65}$$

$$A_v = \frac{1}{1+\eta}$$
 If  $\gamma = 0, A_v = 1$  (66)











$$G_m = g_{m1} \tag{67}$$

$$R_{out} = r_{o1} \parallel r_{o2} \parallel \left( \frac{1}{g_{m_1} + g_{mb_1}} \right)$$
 (68)

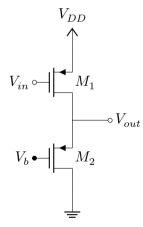
$$A_v = \frac{g_m r_{o1} r_{o2}}{r_{o1} + r_{o2} + (g_m + g_{mb}) r_{o1} r_{o2}}$$
 (69)

If  $r_{o1}$  and  $r_{o2}$  large,  $A_v$  is linear.



# SF with Current Source ( $V_{SB} = 0$ )





# SF with Current Source ( $V_{SB}=0$ )



$$G_m = g_{m1} \tag{70}$$

$$R_{out} = r_{o1} \parallel r_{o2} \parallel \frac{1}{g_{m1}}$$
 (71)

$$A_V = \frac{g_{m1}r_{o1}r_{o2}}{r_{o1} + r_{o2} + g_{m1}r_{o1}r_{o2}} \tag{72}$$

 The sacrifice here is the higher output impedance due to smaller mobility of holes relative to electrons.