Question 1

(a)

Case (i):

For the positive half cycle:

The diode D_1 is in forward bias condition and the diode acts as a short circuited. The diode D_2 is in reverse bias condition and the diode acts as an open circuited. Therefore the overall circuit is conducting. Ideally the diode voltage is treated 0 V. For positive half cycle, the output voltage is equal to input voltage.

Case (ii):

For the negative half cycle:

The diode D_1 is in reverse bias condition and the diode acts as an open circuited. The diode D_2 is in forward bias condition and the diode acts as a short circuited. Therefore the overall circuit is conducting. Ideally the diode voltage is treated 0 V. For negative half cycle, the output voltage is equal to input voltage.

The output waveform is shown in Figure 5.

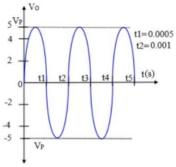


Figure 5

Therefore, the positive peak value is +5 V

_

Therefore, the negative peak value is -5 V

(b)

Case (i)

For the positive half cycle $v_i > 0$, the diode D_i is in reverse bias condition and the diode acts as a open circuit.

The total input voltage is flows as output voltage.

Therefore, the output voltage is equal to input voltage.

Case (ii):

For the negative half cycle $v_i < 0$, the diode D_i is in forward bias condition and the diode acts as a short circuit.

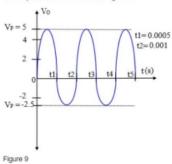
The circuit acts a voltage divider circuit.

Find the total output voltage.

$$\begin{aligned} v_o &= \frac{1 \text{ k}\Omega}{1 \text{ k}\Omega + 1 \text{ k}\Omega} v_i \\ &= \frac{1 \text{ k}\Omega}{2 \text{ k}\Omega} (5 \text{ V}) \\ &= 0.5 (5) \\ v_o &= 2.5 \text{ V} \end{aligned}$$

Therefore, the output voltage is -2.5 V.

The output waveform is shown in Figure 9.



Therefore, the positive peak value is +5 V.

Therefore, the negative peak value is $\boxed{-2.5 \text{ V}}$.

(c)

Case (i):

For the positive half cycle $v_{_{j}}>0$:

The diode D_1 is in reverse bias condition and the diode D_2 is in forward bias condition.

Hence, the diode $D_{\rm i}$ acts as a open circuited to ground and $D_{\rm 2}$ acts as short circuit.

Find the voltage across the $1\,k\Omega$ resistor.

Current flowing $1\,k\Omega$ resistor is $1\,mA$.

Voltage across $1 \, \mathrm{k}\Omega$ resistor is $\, V_{_{\! O}} = \left(1 \, \mathrm{k}\Omega\right) \left(1 \, \mathrm{mA}\right) = 1 \, \mathrm{V}$.

Case (ii):

For the negative half cycle $v_i < 0$:

The diode $D_{\rm i}$ is in forward bias condition and the diode $D_{\rm i}$ is in reverse bias condition. Hence, the diode $D_{\rm i}$ acts as a short circuited to ground and $D_{\rm i}$ acts as open circuit.

Find the voltage across the $\,1\,k\Omega\,$ resistor.

Current flowing $\,1\,k\Omega\,$ resistor is $\,1\,mA$.

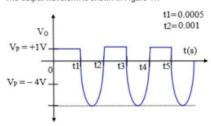
Voltage across $1 \text{ k}\Omega$ resistor is $V_o = V_i + 1 \text{ V}$.

$$V_o = -5 + 1 \text{ V}$$

 $V_o = -4 \text{ V}$

Voltage across $1\,\mathrm{k}\Omega$ resistor is $\,V_{_{o}} \equiv -4\,\mathrm{V}$.

The output waveform is shown in Figure 11.

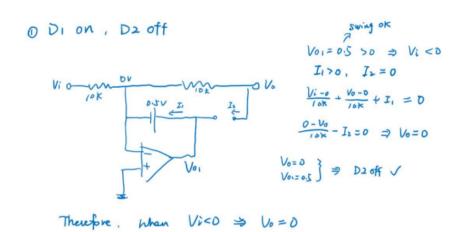


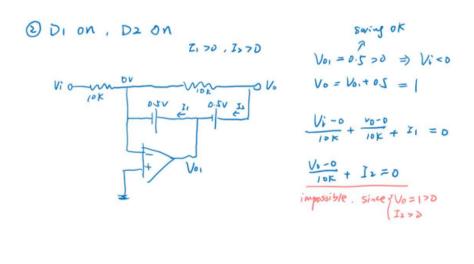
Figure

Therefore, the positive peak value is $\boxed{+1 \, V}$.

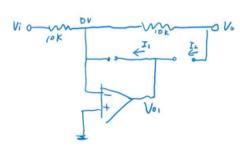
Therefore, the negative peak value is $\boxed{-4\ V}$.

Question 2





3 Di off, D2 off



$$I_{1} = I_{2} = 0$$

$$V_{01} < 0.5$$

$$\frac{V_{i-0}}{I_{0K}} + \frac{V_{0-0}}{I_{0K}} = 0$$

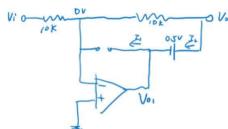
$$\Rightarrow V_{0} = -V_{i}$$

$$\frac{U_{0} - V_{0}}{I_{0K}} - I_{2} = 0$$

$$\Rightarrow V_{0} = 0$$

Therefor, when Vi=0 > Us=0

@ Di off, D2 on



Vi o violation Vs
$$\frac{V_i - \delta}{IDK} + \frac{V_0 - \delta}{IDK} = \delta$$

$$\Rightarrow V_0 = -V_i$$

$$\frac{O - V_0}{IDK} - I_2 = \delta$$

$$\Rightarrow V_0 = -IOK \cdot I_2$$

$$\pm 3V \in Wing$$
 $3 \in V_0 \in D$
 $V_0 = V_0 - 0.5 = -V_0 - 0.5 < 0$
 $-0.5 < V_1 \le 2.5$

. Vi= > > Vo= 0 swip ok

$$\frac{V_i - \delta}{IDK} + \frac{V_0 - \delta}{IDK} = \delta$$

$$\Rightarrow V_0 = -V_0$$

$$\frac{O - V_0}{IDK} - I_2 = \delta$$

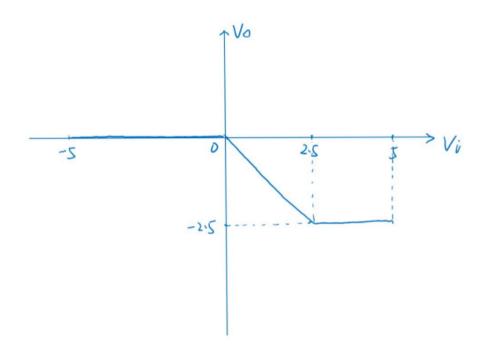
$$\Rightarrow V_0 = -IOK \cdot I_2 < 0$$

$$\Rightarrow V_0 \Rightarrow V_0 < \delta$$

Therefore, when
$$0 < Vi \le 2.5$$

Therefore, when $0 < Vi \le 2.5$
 $\Rightarrow Vo = -Vi$

When $1.5 < Vi \le 5$, the opening output saturates $\Rightarrow Vo_1 = -3V$
 $\Rightarrow V_6 = -2.5V$



Question 3

