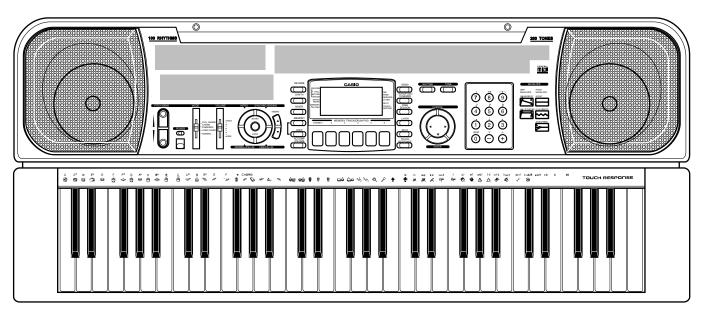
# **CASIO**<sub>®</sub>

# Service Manual

(without price)

# **CTK-611**



CTK-611

**ELECTRONIC KEYBOARD** 

# **CONTENTS**

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#### **SPECIFICATIONS**

**GENERAL** 

Keyboard: 61 standard-size keys, 5 octaves (with touch response on/off)

Drum pads: 6

Tones: 200 (128 General MIDI, 32 synthesized, 8 drum, 32 user); with layer and

split

Rhythm instrument tones: 51

Polyphony: 24 notes maximum (12 for certain tones)
Digital effects: 3 reverb types (HALL, STAGE, ROOM)

Auto accompaniment

Rhythm patterns: 100

Tempo: Variable (216 steps,  $\downarrow$  = 40 to 255)

Chords: 3 fingering methods (CASIO CHORD, FINGERED, FULL RANGE

CHORD)

Rhythm controller: START/STOP, INTRO, NORMAL/NORMAL FILL-IN, VARIATION/VARI-

ATION FILL-IN, SYNCHRO/ENDING

Accomp volume: 0 to 127 (128 steps)

Free session

Number of patterns: 100 (auto-accompaniment in accordance with selected chord progres-

sion)

Memory function

Songs: 2

Recording tracks: 6 (2 through 6 are melody tracks)

Recording methods: Real-time, step

Memory capacity: Approximately 5,200 notes (total for two songs)

Edit function: Equipped

Demo tunes:

Synthesizer function

Parameters: PCM set, amp envelope set, attack rate, release rate, pitch envelope set,

pitch, level, touch sense, pan

Mixer function

Channels: 16

Parameters: Program change number, volume, expression, pan, coarse tuning, fine

tuning, on/off/solo

MIDI: 16 multi-timbre receive, GM Level 1 standard

Other functions

Pitch bend range: 12 semitones upwards and downwards
Transpose: 25 steps (-12 semitones to +12 semitones)
Tuning: Variable (A4 = approximately 440 Hz ± 50 cents)

**Terminals** 

MIDI terminals: IN, OUT

Assignable terminal: Standard jack (sustain, sostenuto, soft, rhythm start/stop)

Headphone/Output terminal: Stereo standard jack

Output Impedance: 120  $\Omega$ 

Output Voltage: 4.5 V (RMS) MAX

Power supply terminal: 9 V DC

Power supply: Dual power supply system

Batteries: 6 D-size batteries

Battery life: Approximately 5 hours continuous operation on manganese batteries

AC adaptor: AD-5

Auto power off:

Turns power off approximately six minutes after last key operation. En-

abled under battery power only, can be disabled manually.

Speaker output: 2.5 W + 2.5 WPower consumption: 9 V = 7.7 W

Dimensions (HWD):  $93.1 \times 37.3 \times 12.0 \text{ cm} (36 \ 11/16 \times 14 \ 11/16 \times 4 \ 3/4 \text{ inches})$ 

Weight: Approximately 5.0 kg (11.0 lbs) (without batteries)

#### **ELECTRICAL**

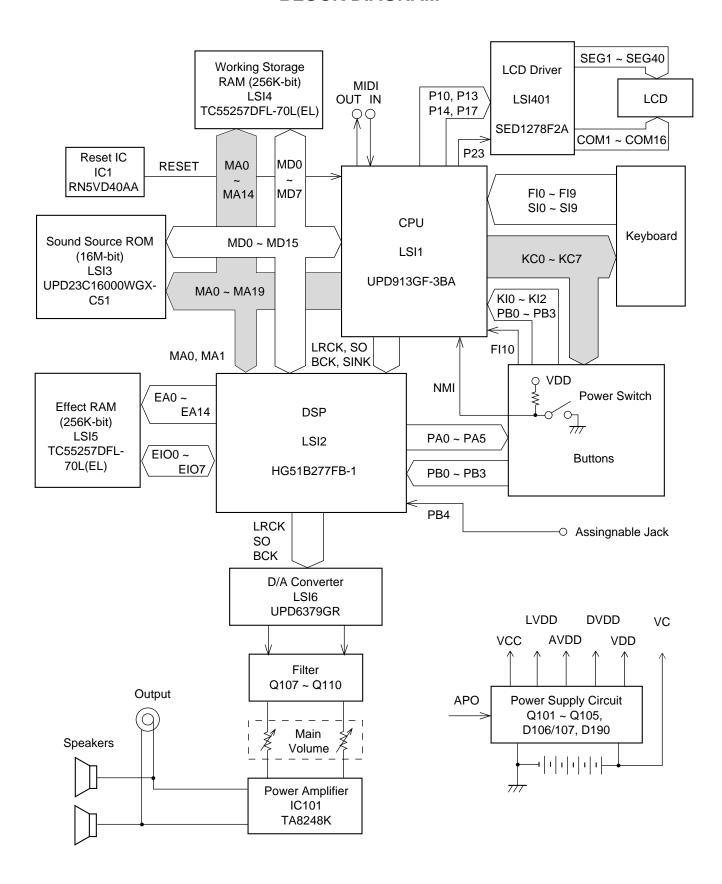
LLLOTRIOAL		
Current drain with 9 V DC:		
No sound output		340 mA $\pm$ 20 %
Maximum volume		1012 mA $\pm$ 20 %
with 12 keys from C3 to B3 pressed in Square wave tone		
Volume: maximum, Touch response: maximum		
Reverb: Hall		
Phone output level (Vrms with 8 $\Omega$ load each channel):	L-ch	90 mV $\pm$ 20 %
with key C6 pressed in Bassoon tone	R-ch	100 mV $\pm$ 20 %
Volume: maximum, Touch response: maximum		
Reverb: Hall		
Speaker output level (Vrms with 4 $\Omega$ load each channel):	L-ch	1150 mV $\pm$ 20 %
with key F5 pressed in Bassoon tone	R-ch	1000 mV $\pm$ 20 %
Volume: maximum, Touch response: maximum		
Reverb: Hall		
Output level (Vrms with 47k $\Omega$ load each channel):	L-ch	1200 mV $\pm$ 20 %
with key F5 pressed in Bassoon tone	R-ch	1000 mV $\pm$ 20 %
Volume: maximum, Touch response: maximum		
Reverb: Hall		
Minimum operating voltage:		6.3 V

# **About General MIDI**

General MIDI standardizes MIDI data for all sound source types, regardless of manufacturer. General MIDI specifies such factors as tone numbering, drum sounds, and available MIDI channels for all sound sources. This standard makes it possible for all MIDI equipment to reproduce the same nuances when playing General MIDI data, regardless of the manufacturer of the sound source.

This keyboard supports General MIDI, so it can be used to play commercially available pre-recorded General MIDI data and General MIDI data send to it from a personal computer.

## **BLOCK DIAGRAM**



# **CIRCUIT DESCRIPTION**

# **KEY MATRIX**

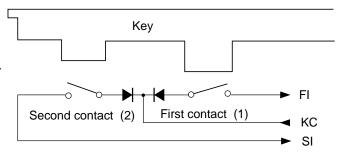
	KC0	KC1	KC2	KC3	KC4	КС	KC6	KC7
FI0	C2 (1)	C#2 (1)	D2 (1)	D#2 (1)	E2 (1)	F2 (1)	F#2 (1)	G2 (1)
SI0	C2(2)	C#2 (2)	D2 (2)	D#2 (2)	E2 (2)	F2 (2)	F#2 (2)	G2 (2)
FI1	G#2 (1)	A2 (1)	A#2 (1)	B2 (1)	C3 (1)	C#3 (1)	D3 (1)	D#3 (1)
SI1	G#2 (2)	A2 (2)	A#2 (2)	B2 (2)	C3 (2)	C#3 (2)	D3 (2)	D#3 (2)
FI2	E3 (1)	F3 (1)	F#3 (1)	G3 (1)	G#3 (1)	A3 (1)	A#3 (1)	B3 (1)
SI2	E3 (2)	F3 (2)	F#3 (2)	G3 (2)	G#3 (2)	A3 (2)	A#3 (2)	B3 (2)
FI3	C4 (1)	C#4 (1)	D4 (1)	D#4 (1)	E4 (1)	F4 (1)	F#4 (1)	G4 (1)
SI3	C4 (2)	C#4 (2)	D4 (2)	D#4 (2)	E4 (2)	F4 (2)	F#4 (2)	G4 (2)
FI4	G#4 (1)	A4 (1)	A#4 (1)	B4 (1)	C5 (1)	C#5 (1)	D5 (1)	D#5 (1)
SI4	G#4 (2)	A4 (2)	#4 (2)	B4 (2)	C5 (2)	C#5 (2)	D5 (2)	D#5 (2)
FI5	E5 (1)	F5 (1)	F#5 (1)	G5 (1)	G#5 (1)	A5 (1)	A#5 (1)	B5 (1)
SI5	E5 (2)	F5 (2)	F#5 (2)	G5 (2)	G#5 (2)	A5 (2)	A#5 (2)	B5 (2)
FI6	C6 (1)	C#6 (1)	D6 (1)	D#6 (1)	E6 (1)	F6 (1)	F#6 (1)	G6 (1)
SI6	C6 (2)	C6# (2)	D6 (2)	D#6 (2)	E6 (2)	F6 (2)	F#6 (2)	G6 (2)
FI7	G#6 (1)	A6 (1)	A#6 (1)	B6 (1)	C7 (1)			
SI7	G#6 (2)	A6 (2)	A#6 (2)	B6 (2)	C7 (2)			

# **BUTTON MATRIX**

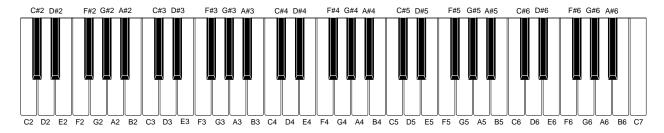
	KC0	KC1	KC2	KC3	KC4	KC5	KC6	KC7
F140	Drum Pad	Drum Pad	Pitch Bend	▼	0	6		Т
FI10	3	1	▼	Enter	9	6	+	Tone
1/10	Drum Pad	Drum Pad	Pitch Bend	Start/	0	0		Dhydhas
KI0	4	2	<b>A</b>	Stop	8	3	_	Rhythm
IZI4	Drum Pad	Tempo			7	0	0	Domo
KI1	5	Step	▼		/	2	0	Demo
KIO	Drum Pad	Manani	Tempo		4	-	4	Transpose/
KI2	6	Memory	<b>A</b>		4	5	1	Tune/MIDI

	PA0	PA1	PA2	PA3	
DDA	Full Range	latra	Touch	Devemb	
PB0	Chord	Intro	Response	Reverb	
DD4	Fingered	Normal/	Fron Consign	Accomp	
PB1	Fingered	Fill-In	Free Session	Volume	
DD2	Casia Chard	Synchro/	Lover	Synth	
PB2	Casio Chord	Ending	Layer		
DD2	Normal	Variation/	Colit	Mixor	
PB3	Normal	Fill-In	Split	Mixer	

Note: Each key has two contacts, the first conatct (1) and second contact (2).



#### NOMENCLATURE OF KEYS



#### POWER SUPPLY CIRCUIT

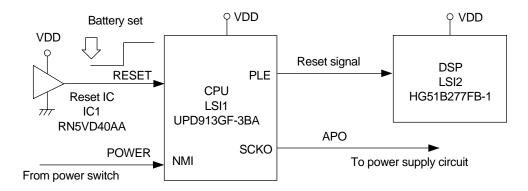
The power supply circuit generates five voltages as shown in the following table. VDD voltage is always generated. The others are controlled by APO signal from the CPU.

Name	Voltage	For operation of
VDD	+5 V	CPU, Reset IC, DSP, Sound source ROM, Working storage RAM, Effect RAM
DVDD	+5 V	LCD driver, Power jack, Sustain jack, MIDI jack
AVDD	+5 V	DAC, Filter
LVDD	+5.6 V	LCD dirver
VCC	+9 V	Power amplifier, Pilot lamp

#### RESET CIRCUIT

When batteries are set or an AC adapter is connected, the reset IC provides a low pulse to the CPU. The CPU then initializes its internal circuit, and clears the working storage RAM.

When the power switch is pressed, the CPU receives a low pulse of POWER signal. The CPU sends APO signal to the power supply circuit, also sends a reset signal to the DSP.



# CPU (LSI1: UPD913GF-3BA)

The 16-bit CPU contains a 1k-byte RAM, three 8-bit I/O ports, two timers, a key controller and serial interfaces. The CPU detects key velocity by counting the time between first-key input signal FI and second-key SI from the keyboard. The CPU reads sound data and velocity data from the sound source ROM in accordance with the selected tone; the CPU can read rhythm data simultaneously when a rhythm pattern is selected. Then the CPU provides 16-bit serial sound data to the DSP. The CPU also controls MIDI input/output and stores sequencer data into the working storage RAM.

The following table shows the pin functions of LSI1.

Pin No.	Terminal	In/Out	Function
1	TXD0	Out	MIDI signal output
2	RXD0	ln	MIDI signal input
3	SCK0	Out	APO (Auto Power Off) signal output
4, 5	TXD1, RXD2	In/Out	Data bus for the LCD driver
6	SCK1	Out	1 MHZ synchronizing pulse output
7	AVCC	In	DVDD (+5 V) source
8	ANO	ln	AC adaptor detection terminal. +5 V when the keyboard is powered by batteries and becomes 0 V to cancel the APO function when AC adaptor is connected.
9	AN1	_	Not used. Connected to ground.
10	AGND	In	Ground (0 V) source
11	BCK	Out	Bit clock output
12	SO	Out	Serial sound data output
13	LRCK	Out	Word clock output
14	GND	In	Ground (0 V) source
15, 16	XLT0, XLT1	In/Out	20 MHz clock input/output
17	VCC	In	+5 V source
18, 19	MD0, MD1	In	Mode selection terminal
20	RSTB	In	Reset signal input
21	NMI	In	Power ON signal input
22	INT/P10	In/Out	Data bus for the LCD driver
23 ~ 30	FI0 ~ FI3 SI0 ~ SI3	In	Terminal for key input signal
31 ~ 38	KC0 ~ KC7	Out	Terminal for key scan signal
39 ~ 46	FI4 ~ FI7 SI4 ~ SI7	In	Terminal for key input signal
47 ~ 50	FI8, FI9 SI8, SI9	_	Not used
51	FI10	In	Terminal for button input signal
52	SI10/P23	Out	Chip enable signal for the LCD driver
53 ~ 55	KI0 ~ KI2	In	Terminal for button input signal
56	MWNB	Out	Write enable signal for the DSP
57 ~ 76	MA0 ~ MA17	Out	Address bus
77	MCSB0	Out	Chip enable signal output for the sound source ROM
78	MCSB1	Out	Not used
79	MCSB2	Out	Chip enable signal output for the DSP

Pin No.	Terminal	In/Out	Function
80	VCC	In	+5 V source
81	GND	In	Ground (0 V) source
82	MRDB	Out	Read enable signal output for the sound source ROM
83 ~ 98	MD0 ~ MD15	In/Out	Data bus
99	PLE	Out	Reset signal output for the DSP
100	P17	In/Out	Data bus for the LCD driver

# **DIGITAL SIGNAL PROCESSOR (LSI2: HG51B227FB-1)**

The DSP receives 16-bit serial sound data output from the CPU and adds the selected effect to the sound data using the effect RAM. Then the DSP provides the sound data to the DAC. The DSP also controls button input/output.

The following table shows the pin functions of LSI2.

Pin No.	Terminal	In/Out	Function
1 ~ 3, 80	PB0 ~ PB3	In	Button input terminals
4	PB4	In	ASSIGNABLE Jack input
5	SO	Out	Serial sound data output for the DAC
6	WCKO	Out	Word clock output for the DAC
7	VDD3	In	+5 V source
8	TEST	_	Not used
9	RESB	In	Reset signal input
10	VSS2	In	Ground (0 V) source
11, 12	XIN, XOUT	In/Out	20 MHz clock input/output
13	WCKI	In	Word clock input from the CPU
14	SI	In	Serial sound data input from the CPU
15	BCKI	In	Bit clock input from the CPU
16	SINC	In	1 MHz synchronizing pulse input
17	VDD2	In	+5 V source
18 ~ 25	100 ~ 107	In/Out	Data bus
26	RCEB	Out	Chip enable signal output for the working storage RAM
27	VSS3	In	Ground (0 V) source
28	AD1	In	Address bus
29	OEB	Out	Output enable signal for working storage RAM
30	WEB	In	Write enable signal
31	VDD3	In	+5 V source
32	CE2	In	Chip enable signal input. High active.
33	AD0	In	Address bus
34	CE1B	In	Chip enable signal input. Low active.
35 ~ 41, 43	EIO0 ~ EIO7	In/Out	Data bus for the effect RAM

Pin No.	Terminal	In/Out	Function
42 , 44, 46 ~ 48, 51 ~ 59, 61	EA0 ~ EA12	Out	Address bus for the effect RAM
45	ECEB	Out	Chip enable signal output for the effect RAM
49	EOEB	Out	Read enable signal output for the effect RAM
50	VSS3	In	Ground (0 V) source
60	EWEB	Out	Write enable signal output for the effect RAM
62, 66, 70, 74, 78	VSS2	In	Ground source
63, 67, 71, 75, 79	VDD2	In	+5 V source
64, 65, 68, 69, 72, 73	PA0 ~ PA5	Out	Button scan signal output
76, 77	PA6/7	Out	Not used

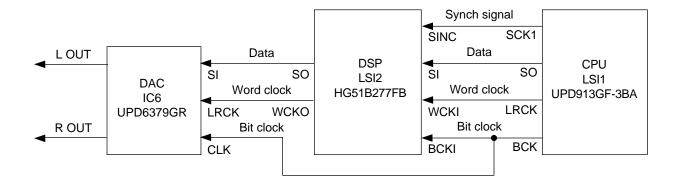
# LCD DRIVER (LSI401: SED1278F2A)

The LCD driver can drive a dot matrix LCD having 40 segment and 16 common lines. The LSI contains 240 graphic symbols in the built-in character generator ROM, and stores 80 characters in the built-in display data RAM. In accordance with command from the CPU, the LSI is capable of displaying up to 16 characters simultaneously. The following table shows the pin functions of LSI 401.

Pin No.	Terminal	In/Out	Function
1 ~ 22, 63 ~ 80	SEG1 ~ SEG40	Out	Segment signal output
23	VSS	_	GND (0 V) source
24, 25	OSC1, OSC2	In/Out	Terminals for the built-in clock pulse generator. The external resistor connected determines the oscillation frequency.
26 ~ 30	V1 ~ V5	In	LCD drive voltage input.  Those voltages are used for generating the stepped pulse of the LCD drive signals.
31, 32	LP, XCLS	_	Not used
33	VDD	In	DVDD (+5 V) source
34, 35	FR, DO	_	Not used
36	RS	In	Data/command determination terminal. High: data, Low: command
37	R/W	In	Read/write terminal. High: read, Low: write
38	Е	In	Chip enable signal. High: enable, the writing is done at fall edge. Low: disenable
39 ~ 42	DB0 ~ DB3	_	Not used. Connected to GND (0 V)
43 ~ 46	DB4 ~ DB7	In/Out	Data bus
47 ~ 62	COM1 ~ COM16	Out	Common signal/output

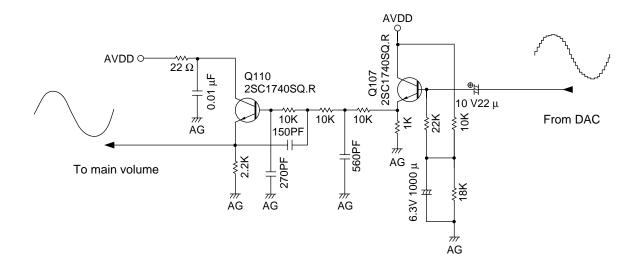
# DAC (LSI6: UPD6379GR)

The DAC receives 16-bit serial data output from the DSP. The data contains digital sound data of the melody, chord, bass, and percussion for the right and left channels. The DAC converts the data into analog waveforms and output them to each channel separately.



# **FILTER BLOCK**

Since the sound signals from the DAC are stepped waveforms, the filter block is added to smooth the waveforms.



# POWER AMPLIFIER (IC101: TA8248K)

The power amplifier is a two-channel amplifier with standby switch. The following table shows the pin function of IC101.

Pin No.	Terminal	In/Out	Function
1	NC	_	Not used
2	B.S.2	_	Terminal for a bootstrap capacitor
3	OUT2	Out	Channel 2 output
4	VCC	In	+9 V source
5	OUT1	Out	Channel 1 output
6	B.S.1	_	Terminal for a bootstrap capacitor
7	Power GND	In	Ground (0 V) source
8	Stand by	In	Power control signal input. 0 V: Off, +9 V: On
9	DC	_	Terminal for a decoupling capacitor
10	NF1	In	Negative feedback input
11	IN1	In	Channel 1 input
12	IN2	In	Channel 2 input
13	NF2	In	Negative feedback input
14, 15	Pre GND	ln	Ground (0 V) source

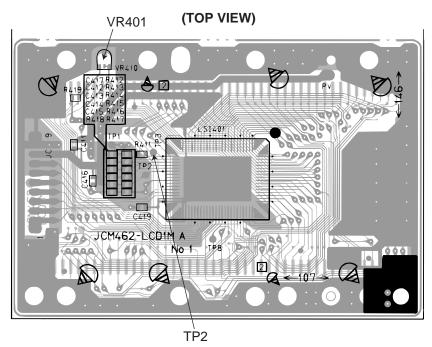
## **ADJUSTMENT**

### **DISPLAY PCB**

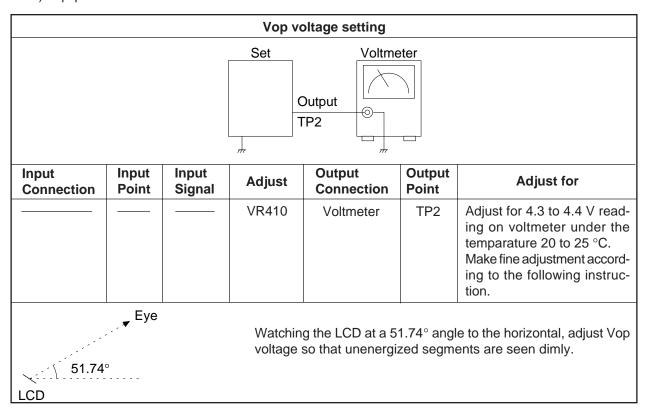
1) Items to be adjusted:

Item	Measuring Instrument
Vop voltage setting	Voltmeter

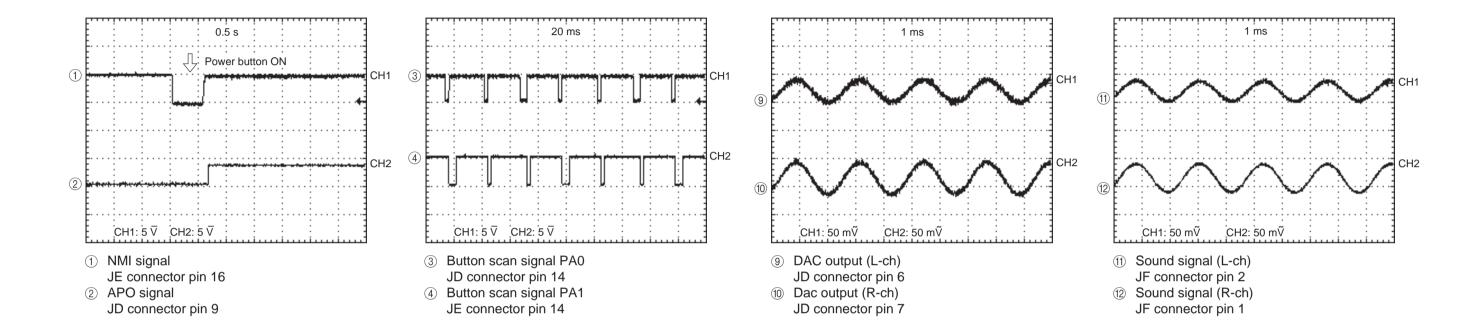
#### 2) Adjustment and Test Point Locations

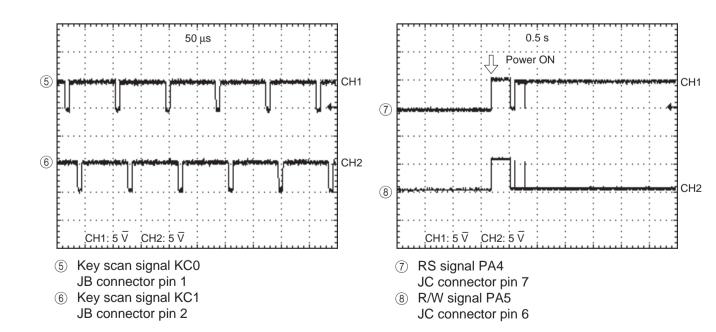


3) Equipment connection/Procedure



# **MAJOR WAVEFORMS**

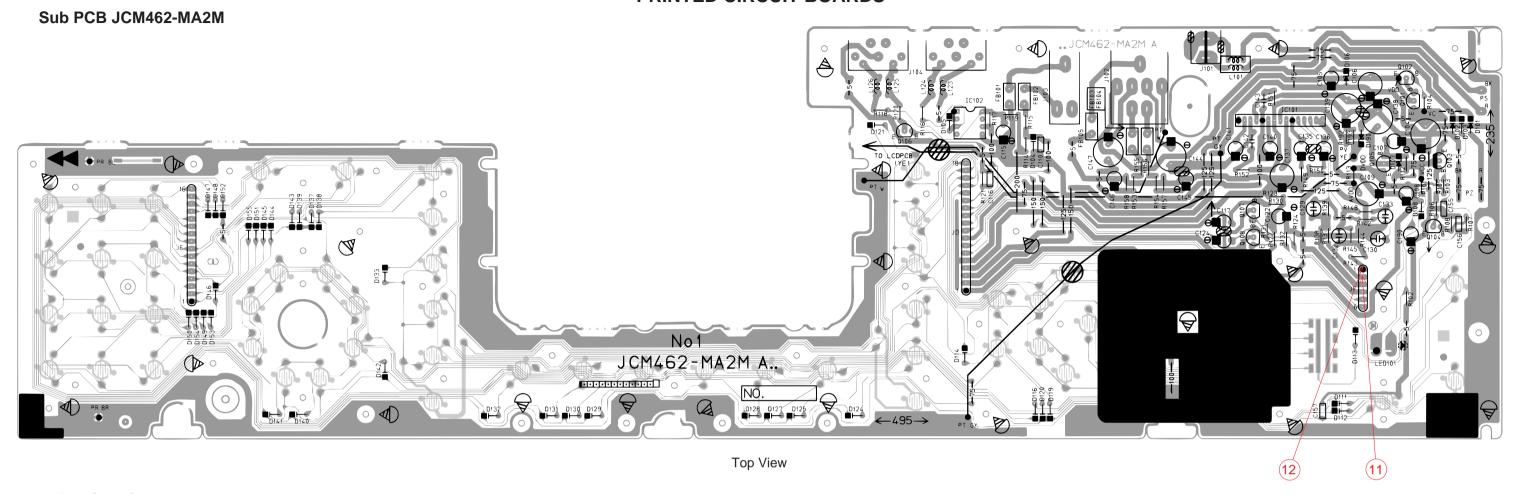




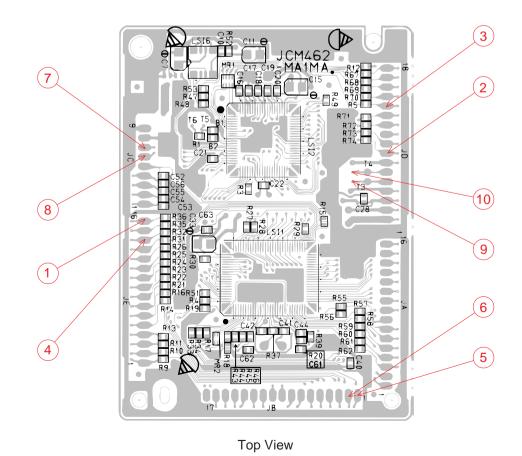
Tone: Whistle (078) Key: A4

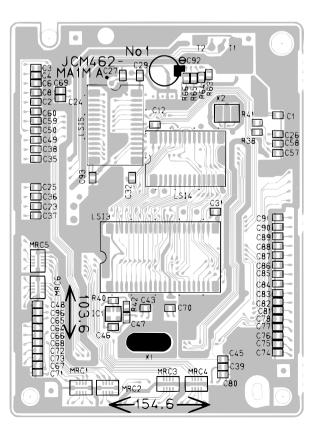
Touch response: Off Reverb: Off Volume: Maximum

# PRINTED CIRCUIT BOARDS



## Main PCB JCM462-MA1M

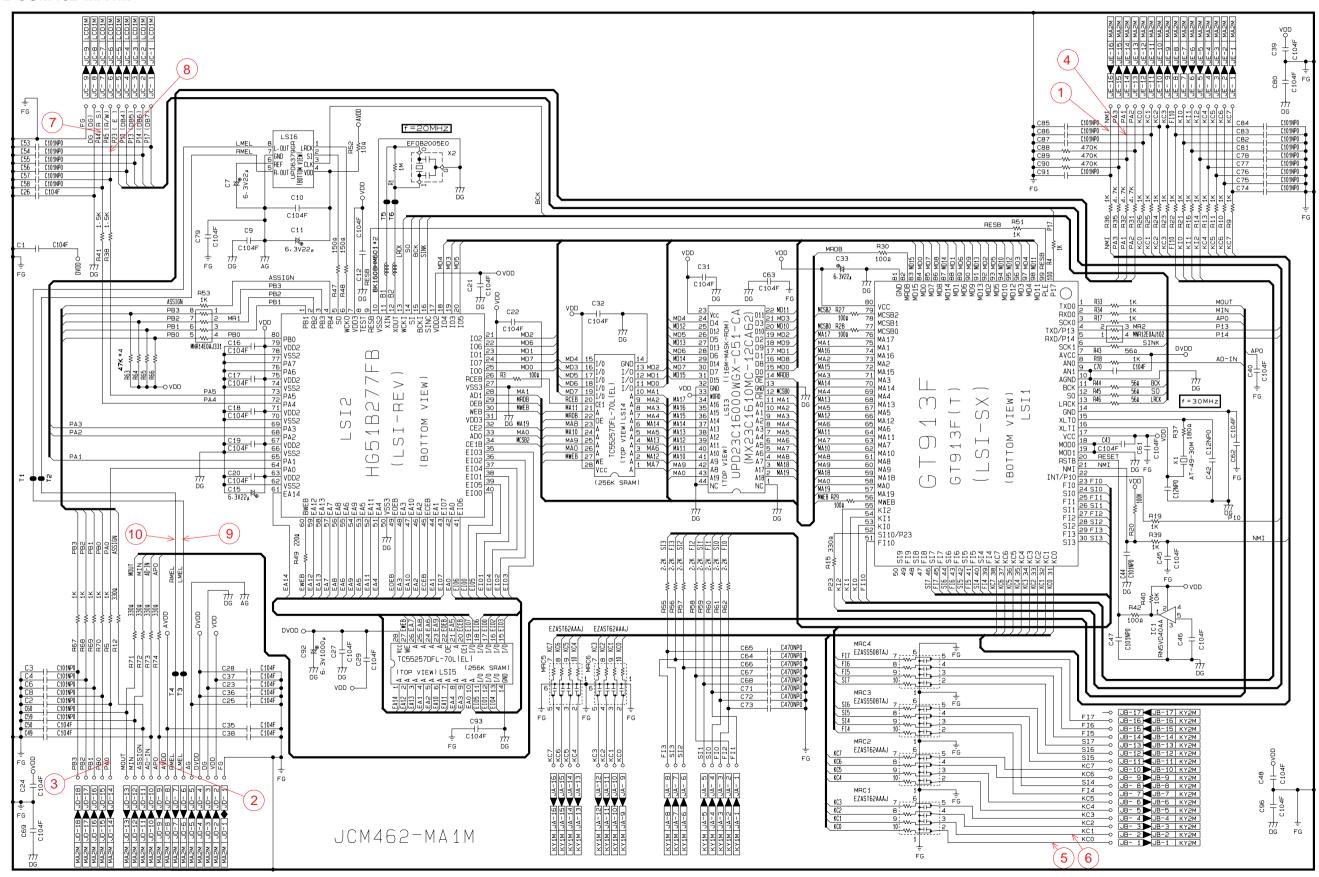




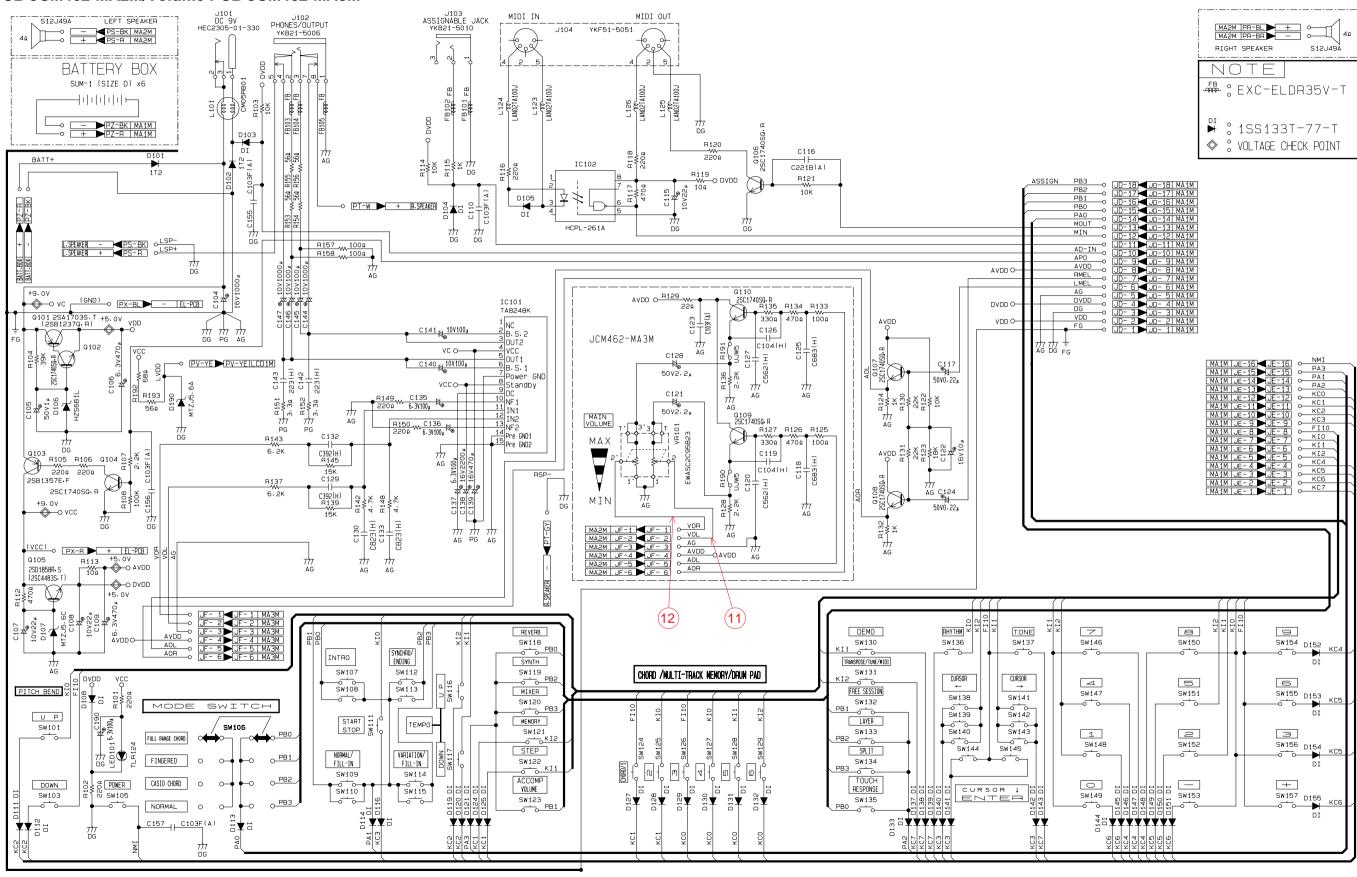
**Bottom View** 

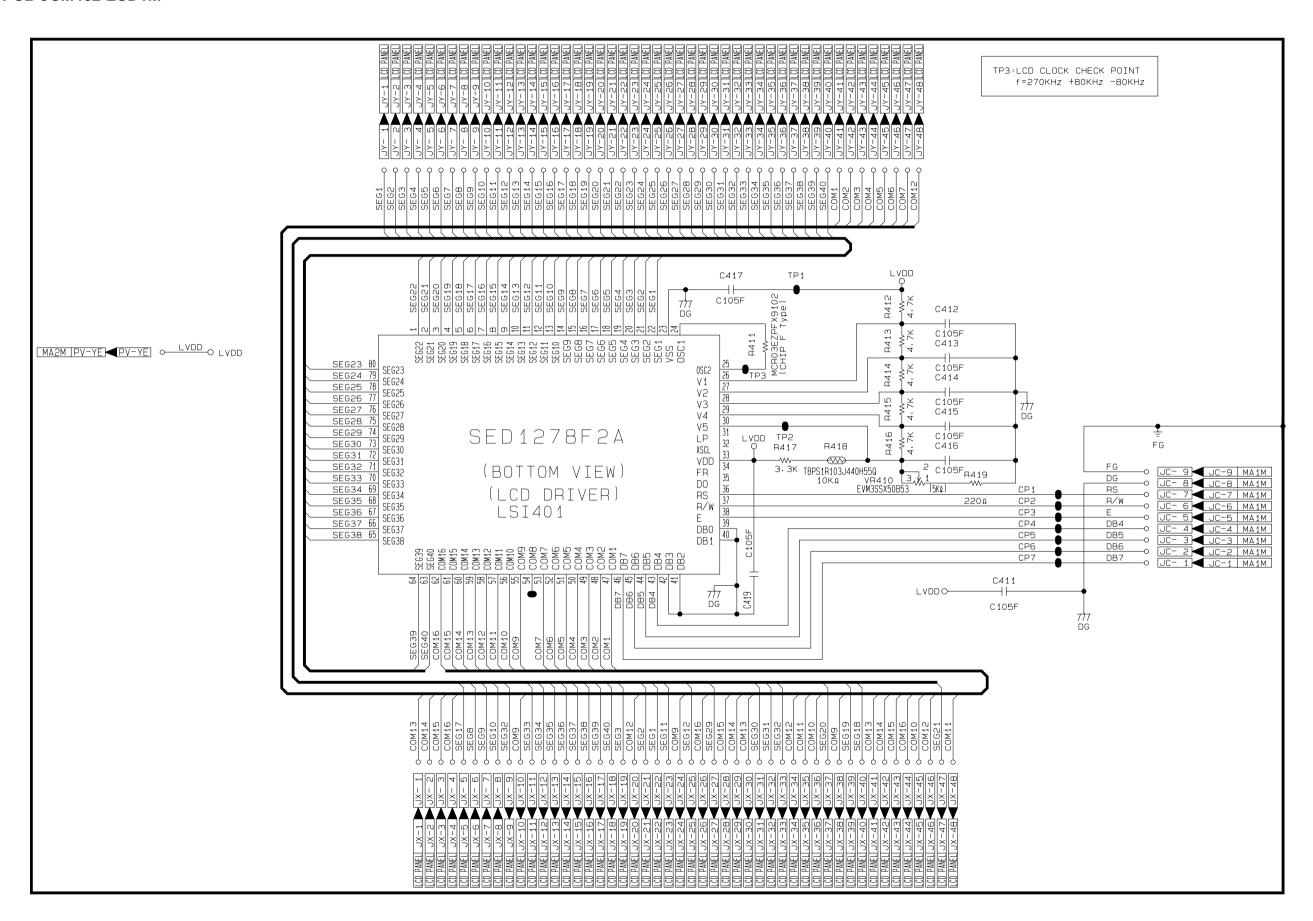
# **SCHEMATIC DIAGRAMS**

#### Main PCB JCM462-MA1M



# Sub PCB JCM462-MA2M/Volume PCB JCM462-MA3M





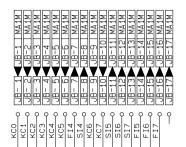
# **Keyboard PCBs JCM617T-KY1M/KY2M**

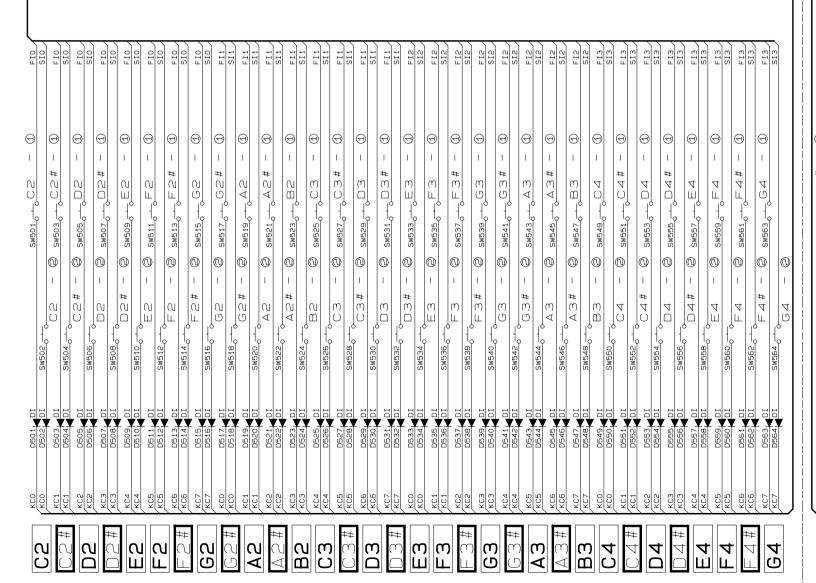
JCM617T-KY1M

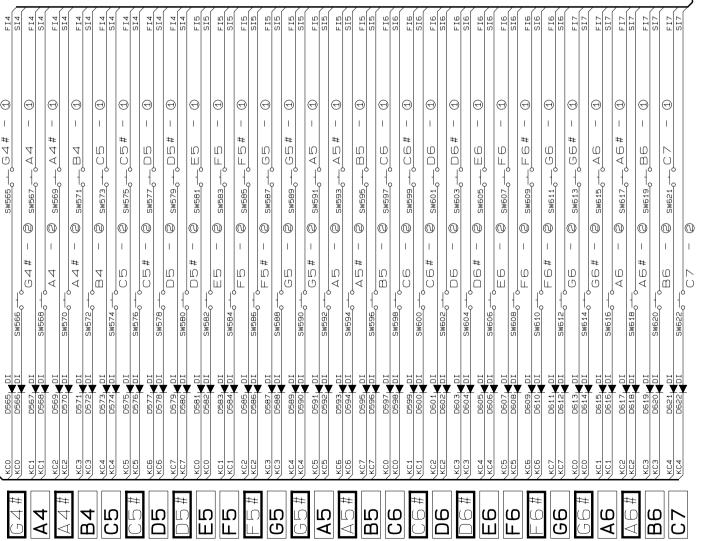
| UA-1 | UA-1 | MAIM | UA-2 | MAIM | UA-3 | UA-3 | MAIM | UA-4 | UA-5 | UA-6 | WAIM | UA-6 | UA-6 | WAIM | UA-10 | UA-9 | WAIM | UA-10 | UA-10 | UA-11 | UA-10 | UA-11 | UA-11 | UA-12 | UA-13 | UA-14 | WAIM | UA-15 | UA-15 | UA-16 | WAIM | UA-16 | UA

**♥** \$ 1S2473T-77-T

JCM617T-KY2M

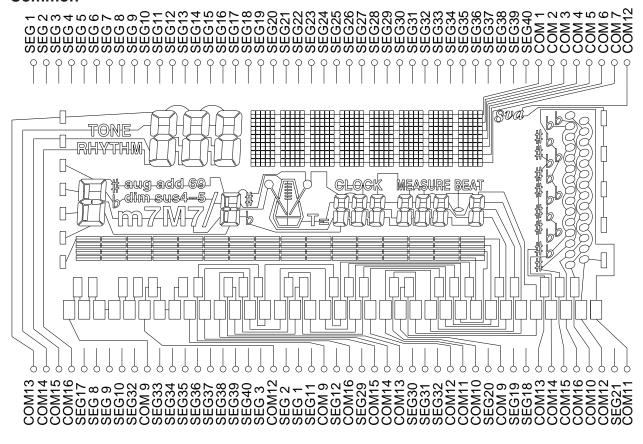




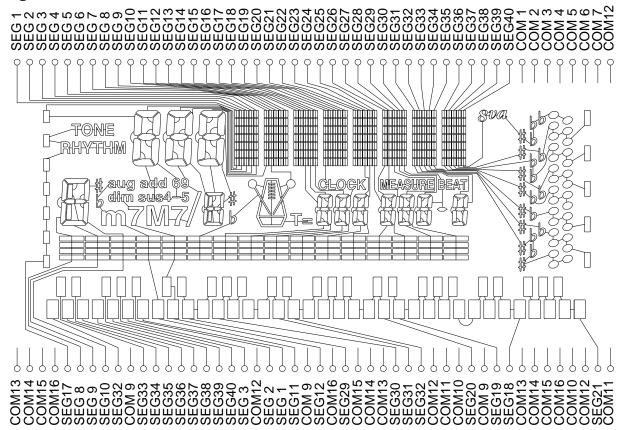


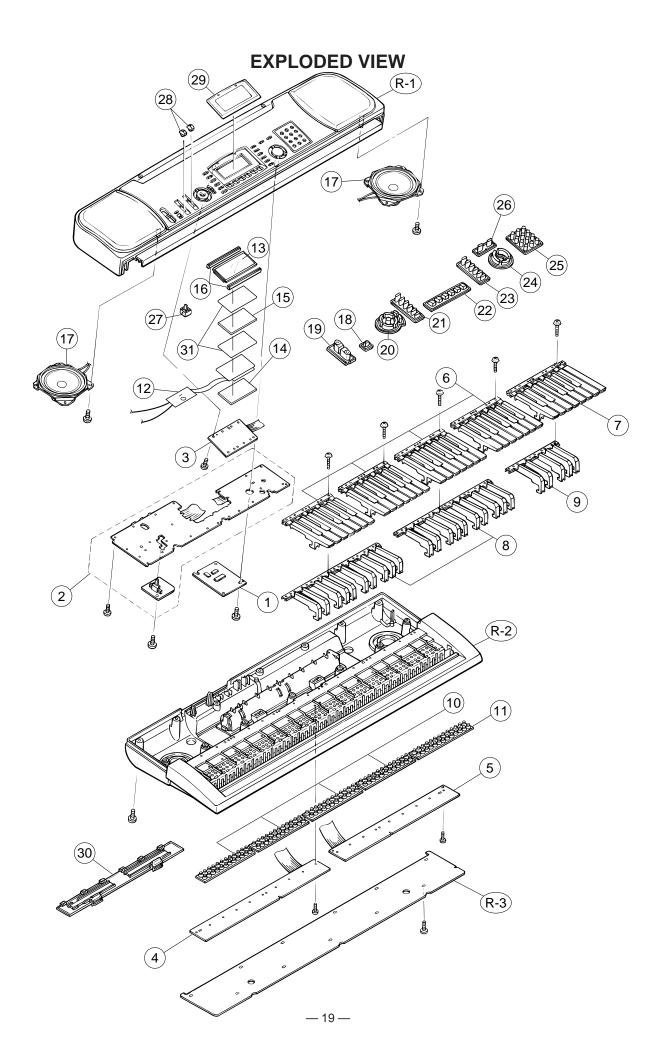
#### **LCD**

#### Common



### Segment





# PARTS LIST

# **CTK-611**

Notes: This parts list does not include the cosmetic parts, which parts are marked with item No. "R-X" in the exploded view.

Contact our spare parts department if you need these parts for refurbish.

- 1. Prices and specifications are subject to change without prior notice.
- 2. As for spare parts order and supply, refer to the "GUIDEBOOK for Spare parts Supply", published seperately.
- 3. The numbers in item column correspond to the same numbers in drawing.

Item	Code No.	Parts Name	Specification	Q	R
Item	Code No.	Faits Name	Specification	"	"
	Main PCB				
1	6925 8440	PCB/ASS'Y (MA1M)	M240613*1	1	В
LSI1	2012 4879	LSI/CPU	UPD913GF-3BA(T)	1	Α
LSI2	2012 2079	LSI/DSP	HG51B277FB-1	1	Α
LSI3	2012 5590	LSI/ROM	UPD23C16000WGX-C51	1	Α
LSI4/LSI5	2012 5572	LSI/RAM	TC55257DFL-70L(EL)	2	Α
LSI6	2105 4746	LSI/DAC	UPD6379GR-E1	1	Α
IC1	2012 1883	IC/MOS	RN5VD40AA-TR	1	Α
MRC1,2,5,6	2845 6456	R-C NETWORK/CHIP	EZAST62AAAJ	4	C
MRC3,4	2845 6457	R-C NETWORK/CHIP	EZASS508TAJ	2	C
X1	2590 2742	OSCILLATOR/CRYSTAL	AT-49-30M	1	C
X2	2590 2699	OSCILLATOR/CERAMIC	EFOB2005E0	1	C
	Sub PCB ass	s'y			
2	6926 9180	PCB/ASS'Y (MA2,3M)	M140554*3	1	В
IC101	2114 5775	IC/LINEAR (POWER AMP)	TA8248K	1	Α
IC102	2252 1248	IC/PHOTOCOUPLER	HCPL-261A	1	В
Q101	2250 0742	TRANSISTOR	2SA1703S,T-AN-T	1	C
		TRANSISTOR	2SC1740SQ,R-TP-T	7	C
Q103		TRANSISTOR	2SB1548-P,CS	1	1
Q105		TRANSISTOR	2SC4483S,T-AN	1	С
D101/D102		DIODE	1T2	2	C
2.02.02	2390 1344		1SS133T-77-T	40	
D106		DIODE/ZENER	HZS6B1LTD-T	1	C
D107		DIODE/ZENER	MTZJ5.6CT-77-T	1	C
D190		DIODE/ZENER	MTZJ5.6AT-77-T	1	C
LED101	2320 3146		TLR124	1	C
J101		JACK/POWER	HEC2305-01-330	1	C
J102		JACK/PHONE	YKB21-5006	'	C
J103		JACK	YKB21-5010	'	C
J104		JACK/DIN	YKF51-5051	'	C
VR101		VOLUME	EWASC2C95B23	'	C
VIIIOI	Display PCB		EWAGOZOSSBZO	<u> </u>	10
3		PCB/ASS'Y (LCD1M)	M240609*2	1	В
LSI401		LSI/LCD DRIVER	SED1278F2A	1	
VR401		POTENTIOMETER/CHIP	EVM3SSX50B53		I -
V11401	Keyboard Po		L V IVIO O O A O O O O O O O O O O O O O O O O	!	10
4		PCB/ASS'Y (KY1M)	M140211*5	1	В
D501 - D564	2301 0101		1S2473-T-77-T	I -	X
5		PCB/ASS'Y (KY2M)	M140212*9	1	B
D565 - D622	2301 0101	, , ,	1S2473-T-77-T	I -	X
D303 - D022	Keyboard ur		132473-1-77-1	30	1^
6		KEY SET/LT WHITE	M312118*1	4	Α
7		KEY SET/LT WHITE	M312118*2	1	A
8		KEY SET/10P BLACK	M111726-1	2	1
9		KEY SET/5P BLACK	M111726-2	1	A
				1	
10		RUBBER/KEY CONTACT	M211704A-1	4	C
11	6922 2771 Panel unit	RUBBER/KEY CONTACT	M211705A-1	1	IC
12		MODULE/EL	YLM-102	1	В
13		1	LD-B10088E		В
	3335 6724			1	
14		SPACER	M440644A-1	1	C
15		SPACER	M440648A-1		C
16	0925 8590	RUBBER/INTERCONNECTOR	M440426-2	2	В

Notes: Q – Quantity per unit R – Rank

Item	Code No.	Parts Name	Specification	Q	R
17	3831 0833	SPEAKER	S12J49A	2	В
18		RUBBER/BUTTON	M312122-2	1	В
19	1	RUBBER/BUTTON	M240543-1	1	В
20	1	RUBBER/BUTTON	M140516-2	1	В
21	1	RUBBER/BUTTON	M240544-1	1	В
22	1	RUBBER/BUTTON	M240544-3	1	В
23	1	RUBBER/BUTTON	M240545-2	1	В
24	1	RUBBER/BUTTON	M240546-2	1	В
25	1	RUBBER/BUTTON	M240547-2	1	В
26	1	RUBBER/BUTTON	M240548-1	1	В
27	1	SWITCH/SLIDE	CSB-12D	1	В
28	1	KNOB	M311859-1	2	
29	1	PLATE/DISPLAY	M240568-3	1	C
30	1	COVER/BATTERY	M311164G*16	1	В
31		FILM	M440651-1	2	
- 01	Accessory	I ILIVI	181770001 1		10
		STAND/MUSIC/BAR TYPE	M340629B*2	1	В
		STAND/MUSIC/BOARD TYPE	M140530-2	1	В

Notes: Q – Quantity per unit R – Rank

# CASIO TECHNO CO.,LTD.

Overseas Service Division

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