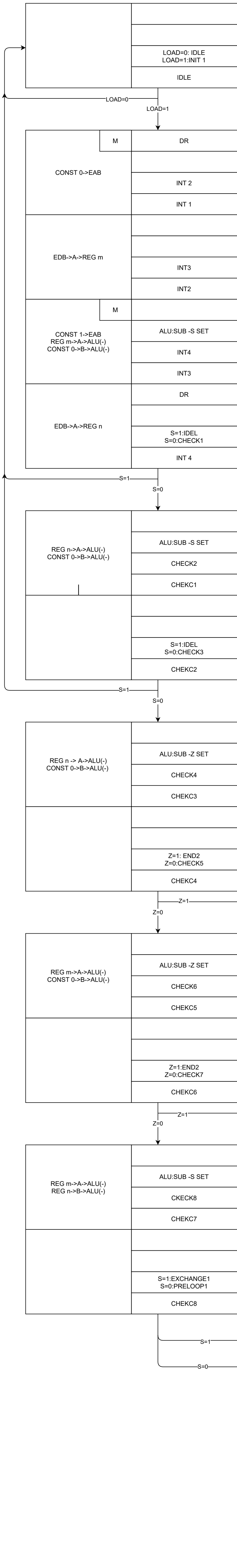


OPERATION	MEMORY ACCESS DIRICTION
	OPERATOR
	NEXT STATE
	CURRENT STATE



CONST 2->EAB REG i->EDB	M	DW
		IDLE
		END1

CONST 2->EAB CONST 0->EDB	M	DW
		IDLE
		END2

