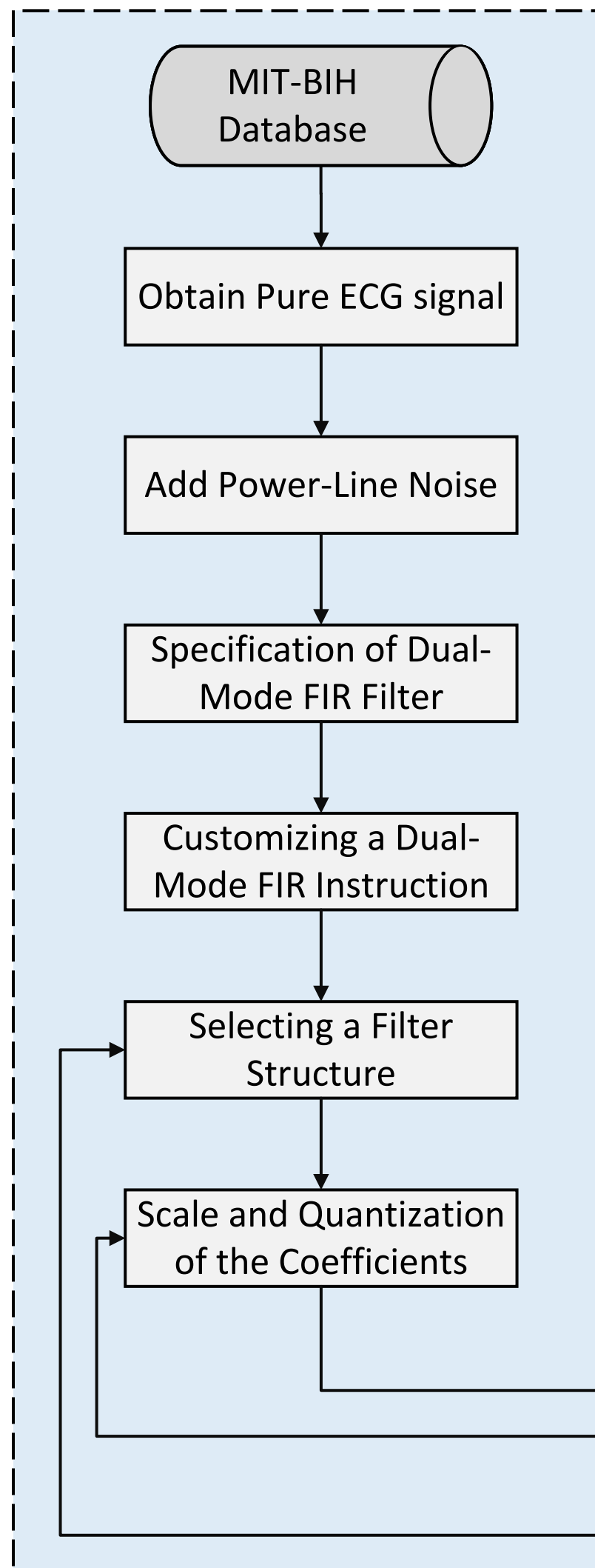
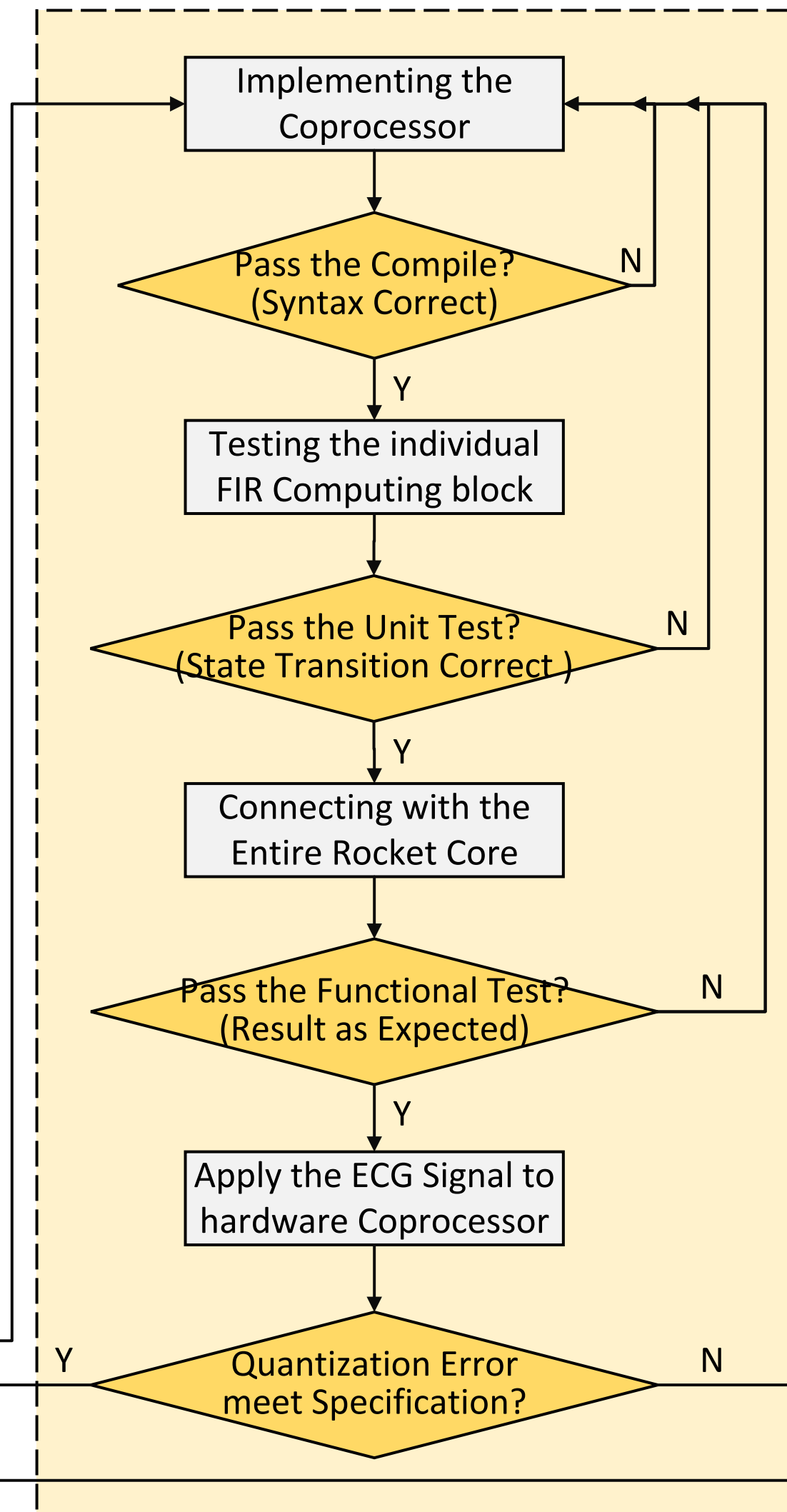


Specification and Methods



Hardware Implementation



Results and Analysis

