

	Throughput	Proportional To	
$\text{thrpt}_{\text{app}} = \min \left(\begin{array}{l} \text{thrpt}_{\text{comp}}, \\ \frac{\text{comp}}{\text{access}_{\text{off}}} \text{BW}_{\text{off}}, \\ \frac{\text{comp}}{\text{access}_{\text{on}}} \text{BW}_{\text{on}}, \\ \frac{\text{comp}}{\text{trans}_{\text{net}}} \text{BW}_{\text{net}} \end{array} \right)$	Compute	P, D	<div>Application-specific</div> <div>Hardware-specific</div> <div>P: Parallelization factor</div> <div>D: Pipelining depth</div>
	Off-chip Memory	P, D	
	On-chip Memory	P	
	On-chip Network	P^2, D	