

**Spatial**

RTL

PIR

**Synthesis**

**SARA**

**Placement  
and Routing**

**Placement  
and Routing**

**FPGAs**

**Plasticine  
(Simulation)**

## Scaling a Reconfigurable Accelerator

1. Convert **imperative control constructs** to a **streaming dataflow graph** that can execute across a distributed network
2. Systematically handle **physical constraints** in Plasticine