

$$\text{thrpt}_{\text{app}} = \min \left( \begin{array}{l} \text{thrpt}_{\text{comp}}, \\ \frac{\text{comp}}{\text{access}_{\text{off}}} \text{BW}_{\text{off}}, \\ \frac{\text{comp}}{\text{access}_{\text{on}}} \text{BW}_{\text{on}}, \\ \frac{\text{comp}}{\text{trans}_{\text{net}}} \text{BW}_{\text{net}} \end{array} \right)$$

**Throughput**

**Proportional To**

Compute

$P, D$

Off-chip  
Memory

$P, D$

On-chip  
Memory

$P$

On-chip  
Network

$P^-, D$

Application-specific  
Hardware-specific  
P: Parallelization factor  
D: Pipelining depth