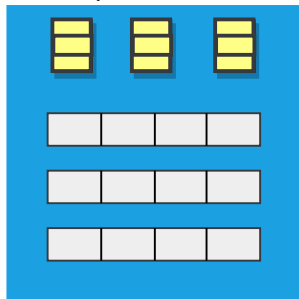


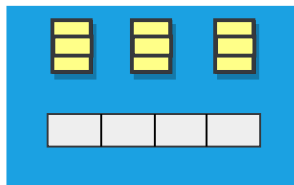
Input Buffers

Pipelined
CGRA



SIMD Compute

Time
Scheduled
CGRA



Pipeline

Time-Scheduled Execution

