

Spatial

- Banking + Buffering
- Loop Unrolling

Nested loops with explicit memory hierarchy

Low-Level IR

- VB Allocation
- VB Partitioning

Flat dataflow graph with duplicated controllers

Mapping

- Place and Route
- VC Allocation

Mapping of application PBs onto VBs

Simulation

Cycle accurate simulator
Integrated with DRAMSim and Booksim
Energy estimation using simulated activity