

**Spatial**

RTL

PIR

**Synthesis**

**SARA**

**Placement  
and Routing**

**Placement  
and Routing**

**FPGA**

**Plasticine  
(Simulation)**

## Scaling a Reconfigurable Accelerator

1. Support **imperative control constructs** on a distributed network
2. Systematically handle **hardware constraints** on a hierarchical architecture