

Spatial

RTL

PIR

Synthesis

SARA

**Placement
and Routing**

**Placement
and Routing**

FPGA

**Plasticine
(Simulation)**

Scaling a Reconfigurable Accelerator

1. Convert **imperative control constructs** to **streaming dataflow constructs** that can execute on a distributed network
2. Systematically handle **hardware constraints** in Plasticine