# Plan Ahead User Guide

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When running the PlanAhead™ tool from Project Navigator, a subset of the standalone PlanAhead tool features is available. The following chapters of the PlanAhead User Guide (UG632) describe the features that are supported:

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This guide assumes the user has performed the PlanAhead Tutorial or is otherwise familiar with the PlanAhead tool. Additional Resources Perform the PlanAhead Tutorial to learn most of the PlanAhead functionality using a sample design walk-through. Refer to the PlanAhead User Guide for information about specific PlanAhead

# PlanAhead Methodology Guide - Xilinx

CiteSeerX - Document Details (Isaac Councill, Lee Giles, Pradeep Teregowda): UG632 (v13.2) July 6, 2011Xilinx is disclosing this user guide, manual, release note, and/or specification (the "Documentation") to you solely for use in the development of designs to operate with Xilinx

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# PlanAhead Software Tutorial - Xilinx

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Partial Reconfiguration Tutorial www.xilinx.com 7 UG743 (v 13.4) January 3, 2012 Software Tools Flow Partial reconfiguration uses a bottom-up synthesis approach with top-down implementation methodology. This tutorial uses the Xilinx Synthesis Technology (XST) to synthesize the design, and the PlanAhead tool to implement the design.

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