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Allegro PCB SI provides users a way to derate setup and hold margins through user-defined derating tables for different signals in the source synchronous bus. (See Figure 3.) Serial linK deSign enVironMent (aVailaBle in allegro PCB Si gXl only) Serial link design environment is a virtual prototyping environment for designs with

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The SI analysis portion of this thesis will also involve using Cadence Allegro PCB SI for EM simulation. There is a brief overview on SI modeling. SigXplorer is again frequently used here, and will be revisited. Eye diagram simulations of PCB nets and other SI simulation reports are discussed. SigWave is used as the waveform viewer.

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