Word Size and Data Types

Embedded Software Essentials C2M1V2

Architecture [S1]

 Architecture designed to implement assembly instructions

 Complex Instruction Set Computer (CISC)

 Reduced Instruction Set Computer (RISC)

CPU Instruction Instruction | General Stack pointer Purpose Program Registers Counter Instruction Register Instruction Arithmetic Logical Unit (ALU) Result

Flas

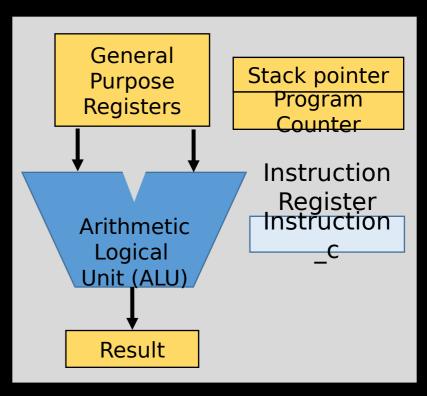
Advanced RISC Machine (ARM)

Units of Operation [S2]

- Instruction Fundamental unit of work or operation
 - Arithmetic
 - Logical
 - Program Flow Control
 - Load/Store

 Word – fundamental operand size for each operation

Inside a CPU



Example: 32-bit Machines are built to do 32-bit math most optimally

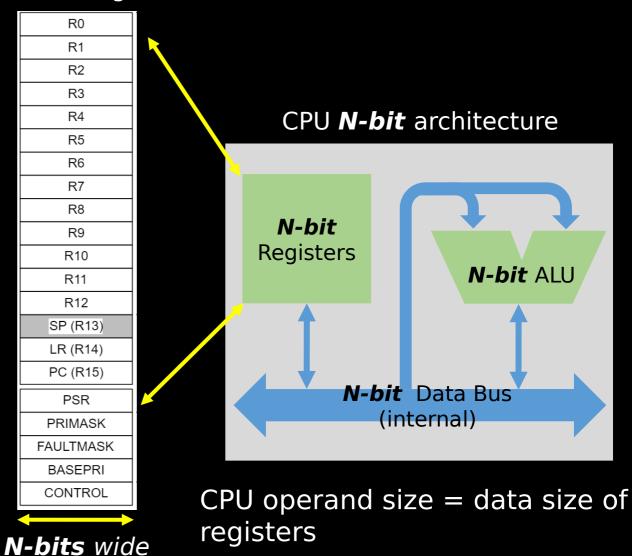
Units of Operation [S3]

Core CPU Registers

 General Purpose Registers in CPU will be size of the Word

ARM 32-bit Word = 32-bit registers

- Cortex-M Processors has General and Special Purpose CPU Core registers
 - R0-R12 General Purpose
 - R13-R15 Reserved Role
 - Program Status Registers
 - Exception Mask Registers
 - Control Register



Instruction Sizes [S4]

- Instruction size can vary
 - ARM Instruction Set ARMv6-M

 ☐ 16-Bit and 32-Bit
 - Thumb-2 Instruction Set
 ☐ 16-Bit

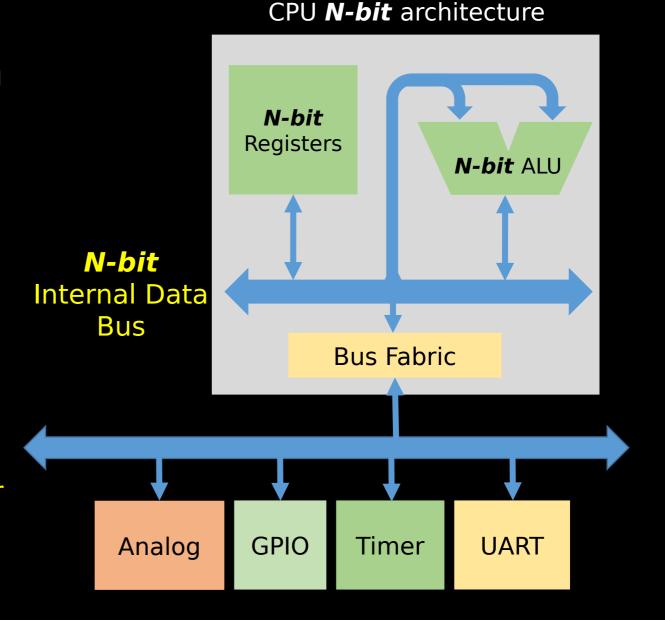
Example Output with Machine Code and Assembly Code

```
000104c8 <main>:
                          b5b0
                                           push
 16-Bit
Instructio
               104ca:
                          b096
                                          sub
   ns
          $p, #88
 32-Bit
              104cc:
                         af00
                                          add
Instructio
          r7<u>sp</u>, #0
   ns
                          f241 0330
                                          movw
                       Machine
                                       Assembly
          r3ns#444444
          r3. #2
```

Units of Operation [S5]

- There are a lot of Busses in a Microcontroller
 - Internal System Busses
 - Example: ARM AHB
 - External Peripheral Busses
 - Example: ARM APB
- Bus is at least the size of the instruction

External
Peripheral
Data Bus or
Crossbar



C-Programming Types [S6]

 Sizes of C data types are ambiguous and vary between architectures

C-Standard
 Specifies a minimum each variable can be

Тур	Siz	Value Range (min)
signed char int8_t	e At least 8- bits	[-2 ⁷ , +2 ⁷ -
unsigned char uint8_t	At least 8- bits	[0, +2 ⁸ - 1]
signed short int	At least 16-bits	[-2 ¹⁵ , +2 ¹⁵ - 1]
unsigned short int	At least 16-bits	[0, +2 ¹⁶ - 1]
uint16_t signed long int int32_t	At least 32-bits	[-2 ¹⁵ , +2 ¹⁵ - 1]
unsigned long int uint32_t	At least 32-bits	[0, +2 ³² - 1]

Standard Integer Sizes[S7]

Variable length Types can cause portability issues

- Explicitly defined types that specify storage and sign
 - Provide exact size, fast size, and minimum size

 Defined in the stdint.h header file

```
int8 t
           Exactly 8-bits
int16 t
           Exactly 16-bits
int32 t
           Exactly 32-bits
int64 t
           Exactly 64-bits
uint8 t
           Exactly 8-bits
uint16 t-
           Exactly 16-bits
uint32 t-
            Exactly 32-bits
            Exactly 64-bits
uint64 t-
```

Standard Integer Sizes [Standard Integer Size

- Explicitly defined types that specify exact size and sign
 - U = Unsigned

 - t = type

```
uint16 t
                             int32 t
            16-bits
                                  32-bits
                       signed
unsigned
      integer
                            integer
```

```
/* 8-bit signed/unsigned Integers */
typedef signed char int8 t;
typedef unsigned char uint8 t;
```

uint8 t var1; /* 16-bit signed/unsigned Integers */ • Int = Integer typent32 t var3 = -100, typedef signed short int int16_t; typedef unsigned short int uint16 t;

```
/* 32-bit signed/unsigned Integers */
typedef signed long int int32 t;
typedef unsigned long int uint32 t;
```

```
/* 64-bit signed/unsigned Integers */
typedef signed long long int int64 t;
typedef unsigned long long int uint64 t
```

```
#endif /* STDINT H */
```

Standard Integer Sizes [S9]

- You need a data size that allows for the fastest access while having at least N bits
 Types
 - Typically rounds up to word size
 - Most optimum size for operations

- You need the smallest data size that has a minimum size N bits
 - Least Types

```
int fast8 t
               int least8 t
int fast16 t
               int least16 t
               int least32 t
int fast32 t
               int least64 t
int fast64 t
               uint least8 t
uint fast8 t
               uint least16
uint fast16 t
uint fast32 t
uint fast64 t
               uint least32
               uvat1least64
   int fast8 t
   uint least16 t var2 = 12;
```

Typedef [S10]

typedef enum

COLOR BLUE =

COLOR GREEN

Color {

0,

= 2,

} Color t;

- Typedef keyword allows programmer to create own types (like an alias)
 - Can apply to standard types or derived types

```
typedef unsigned short int uint16 t;
                   typedef struct Data / 32-bit signed/unsigned Integers */
                     int32_t temperature signed long int int32_t;
                                          typedef unsigned long int uint32 t;
                     unt32 t date;
                     unt32 t time;
                                          /* 64-bit signed/unsigned Integers */
COLOR RED = 1, } Data t;
                                          typedef signed long long int int64 t;
                                          typedef unsigned long long int uint64 t
                                          #endif /* STDINT H */
```

#ifndef STDINT H

#define STDINT H

/* 8-bit signed/unsigned Integers */

/* 16-bit signed/unsigned Integers */

typedef signed short int int16 t;

typedef signed char int8 t;

typedef unsigned char uint8 t;