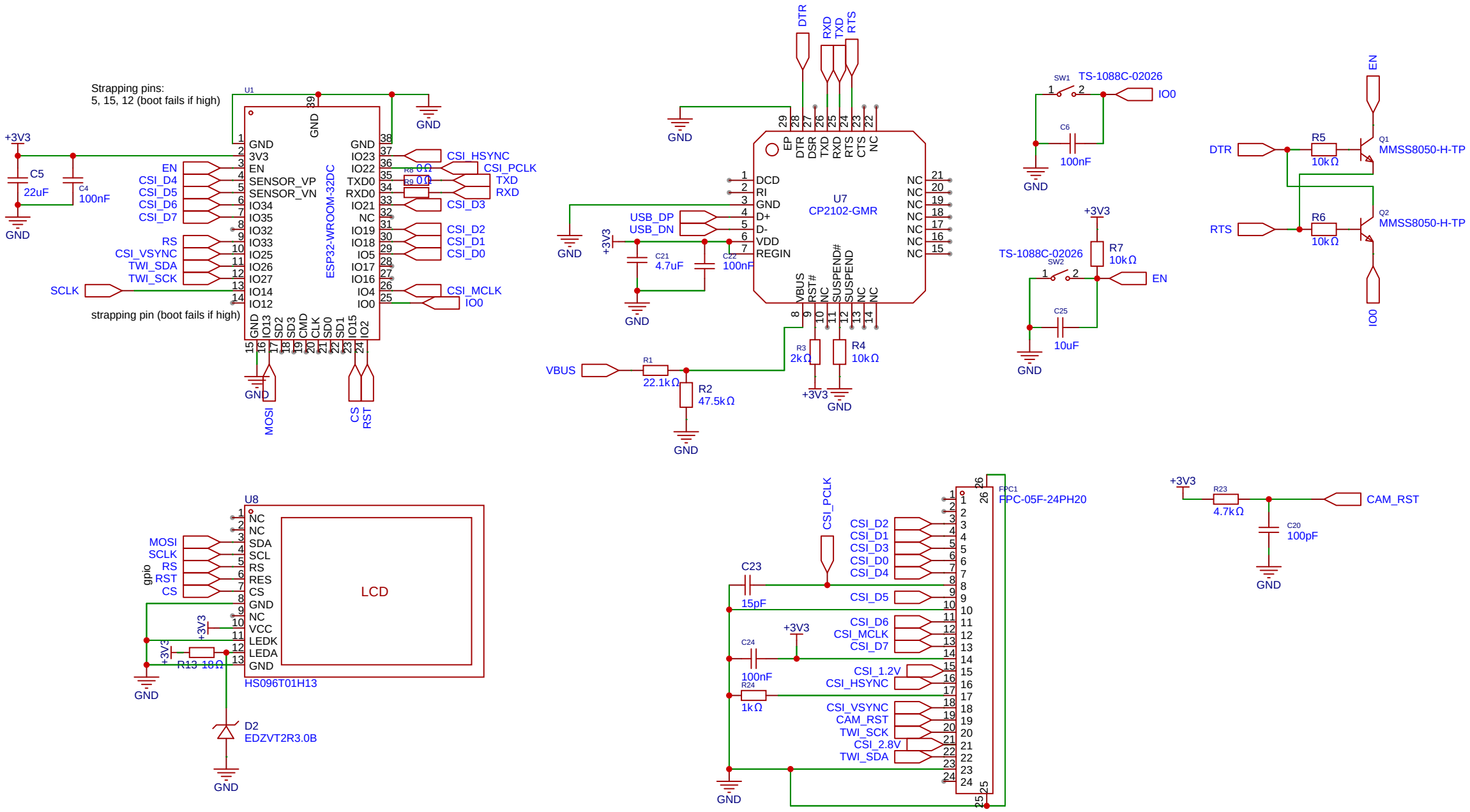
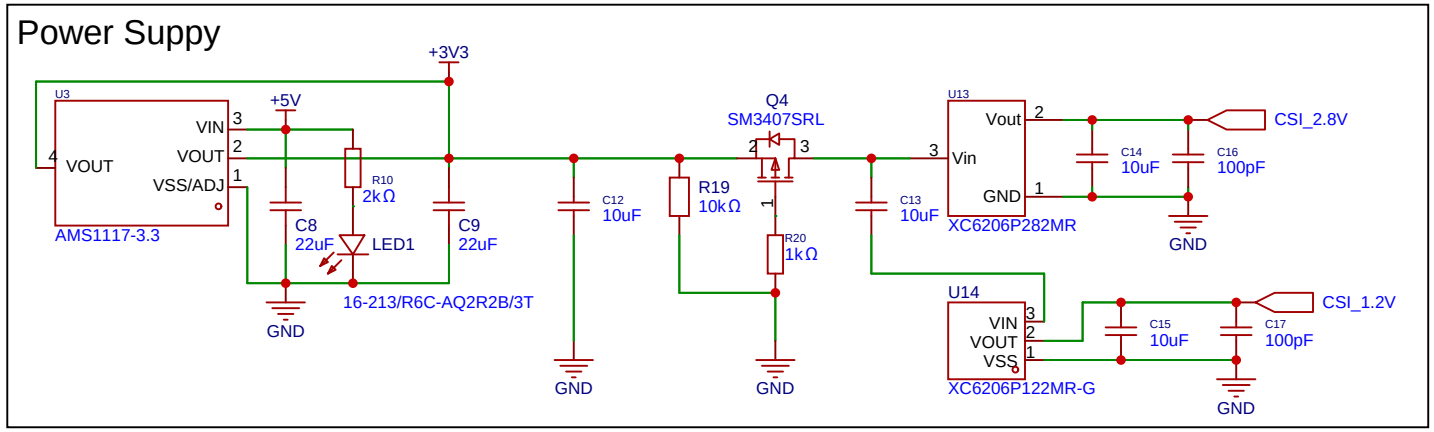
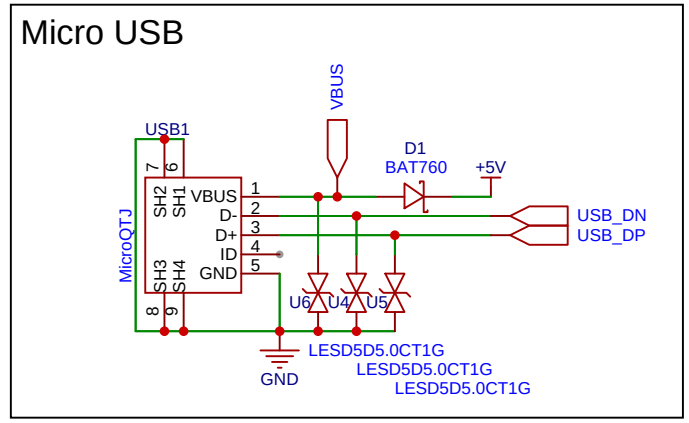
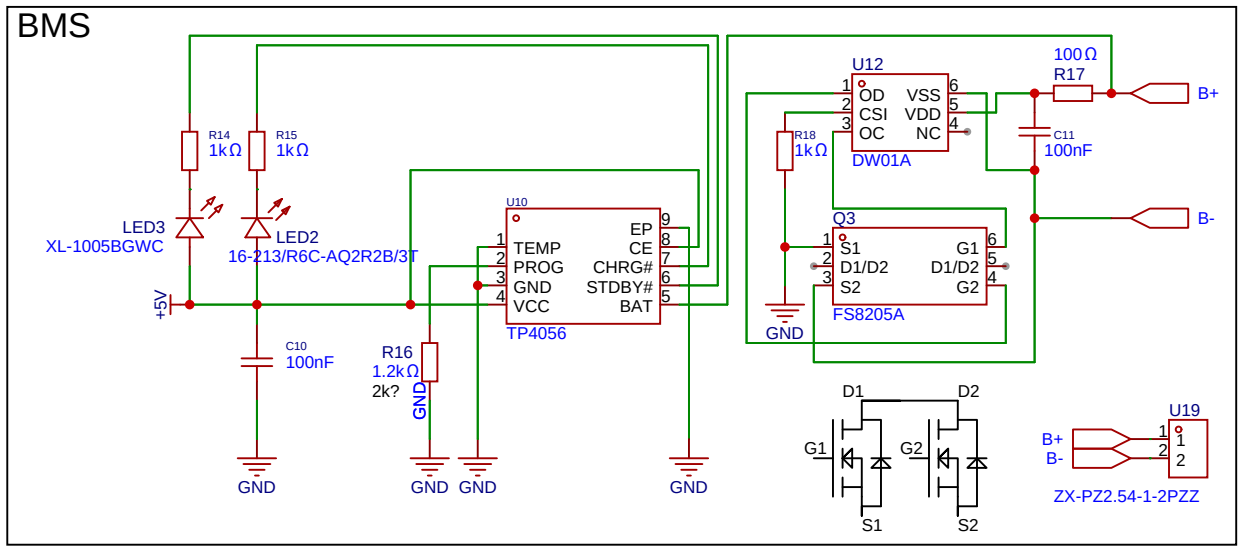

Strapping pins:
5, 15, 12 (boot fails if high)
strapping pin (boot fails if high)
U1
ESP32-WROOM-32DC
GND
3V3
EN
SENSOR_VP
SENSOR_VN
IO34
IO35
IO32
IO33
IO25
IO26
IO27
IO14
IO12
IO13
SD2
SD3
CMD
CLK
SD0
SD1
IO15
IO2
IO23
IO22
TXD0
RXD0
IO21
NC
IO19
IO18
IO5
IO17
IO16
IO4
IO0
+3V3
C5
22uF
C4
100nF
CSI_D4
CSI_D5
CSI_D6
CSI_D7
CSI_VSYNC
TWI_SDA
TWI_SCK
SCLK
CSI_HSYNC
CSI_PCLK
TXD
RXD
CSI_D3
CSI_D2
CSI_D1
CSI_D0
CSI_MCLK
IO0
R8 0Ω
R9 0Ω
MOSI
CS
RST
U7
CP2102-GMR
DTR
RXD
TXD
RTS
DCD
RI
D+
D-
VDD
REGIN
EP
DSR
CTS
NC
USB_DP
USB_DN
C21
4.7uF
C22
100nF
VBUS
SUSPEND#
SUSPEND
RST#
R1
22.1kΩ
R2
47.5kΩ
R3
2kΩ
R4
10kΩ
SW1
TS-1088C-02026
IO0
C6
100nF
SW2
R7
10kΩ
EN
C25
10uF
R5
10kΩ
R6
10kΩ
Q1
MMSS8050-H-TP
Q2
MMSS8050-H-TP
U8
LCD
HS096T01H13
gpio
MOSI
SCLK
RS
RST
CS
NC
SDA
SCL
RES
VCC
LEDK
LEDA
R13 10Ω
D2
EDZVT2R3.0B
FPC1
FPC-05F-24PH20
CSI_PCLK
CSI_D2
CSI_D1
CSI_D3
CSI_D0
CSI_D4
CSI_D5
CSI_D6
CSI_MCLK
CSI_D7
CSI_1.2V
CSI_HSYNC
CSI_VSYNC
CAM_RST
TWI_SCK
CSI_2.8V
TWI_SDA
C23
15pF
C24
100nF
R24
1kΩ
R23
4.7kΩ
C20
100pF
CAM_RST







Schematic	Schematic1	Update Date	2024-11-21
		Create Date	2024-10-11
Page	PMU/BMS	Part Number	JLCPCB-002
Drawn	EasyEDA	SmartGlasses	
Reviewed	EasyEDA		
		VER	SIZE
EasyEDA		V0.1	A4
		PAGE 2 OF 2	
		EasyEDA.com	