

File - Mips_Processor

```
1 Input : 10
2 Instruction running:lui
3 Instruction running:ori
4 Instruction running:lui
5 Instruction running:lw
6 Instruction running:addi
7 Instruction running:addi
8 Instruction running:add
9 Instruction running:add
10 Instruction running:add
11 Instruction running:sub
12 Instruction running:beq
13 Instruction running:j
14 Instruction running:add
15 Instruction running:add
16 Instruction running:add
17 Instruction running:sub
18 Instruction running:beq
19 Instruction running:j
20 Instruction running:add
21 Instruction running:add
22 Instruction running:add
23 Instruction running:sub
24 Instruction running:beq
25 Instruction running:j
26 Instruction running:add
27 Instruction running:add
28 Instruction running:add
29 Instruction running:sub
30 Instruction running:beq
31 Instruction running:j
32 Instruction running:add
33 Instruction running:add
34 Instruction running:add
35 Instruction running:sub
36 Instruction running:beq
37 Instruction running:j
38 Instruction running:add
39 Instruction running:add
40 Instruction running:add
41 Instruction running:sub
42 Instruction running:beq
43 Instruction running:j
44 Instruction running:add
45 Instruction running:add
46 Instruction running:add
47 Instruction running:sub
48 Instruction running:beq
49 Instruction running:j
50 Instruction running:add
51 Instruction running:add
52 Instruction running:add
53 Instruction running:sub
54 Instruction running:beq
55 Instruction running:j
56 Instruction running:add
57 Instruction running:add
58 Instruction running:add
59 Instruction running:sub
60 Instruction running:beq
61 Instruction running:j
62 Instruction running:add
63 Instruction running:add
64 Instruction running:add
65 Instruction running:sub
66 Instruction running:beq
67 Instruction running:sw
68 55
69
70 Process finished with exit code 0
71
```