

File - Mips\_Processor

```
1 Input : 6
2 Instruction running:lui
3 Instruction running:ori
4 Instruction running:lui
5 Instruction running:lw
6 Instruction running:addi
7 Instruction running:addi
8 Instruction running:mult
9 Instruction running:beq
10 Instruction running:addi
11 Instruction running:j
12 Instruction running:mult
13 Instruction running:beq
14 Instruction running:addi
15 Instruction running:j
16 Instruction running:mult
17 Instruction running:beq
18 Instruction running:addi
19 Instruction running:j
20 Instruction running:mult
21 Instruction running:beq
22 Instruction running:addi
23 Instruction running:j
24 Instruction running:mult
25 Instruction running:beq
26 Instruction running:addi
27 Instruction running:j
28 Instruction running:mult
29 Instruction running:beq
30 Instruction running:sw
31 720
32
33 Process finished with exit code 0
34
```