

Assignment - I

OC

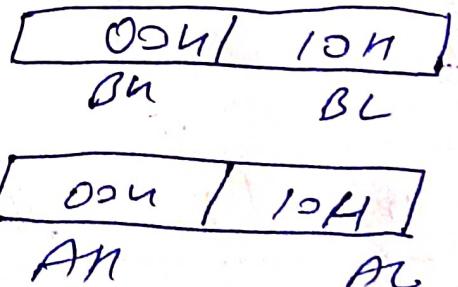
YASH GUPTA

S20200010274

Q-1 Explain the workflow of the following instruction: MOV AX, BX

Ans

- The MOV instruction is the most important command in the 8086 because it moves data from one location to another.
- MOV AX, BX is a register addressing mode,
- operand value is present in register BX .
- The instruction MOV AX, BX transfers the data of BX to AX .
- for eg MOV BX, 0010H
 MOV AX, BX



Q-2

Explain the execution of
following & which flag will be set?

(a)

MOV DH, 3 ; This is an immediate addressing mode 3H will be stored in DH.

NEG DH; 2's complement is applied for value at DH and is stored there only.

Carry flag, Sign flag, Auxiliary flag are Set
after this instruction

NOTE : NEG DH, 2 → error

SUB DH, 1 ; Value stored at DH will be decreased by 1 and stored in DH
 $DH = DH - 1$

Sign flag, parity flag

SUB DH, 73H ; The hexa decimal value at DH is subtracted by hexa decimal 73 and stored in DH
 $DH = DH - 73$

Sign flag

ADD DH, AX ; The value at DH is added with AX and stored in DH

$$DH = DH + AX$$

Carry & Auxiliary

ADD DH, AH ; The value at DH is added with AH and stored in DH

$$DH = DH + AH$$

No flags

CMP DH, AX ; The value at DH is compared with AX

Carry flag, parity flag and auxiliary flag

ADD DH, BX ; The value at DH is added to BX and stored in DH

No flags

(b) MOV AX, OFEH ; $AX \rightarrow \text{OFEH}$ (immediate
addressing mode)

INX AX ; $AX \rightarrow AX + 1$
(Parity flag)

MOV BX, D ; $DX \rightarrow 0000H$ (Immediate addressing)

ADD BX, AX ; $DX \rightarrow DX + AX$
(Parity flag)

NEG BX ; $BX \rightarrow 2^{\text{complement}}$ of value at DX
(Carry, Sign, auxiliary flag)

SUD AX, BX ; $DX \rightarrow AX - BX$
(Carry flag)

NEG AX ; $AX \rightarrow 2^{\text{complement}}$ of value at AX
(Carry, Sign, auxiliary flag)

NEG BX ; $BX \rightarrow 2^{\text{complement}}$ of value at DX

SBB AX ; subtract DX from AX along with borrow
(Sign, auxiliary)

DEC AX ; $AX \rightarrow AX - 1$
(Sign flag)

ADC BX, BX ; BX \rightarrow BX + BX + Carry
auxillary

SUB AX, BX ; AX \rightarrow AX - BX
Sign, auxillary

NEA BX ; BX \rightarrow 2's complement of BX
Carry, Sign, auxillary

NEA AX ; AX \rightarrow 2's complement of AX
Carry, Auxillary

(c) IMOV CL, 1H ; CL \rightarrow 1H (immediate)

SUB CL, 65H ; CL \rightarrow CL - 65H

Carry, Sign, parity, auxillary

NEA CL, 27H \rightarrow Error

Instead NEA CL ; CL \rightarrow 2's complement of CL

Carry, auxillary

ADD CL, 86H ; CL \rightarrow CL + 86H

Sign flag

SB B CL, 33H , CL \rightarrow CL - 33H with borrow

Sign, parity flag

(d) $\text{MOV AX, OF2FH}; \quad AX \rightarrow F24FH$
 $\text{MOV BX, DA3FSH}; \quad BX \rightarrow A3F5H$
 $\text{PDP BX, AX}; \quad BX \rightarrow AX + AX$

[Copy, sign, parity & auxiliary]

$\text{SBD AX, OF24EH}; \quad AX \rightarrow AX - F24EH$
with borrow
[Zero and parity flag]

$\text{MOV CX, 1000H}; \quad CX \rightarrow 1000H$
 $\text{ADD CX, 0E14CH}; \quad CX \rightarrow CX + E14C$
 $\text{ADC CX, 0EEECH}; \quad CX \rightarrow CX + EEEFH$

[Carry flag]
[Sign flag, parity flag, auxiliary flag]

(e) $\text{MOV BX, 9000H}; \quad BX \rightarrow 9000H$
 $\text{SUB BX, 0FFFH}; \quad BX \rightarrow BX - FFFFH$
[Carry, auxiliary flag]

$\text{MOV CX, 5750H}; \quad CX \rightarrow 5750H$
 $\text{RDC CX, 0FA00H}; \quad CX \rightarrow CX + FA00H + \text{Carry}$

[Carry, parity flag]

$\text{MOV DX, 0EBB3H}; \quad DX \rightarrow EB03H$
 $\text{RDC DX, 144CH}; \quad DX \rightarrow 144CH + \text{Carry}$

[Carry, parity, auxiliary flags]

$\text{MOV BX, 0D22H}; \quad BX \rightarrow 2H$
 $\text{RDP BX, 0EECH}; \quad BX \rightarrow BX + EEECH$

[Sign, parity, auxiliary]

Q.3 $Z = (X^2 + Y^2) / (X^2 - Y^2)$

X and Y are 8 bit numbers

Z is a 16 bit number

DATA SEGMENT

X DB 31H

Y DB 12H

Z DW 0000H

DATA ENDS

CODE SEGMENT

ASSUME CS:CODE DS:DATA

START: MOV AX, DATA; initializing the data segment

Mov DS, AX

Mov AL, X ; AL \rightarrow 31H

MUL AL; AX \rightarrow AL * AL

Mov [BX], AX ; stores X^2 in 500H

AL in 500H

AX in 501H

Mov AL, Y ; AL \rightarrow 12H

MUL AL; AL \rightarrow AL * AL &

Mov [BX]; stores Y^2 in 504H

AL in 500H

AX in 501H

MOV AX, [S00H]; moving to AX from S00H to AX

MOV BX, [S00H]; moving to BX from S00H to BX

ADD AX, BX; AX → AX + BX

MOV CX, [S00H]; moving to CX from S00H

SUB CX, BX; CX → CX - BX

DIV CX; AX → $\text{Q}(AX)/CX$

quotient in AX

remainder in DX

LEA BX, 2 ; finding address of 2 in

memory location 2000H + BX

MOV BX, 2, storing result in 2

STDP: PDP

COPG CPOS

CPD START

Q-9 Two numbers x_1 & x_2 are stored in memory. Perform following operation & store in x_3 .

$$x_3 = (x_1 \text{ AND } OFH) \text{ XOR } (x_2)$$

DATA SEGMENT

$x_3 \text{ DD } 0000H$

DATA SEGMENT

CODE SEGMENT

ASSUME CS: CODE, DS: DATA

START

MOV AX, 2000 ; Initializing Data register

MOV DS, AX

MOV [600H], 31H ; Storing 31H at 600H
offset value

MOV [700H], 12H ; Storing 12H at offset

MOV CH, [600H] ; copying value at 600H to CH

AND CH, OFH ; CH \rightarrow CH AND OFH

MOV DH, [800H] ; copying value at 800H to DH

XOR CH, DH ; CH \rightarrow CH \oplus DH

LEA AX, [x3]; Loading offset value of
 x_3 to AX

MOV [BX], AL ; Storing the final result
at x_3

STOP : NOP

CODE ENDS

END START

Q-5

DATA SEGMENT

ARRAY1 DB 21H, 25H, 26H, 26H, 00H, 01H, 02H,
03H, 09H, 05H, 06H
; declaring array 1 as Array 1

ARRAY2 DB 21H, 25H, 26H, 00H, 01H, 02H, 03H, 09H, 04H
; declaring array 2 as Array 2

DATA ENDS

CODE SEGMENT

ASSUME CS:CODE DS:DATA

START: MOV AX, DATA

MOV DS, AX

MOV AX, 0000H ; Storing 0000H at AX

LEA SI, ~~ARRAY1~~; Loading address of 1st element
of array1 in SI.

LEA DI, ARRAY2; Loading address of 2nd elements
of array2 in DI.

MOV CX, 0000AH ; Storing 0000H in CX

LABEL: MOV AL, [SI]; moving address of SI in AL

MOV BL, [DI]; moving address of DI in BL

CMP AL, BL ; Comparing values of AL, BL and
storing in AL

JNZ LABEL1 ; Jump if not zero to label 1

INC SI, Increment SI

INC DI, Increment DI

DEC CX, Decrease CX

JNL LABEL; jump if not zero to Label

MOV [S000H], 01H; Storing 01H in S000H

IMR STOP

LABEL ; MOV [S00H], 00H ; storing 00H in DS:00H
STOP; NOP
CODE ENDS
CNS START

Q-76

DATA SEGMENT

STRING DB 'MY NAME IS'
STRING1 DB 22 DUP(0)

DATA ENDS

CODE SEGMENT

ASSUME CS:CODE, DS:DATA ES:DATA

START

MOV AX, DATA; Initializing data and ES registers

MOV DS, AX

MOV ES, AX

MOV AX, 0000H ; Loading word value at AX

LEA SI, string ; Loading SI with address of string

LEA DI, string1 ; Loading DI with address of string1

MOV DL, 30H ; DL → 0BH for implementing loop

TAD; MOV AL,(SI) ; storing string in STRING1 using

STOSB

INC SI

DEC DL

JNZ TAD

MOV AL, 'S' ; adding SANTA to my NAME IS

STOSB

MOV AL, 'A'

STOSB

MOV AL, 'N'

STOSB

MOV AL, 'T'

STOSB

MOV AL, 'A'

STOSB

MOV AL, 'S'

STOSB

MOV AL, 'C' ; adding CLAUS to myNAME IS

STOSB

MOV AL, 'E'

STOSB

MOV AL, 'A'

STOSB

MOV AL, 'U'

STOSB

MOV AL, 'S'

STOSB

STDP : NDP

CODE ENDS

END START

Q-7 In the following program determine the values of x_1 , y_1 , z_1 at the end.

DATA SEGMENT

x_2 DW 0302 H

y_2 DW 0715 H

z_2 DW F227H

DATA ENDS

CODE SEGMENT

ASSUME CS: CODE, DS: DATA

START:

MOV AX, DATA; Initializing DATA Segment

MOV DS, AX

MOV AX, x_2 ; AX \rightarrow 0302 H

MOV BX, y_2 ; BX \rightarrow 0715 H

MOV CX, z_2 ; CX \rightarrow F227H

PUSH CX; CX is pushed to stack

PUSH BX

PUSH AX

PUSH BX

PUSH CX

PUSH AX

XCHG BX, z_2 ; Exchange value of BX, z_2

XCHG CX, AX; Exchange value of CX, AX

MOVF z_1 , AX;

MOV V1, CX

MOV X1, BX

CODE ENDS

END START

Ans $x_1 = 5f\ 22H, y_1 = 0302H \quad 21 = 6f\ 227H$

Q-8 See if you can spot the grammatical errors in the following instructions

(a) MOV BH, AX

- AX is 16 bit register while BH is 8 bit register.
- 16 bit data can't be stored in a 8 bit register.

(b) MOV DX, CL

- DX is 16 bit register and CL is 8 bit register.
- 8 bit data can't be stored in a 16 bit register.

(c) MOV 7632H, CX

- We can't store a value of CX register in 7632H value. But 8 or 16 bit can be considered as a memory location and proceeds.

~~(d)~~ (c) ADD AL, 2073H

- We can't add the 8 bit data and a 16 bit data.

(e) IN BL, 04H

- Data can't be transferred from port. The BL register can only be transferred to AL, AX and DX register.

Q-9 Describe the function of each assembler directives and instruction statement in the following short program

;PRESSURE - REG PROGRAM

DATAHERE SEGMENT

PRESSURE DB 0; Storage for pressure

DATAHERE ENDS

PRESSURE - PORT EQU 0FH; Pressure sensor connected to port 04H

CORRECTION-FACTOR EQU 0H; Current factor of 0AH

CODEHERE SEGMENT

ASSUME CS: CODEHERE, DS: DATAHERE
MOV AX, DATAHERE; Initialising Data segment

MOV DS, AX

IN AL, PRESSURE-PORT; value of pressure is stored in AL register

ADD AL, CORRECTION-FACTOR;

value of correction factor is added to AL content and replaced

MOV PRESSURE, AL; the value of

AL register is transferred to pressure

CODEHERE ENDS

ENDP

ASSEMBLER DIRECTIVES USED:

DB - Data Byte - Refers to byte data

Thus DB reserves a byte in the program memory

CON - Refining assembler constant

associates a constant value to the name of a constant

END - End of program

It shows that there are no instructions after it

Q-1 Write the 8086 instruction which will perform the indicated operation.

a. copy AL to BL

MOV BL, AL

b. Load 43H in CL

MOV CL, 43H

c. Increment the contents of CX by 1.

INC CX

d. Add 07 to DL

ADD DL, 07H

e. Copy SP to BP

MOV BP, SP

f multiply AL times BL
mul BL

g Copy AX to a memory location at offset
24SAH in the data segment

mov [24SAH], AX; AX will hold 245AH
or [24SAH], AH; AH will hold 243SH

h. Reverse SP by 2

push SP

i. Rotate the most significant bit of AL clockwise
to least significant bit position.

ROL AL, 1

j. copy CL to a memory location whose
offset is in BX
mov [BX], CL

k. mask the most significant bit of AX
to a 1, but do not affect the
other bits

OR AX, 3000H

l. Invert the lower 4 bits of CL,
but do not affect the sign bit
XOR CL, OFH

Q-11 Actual location (address) of the memory for the following command

MOV AX, 5000 [BX][SI]

$$= 16 \times DS + 5000H + [BX] + [SI]$$

Q-12 What will happen if you execute the following command in 8086.

(a) PUSH SS0H

This instruction pushes the contents of SS0H and SS1H to the stack. The stack pointer is decremented by 2.

(b) CMP AX, BX

It will compare AX & BX by doing

Subtraction
CF ZF SF
0 0 0

AX - BX

AX = BX 0 0 1 0

AX < BX 1 0 1 0

(c) Neg AL
z's complement of AL is calculated and replaced

(d) TEST AX, BX

Test instruction is used to add operands & update the flags without changing the values in either of the registers

Q-13 An assembly language program which converts the Fahrenheit temperature to Celsius using the following relation: $C = (F - 32) \frac{5}{9}$

DATA SEGMENT
f DB 29 ; consider Fahrenheit = 41°f

DATA ENDS

CODE SEGMENT

ASSUME CS:CODE DS:DATA

START: MOV AX, DATA ; Initiating Data Segment

Mov DS, AX

Mov AX, 0000H ; Clearing Ax register

Mov AL, f ; copying f value to AL

$\text{SUB AL, 20H} ; \text{AL} \rightarrow \text{AL } 20H = 09H$
 $\text{MOV BH, 05H} ; \text{BH} \rightarrow 05H$
 $\text{MUL BH} ; \text{BX} \rightarrow \text{BH} \times \text{AL } 45H$
 $\text{MOV CH, 09} ; \text{CH} \rightarrow 09H$
 $\text{DIV CH} ; \text{AL} \rightarrow \text{quotient } 60H$
 $\text{MOV CL, 02} ; \text{copying AL to f}$
STOP : NOP
COPY GRPS
END START

Q-IT See if you can find my errors in the following instructions or groups of instructions

a. CNTDOWN : MOV BL, 72H
 DEC BL
 JNZ 2 CNTDOWN

This will make an infinite loop

b. ADD CX, AL
addition can't be performed b/w
8-bit & 16-bit registers

c. JMP BL

There is no such instruction.
We have to use labels instead of
registers.

d. JNZ (BX)

There is no such instruction
we have to use label instead of a
register.

O-RS Compute the average of 4 bytes stored
in an array in memory.

DATA SEGMENT

NUNS DB 01H, 0AH, 35H, 08H
AUG DB 1 DUP(0)
DATA CS:SEGMENT DS:DATA
CSEG ENDS

START: MOV AX, DATA; Initializing the data
segment

Mov PS, AX

Mov AX, 0000H

Mov CL, 01H; taking CL as counter
in loop.

LGA BX, NUNS; Loading BX with NUNS
starts address

NRG: ADD AL, (BX); Implementing loop
and adding all elements
and storing in AL

INC BX

PCL CL

JNZ NRG; if CL=0 loop will
stop

MOV CL, 0AH; CL → 0AH

MOV BX, CL; Content of AX/CL is
in BX

LGA BX, BX+4; Loading BX with
BX+4 address

MVR (BX), AL; Storing average of 4
numbers in memory

Stop : NOP

COPC GNOS

ENP START