

Semiconductor

P type

III

B

Al

Ga

In

n-type

N

P

As

Si

Built-in potential barrier

$$V_{bi} = \frac{kT}{e} \ln\left(\frac{N_A N_D}{n_i^2}\right)$$

\downarrow
 V_T - Thermal Voltage

$n_i = 1.5 \times 10^{10} \text{ cm}^{-3}$ for silicon
at room temperature.

$$I_D = I_s (e^{\frac{V_D}{V_T}} - 1) \approx I_s e^{\frac{V_D}{V_T}}$$

\downarrow

Reverse-bias
saturation current

Half-Wave Rectifier

6 ATACI 2011

$$V_{dc} = \frac{V_m}{\pi} \quad V_{rms} = \frac{V_m}{2}$$

$$\eta = \frac{\frac{V_m^2}{RL}}{\frac{V_{rms}^2}{RL}} = 40.53\%$$

$$\text{Form Factor}^2 = \frac{V_{rms}}{V_{avg}} = 1.57$$

$$\text{Ripple factor}(\%) = 1.21 \text{ or } 121\% \\ (\text{Ideal - zero})$$

Full-Wave Rectifier

$$V_{dc} = \frac{2V_m}{\pi} \quad V_{rms} = \frac{V_m}{\sqrt{2}} \quad (\eta) = 0.482$$

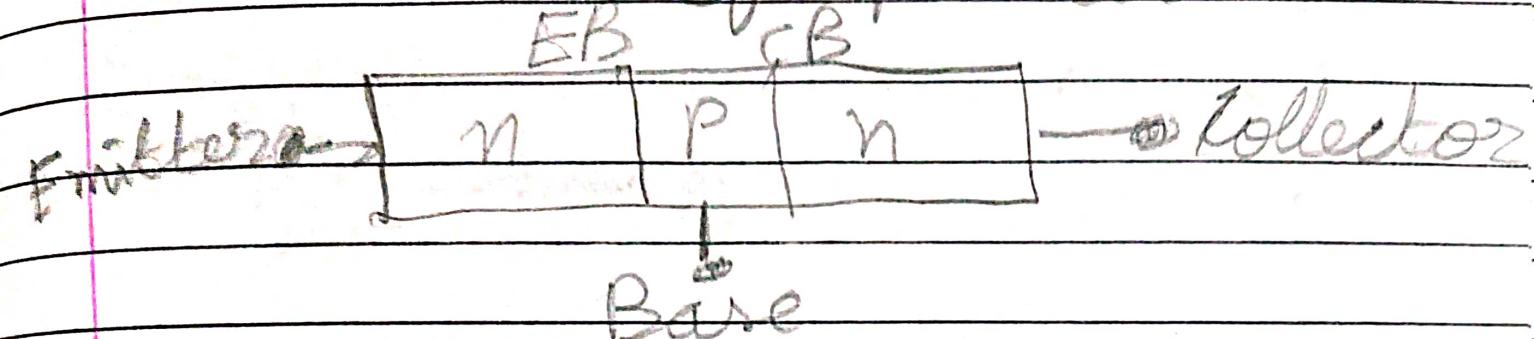
$$\text{Form Factor} = 1.11$$

$$V_{pf} = \text{Peak Factor} = \frac{V_m}{V_{rms}} = \sqrt{2}$$

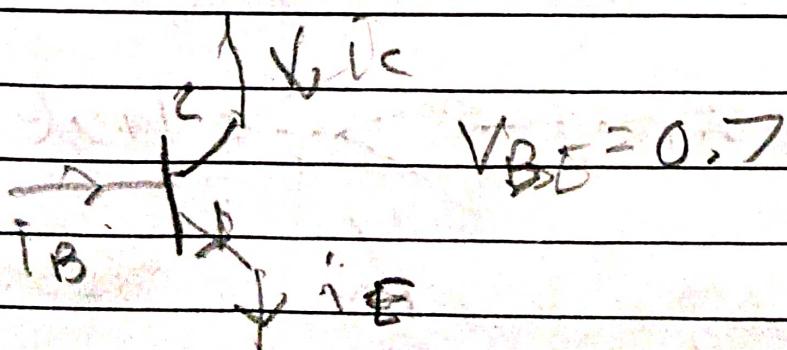
BJT

DATE _____
PAGE _____

Modes of Operation



EB	CB	Mode
FB	RB	Active
FB	FB	Saturation
RB	RB	Cutoff
RB	FB	Reverse-active



NPN Transistor

$$I_E = I_C + I_B$$

$$I_C = \alpha I_E \quad I_C = \beta I_B$$

mobility $\mu_n > \mu_p$

Operational Amplifier

Ideal characteristics

$$A_{OL} = \infty \quad R_o = 0$$

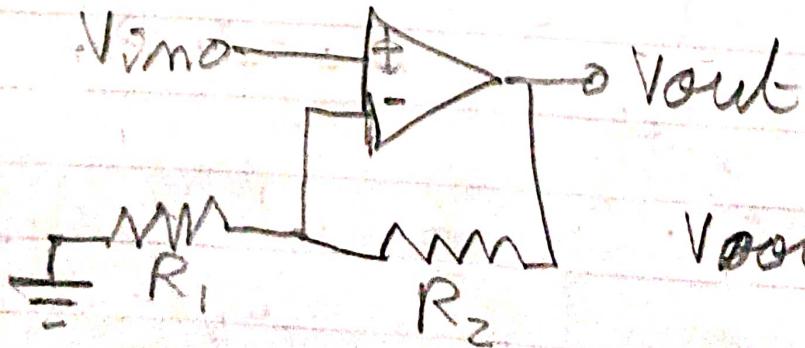
$$R_i = \infty \quad BW = \infty$$

$$V_o = 0 \quad \text{if } V^+ = V^- = 0$$

$$\text{CMRR} = \frac{A_{V \text{ diff}}}{A_{CM}}$$

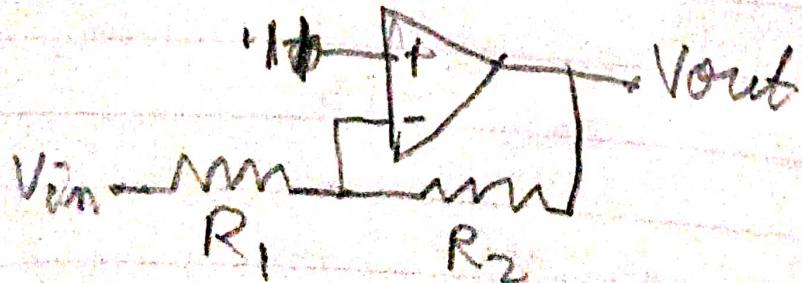
$$\text{Slew Rate} = \frac{\Delta V_{out}}{\Delta t}$$

Non inverting OP-amp



$$V_{out} = V_{in+} \left(1 + \frac{R_2}{R_1} \right)$$

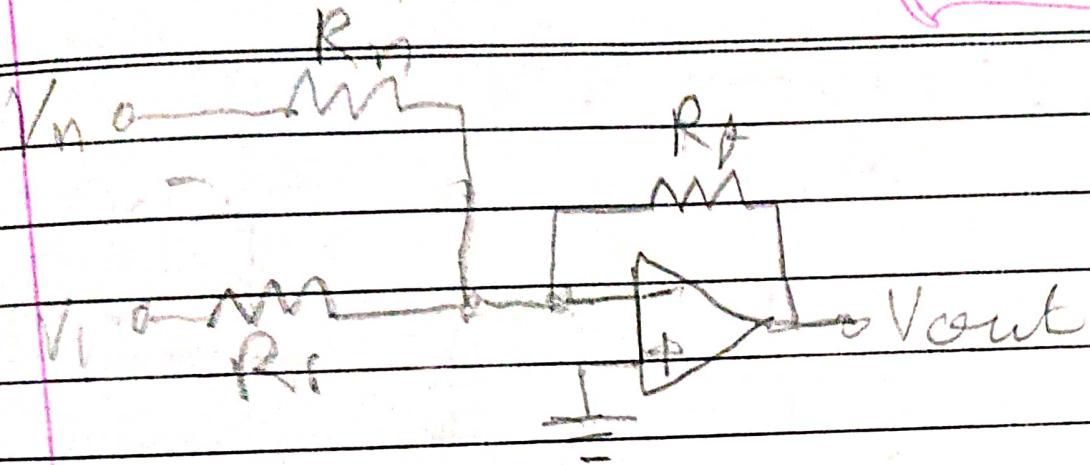
Inverting Op-Amp



$$V_{out} = V_{in-} \left(-\frac{R_2}{R_1} \right)$$

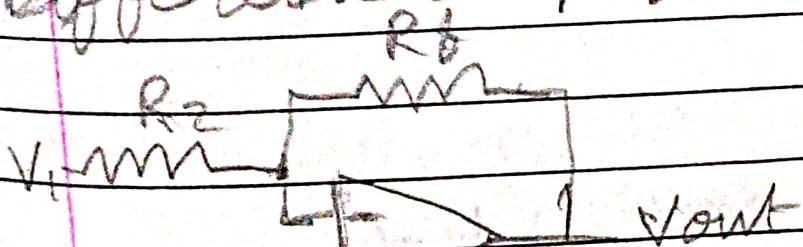
Inverting Summing Amplifier

DATE _____
PAGE _____



$$V_{out} = - \left(\frac{R_f}{R_i} + \frac{R_f}{R_n} \right) V_{in}$$

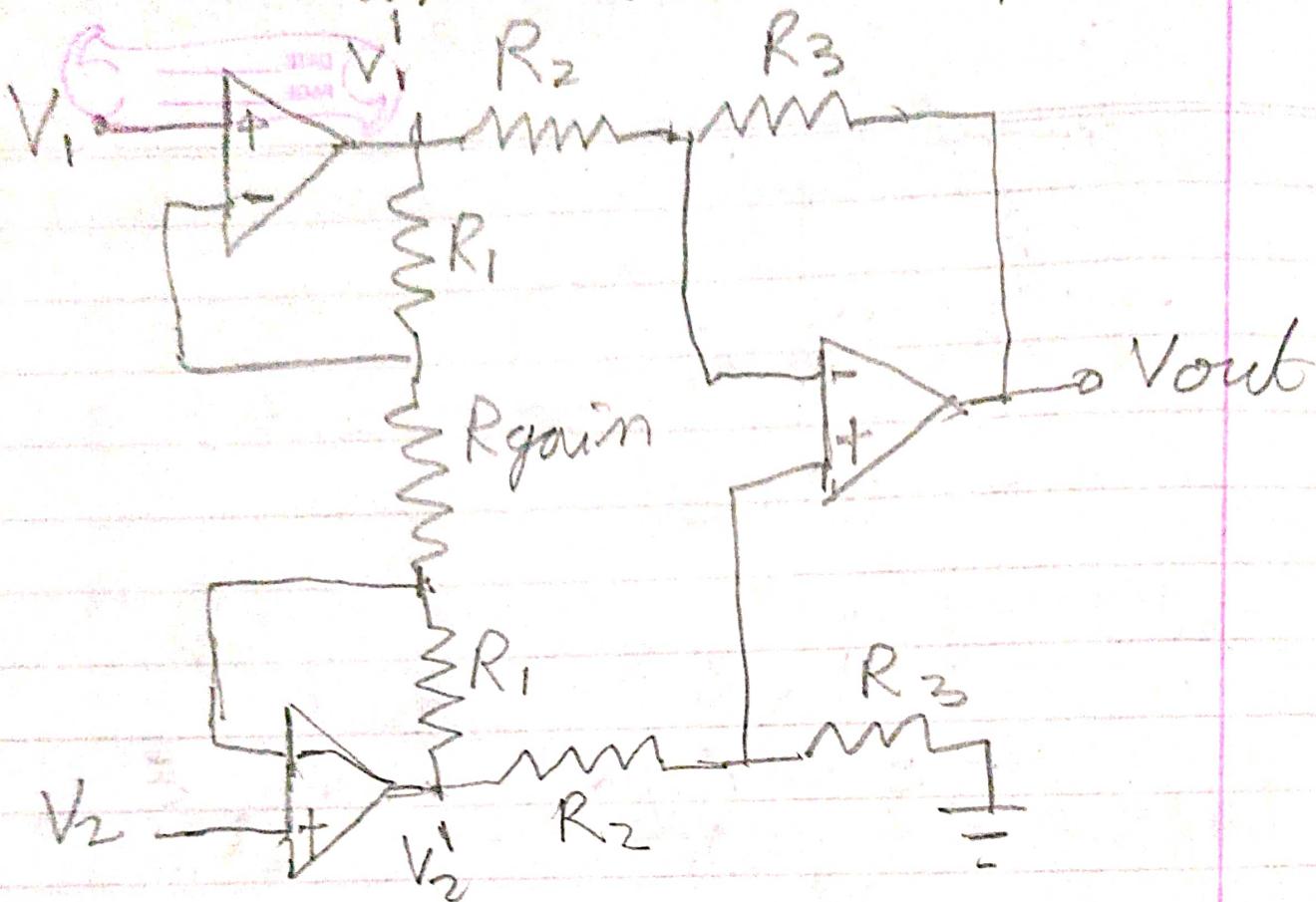
Difference amplifier



$$V_{out} = V_1 \frac{R_f}{R_2} + V_2 \frac{R_f}{R_2}$$

$$+ \frac{V_2 R_g}{R_2 + R_g} \left(1 + \frac{R_f}{R_2} \right)$$

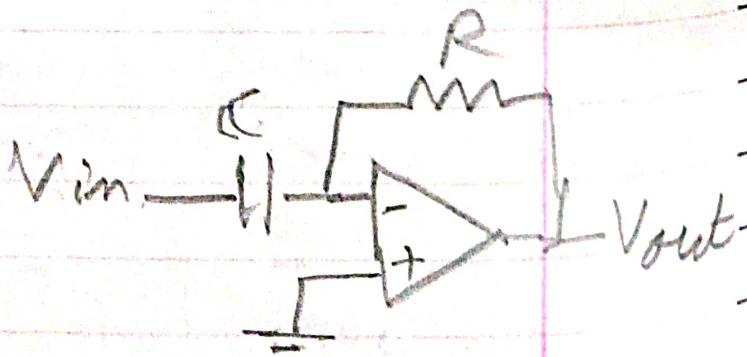
Instrumentation amplifier



$$V_{out} = \left(1 + \frac{2R_1}{R_{gain}}\right) \left(\frac{R_3}{R_2}\right) [V_2 - V_1]$$

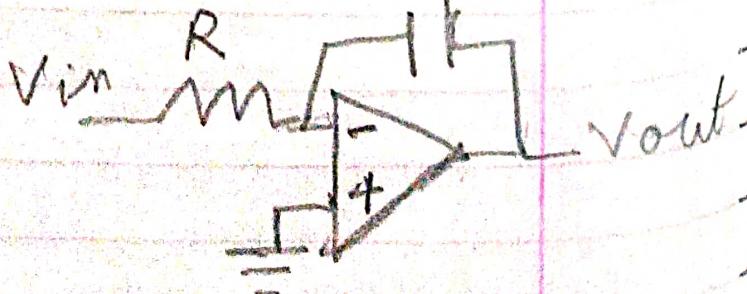
Differentiator

$$V_o = -RC \frac{dV_i}{dt}$$



Integrator

$$V_o = -\frac{1}{RC} \int v_i(t) dt$$



Low Pass



Active Filters

R

V_{in} ——————

$$\frac{1}{j\omega C}$$

$$\frac{1}{j\omega L}$$

$$V_{out} = \frac{j\omega L}{j\omega L + R} V_{in}$$

$$\phi = -\tan(\omega_c R)$$

$$= -\tan\left(\frac{\omega}{\omega_c}\right) = -\tan\left(\frac{\omega}{f_c}\right)$$

$$f_c = \frac{1}{2\pi RC}$$

2nd order

$$f_c = \frac{1}{2\pi \sqrt{R_1 C_1 R_2 C_2}}$$

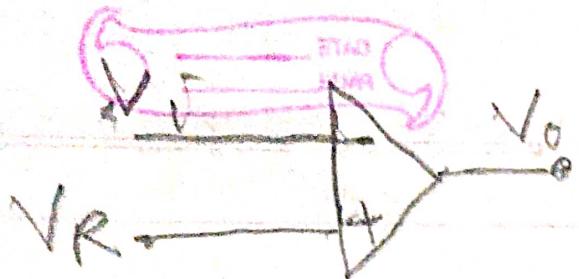
High Pass Filter

$$V_{in} \xrightarrow{\sum R} V_{out} = \frac{R}{j\omega C}$$

$$\phi = \tan\left(\frac{1}{\omega RC}\right) = \tan\left(\frac{\omega}{\omega_c}\right)$$

$$= \tan\left(\frac{f_c}{\omega}\right)$$

Inverting comparators



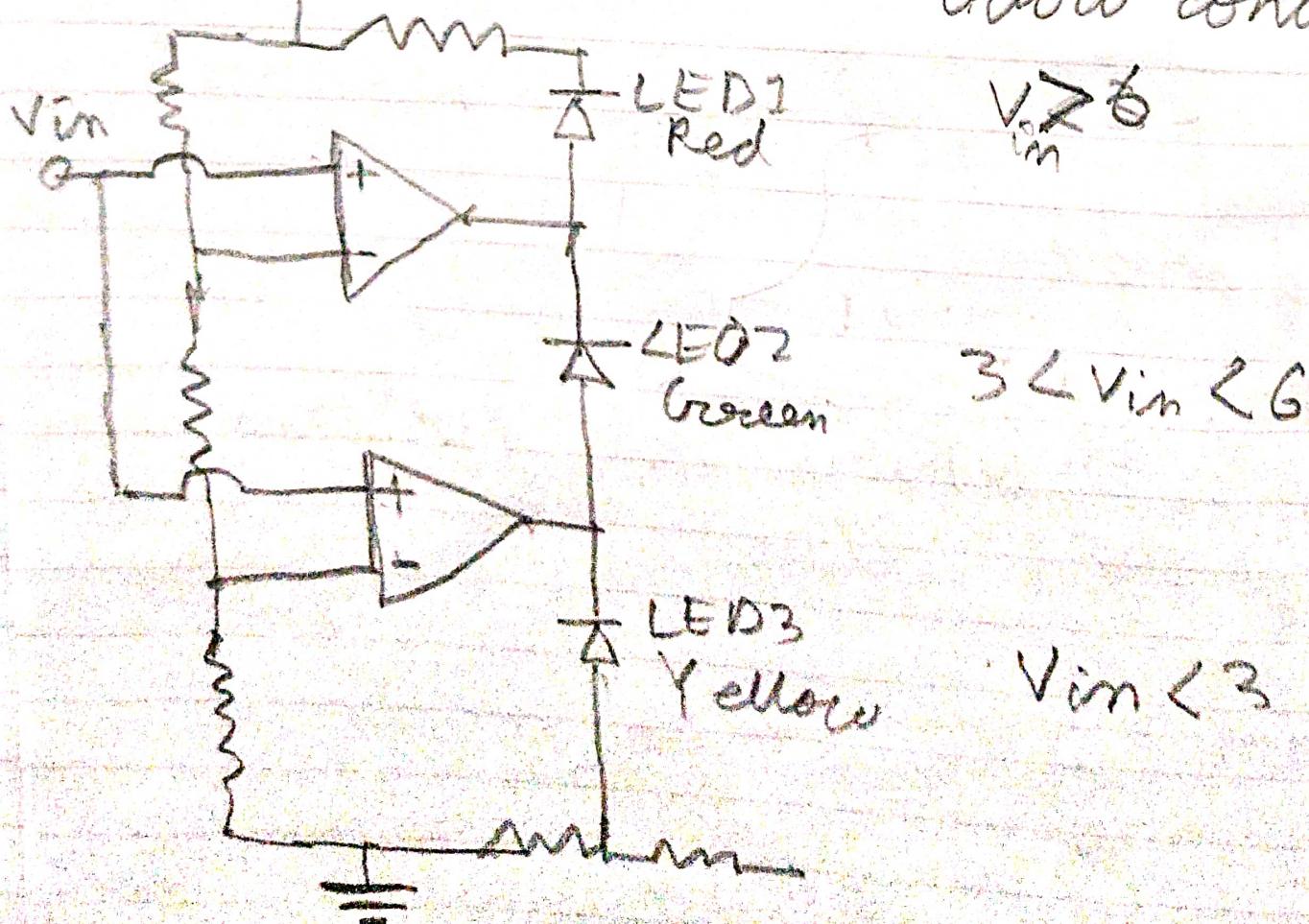
$$V_o = -V_{sat} \quad V_i > V_R$$

$$V_o = V_{sat} \quad V_i < V_R$$

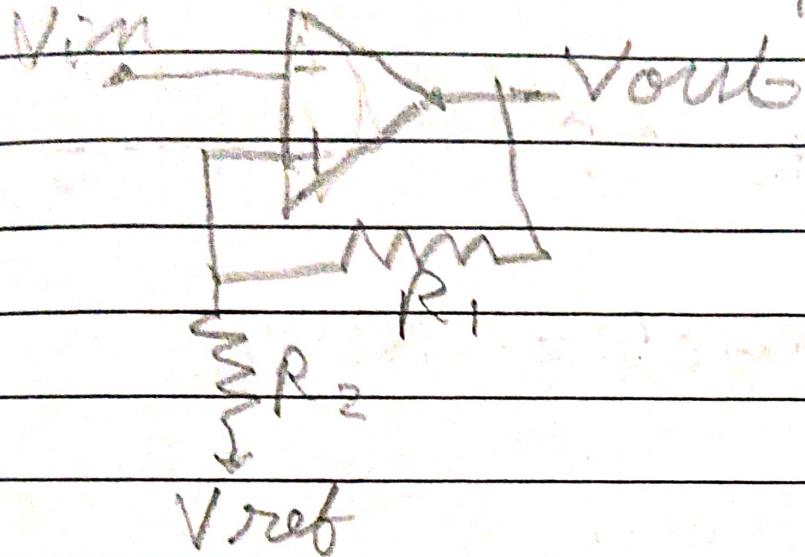
Zero crossing detectors



3-Level comparators with LED glow condition



Regenerative comparators (Schmitt trigger)



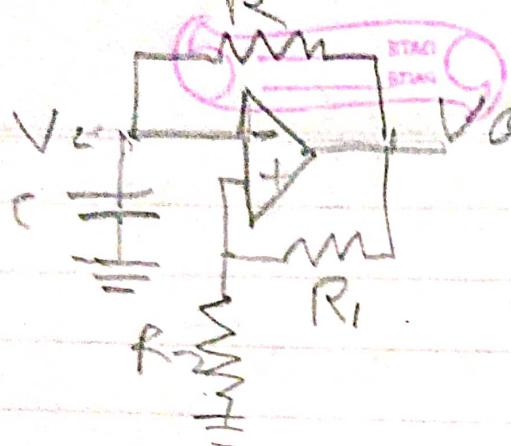
$$V_{out} = +V_{sat}$$

$$V^+ = \frac{R_1}{R_1 + R_2} V_{ref} + \frac{R_2}{R_1 + R_2} V_{sat}$$

$$V_{out} = -V_{sat}$$

$$V^+ = \frac{R_1}{R_1 + R_2} V_{ref} - \frac{R_2}{R_1 + R_2} V_{sat}$$

A stable multivibrator



Vout

$$\beta = \frac{R_2}{R_1 + R_2}$$

Duty cycle

$$= 50\%$$

$$T = 2RL \ln \left(\frac{1+\beta}{1-\beta} \right)$$

$$\text{Duty cycle} = \frac{R_1 + R_2}{R_1 + 2R_2}$$

$$t_{\text{high}} = 0.693 \times L (R_1 + R_2)$$

$$t_{\text{low}} = 0.693 \times C \times R_2$$

using
555 Timer

② Stable state

$$T = RC \ln \left(\frac{1 + V_D/V_{\text{sat}}}{1 - \beta} \right)$$

$$T = 1.1RC$$

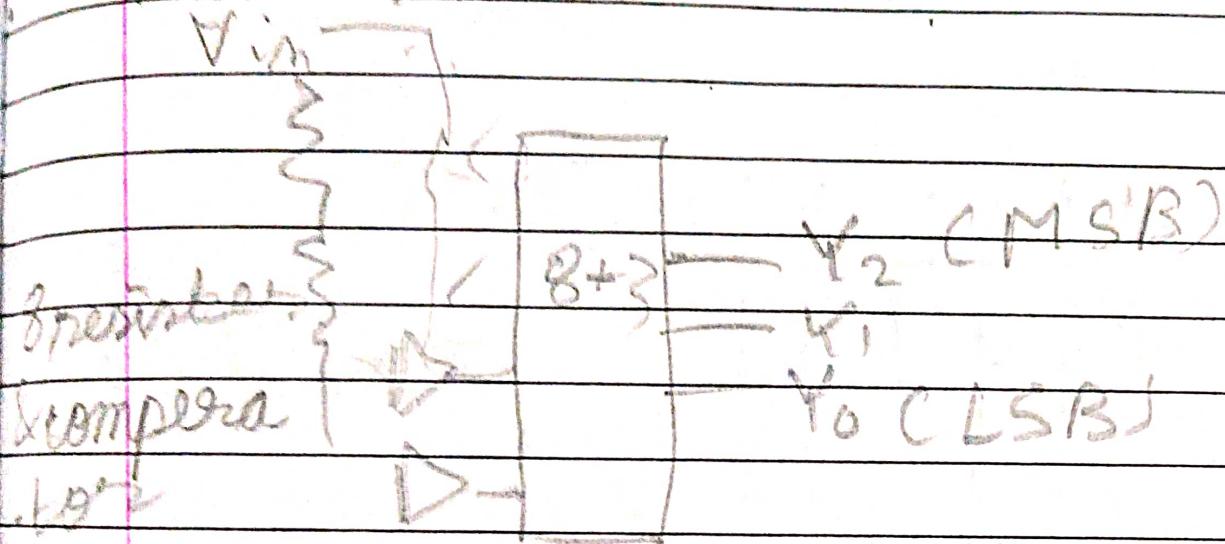
1 stable state
555 Timer

$$* V_C = V_b + (V_i - V_b) e^{-t/RC}$$



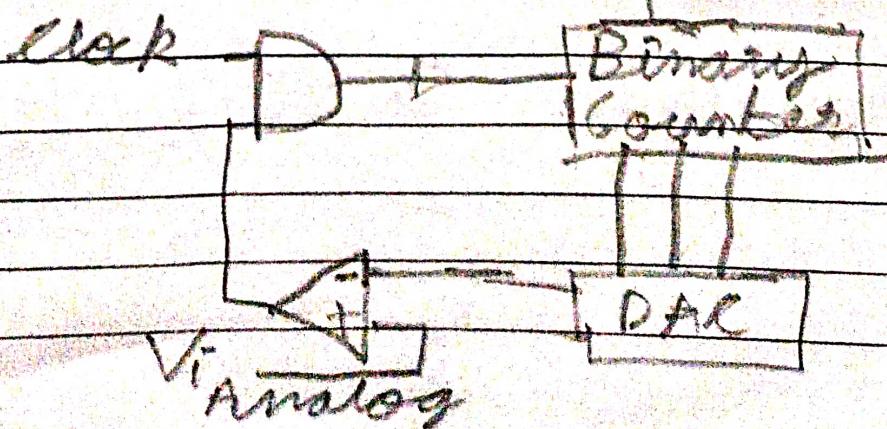
3 bit ADC

Flash / Parallel ADC



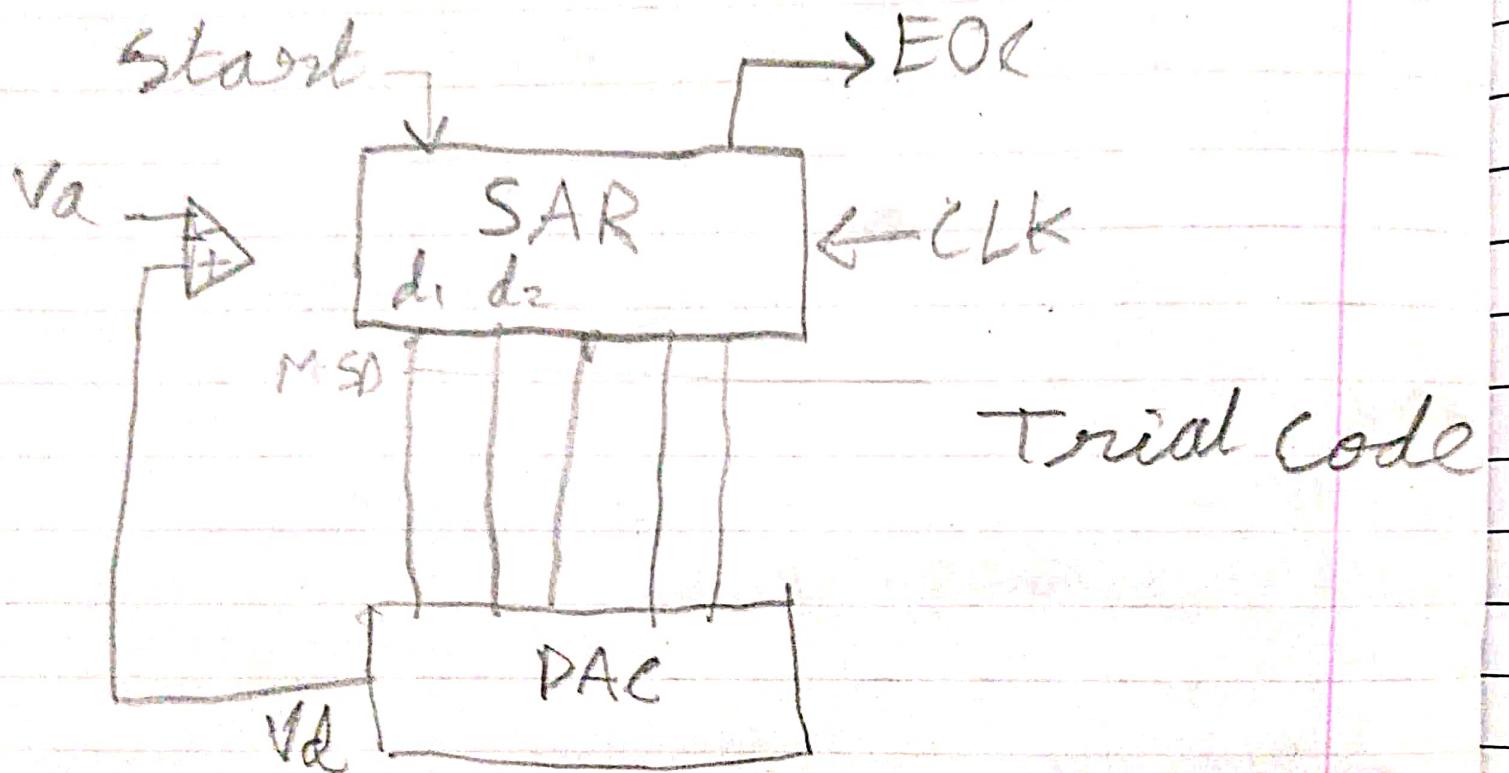
Counter type ADC

- Reset (initially set counter to zero)
- clock - signal generator
- DAC



SA ADC

- Faster than counter ADC



- Initially $D_1, D_2, D_3, D_4 = 1000$

$$V_d < V_a$$

$$D_2 \leftarrow 1$$

$$D_1, D_2, D_3, D_4 = 1100$$

$$V_d > V_a$$

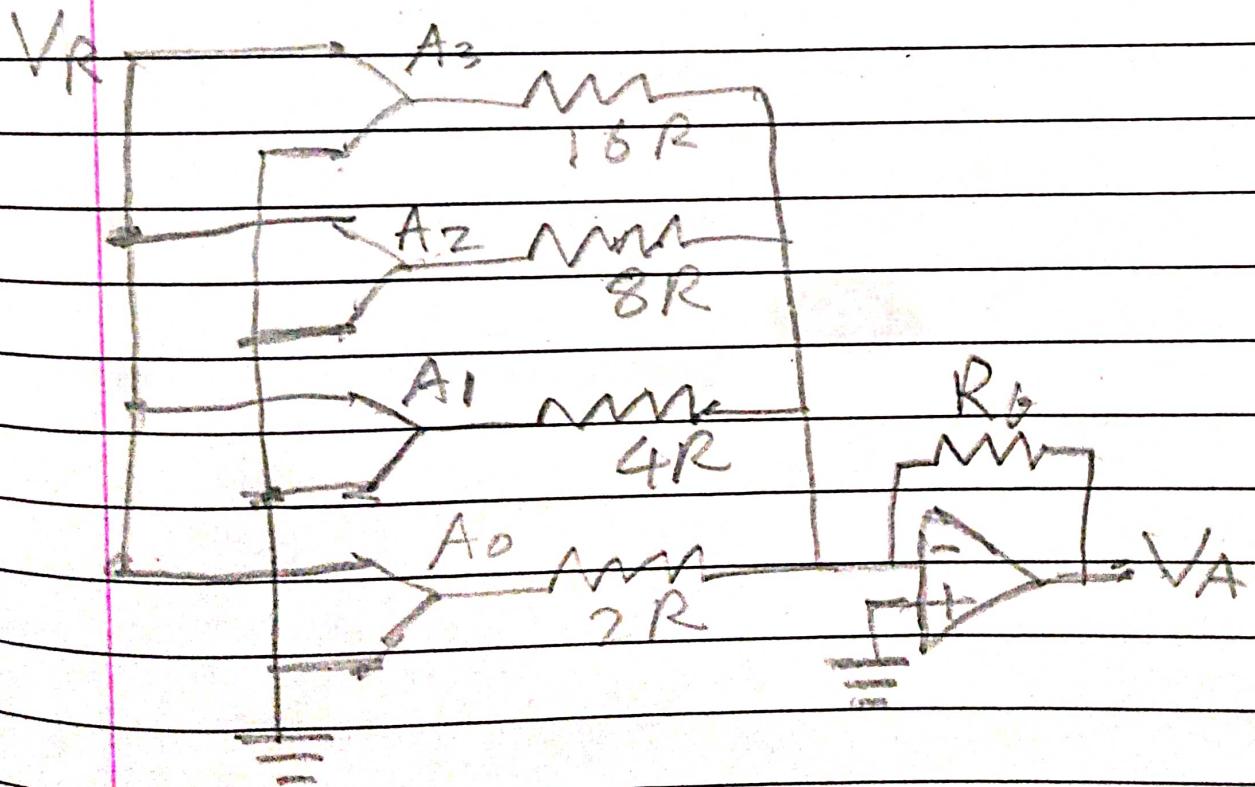
$$D_1 = 0 \quad D_2 = 1$$

$$D_1, D_2, D_3, D_4 = 0100$$

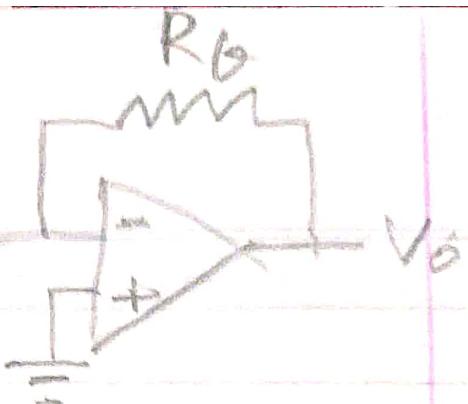
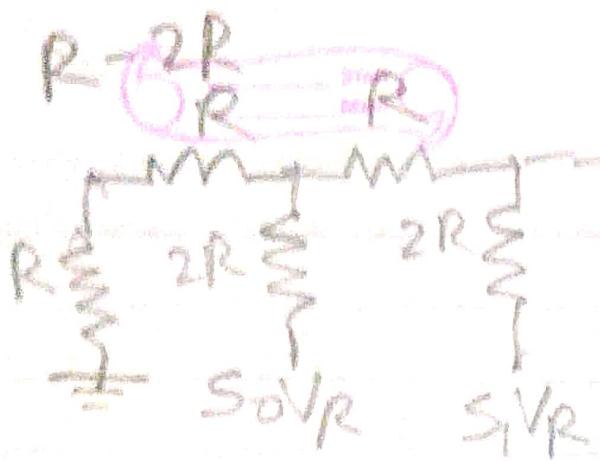
Digital to Analog converters

$$V_R = K \sum_{k=0}^{N-1} S_k 2^k$$

Binary Weighted DAC



$$V_o = -\frac{R_g}{R} V_R [S_0 2^{-1} + S_1 2^{-2} + \dots]$$



$$V_o = -\frac{R_B}{R_{TH}} \frac{V_R}{2^N} \sum_{k=0}^{N-1} S_k 2^k$$