

LAB - 2

YASH GUPTA

Experiment - 2

S20200010234

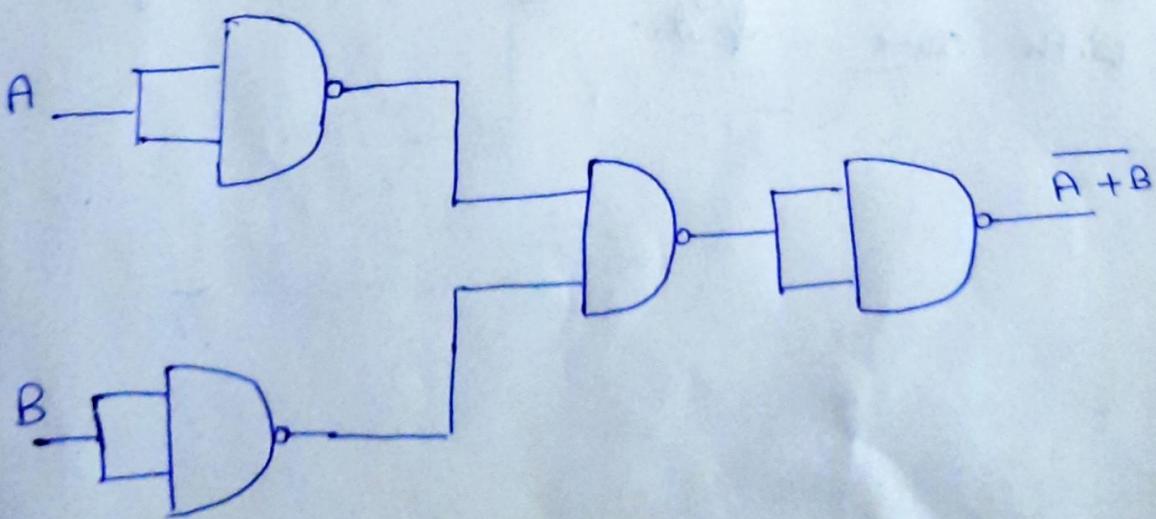
DATE:- 24/12/2020

Q-1AIM:- To build NOR gate using NANDRequirements:- Logism SoftwareTRUTH TABLE:-

Input		Output
A	B	Y
0	0	1
1	0	0
0	1	0
1	1	0

Boolean Expression:-

$$\text{Y} = \overline{A + B} \quad \text{--- (1)}$$

Logic Circuits:-

CHECKING THE CIRCUIT

Case (i) If $A = "0"$, $B = "0"$, then $Y = 1$,

Substitute in Eq ①

$$Y = \overline{A+B}$$

$$Y = \overline{0+0}$$

$$Y = \bar{0}$$

$$Y = 1, \text{ Satisfied}$$

Case (ii) If $A = "1"$, $B = "0"$, then $Y = 0$,

Substitute in Eq ①

$$Y = \overline{A+B}$$

$$Y = \overline{1+0}$$

$$Y = \bar{1}$$

$$Y = 0, \text{ Satisfied}$$

Conclusion: The NOR gate using NAND gate
is simulated using logicism and verified
with some inputs.

Q-2

AIM :- To build XOR gate using NAND

Requirements :- Logism Software

TRUTH TABLE :-

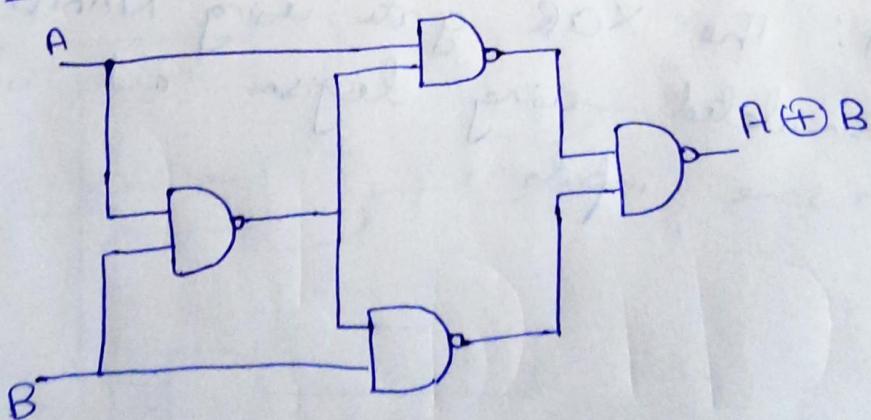
Input		Output
A	B	C
0	0	0
1	0	1
0	1	1
1	1	0

Boolean Expression :-

$$C = A \oplus B$$

$$C = A\bar{B} + \bar{B}A \quad - \text{Eq(1)}$$

Logical Circuits :-



CHECKING THE CIRCUIT

Case(i) If $A = "0"$, $B = "0"$, then $C = "0"$.

Substitute in Eq ①

$$C = A\bar{B} + B\bar{A}$$

$$C = 0\bar{0} + 0\bar{0}$$

$$C = 0 \cdot 1 + 0 \cdot 1$$

$$C = 0 \quad , \text{ Satisfied}$$

Case (ii) If $A = "1"$, $B = "0"$, then $C = "1"$,

Substitute in Eq ①

$$C = A\bar{B} + B\bar{A}$$

$$C = 1\bar{0} + 0\bar{1}$$

$$C = 1 \cdot 1 + 0 \cdot 0$$

$$C = 1 \quad , \text{ Satisfied}$$

Conclusion: The XOR gate using NAND gate is simulated using logicism and verified with some inputs.

Q-3

AIM:- To build Half adder

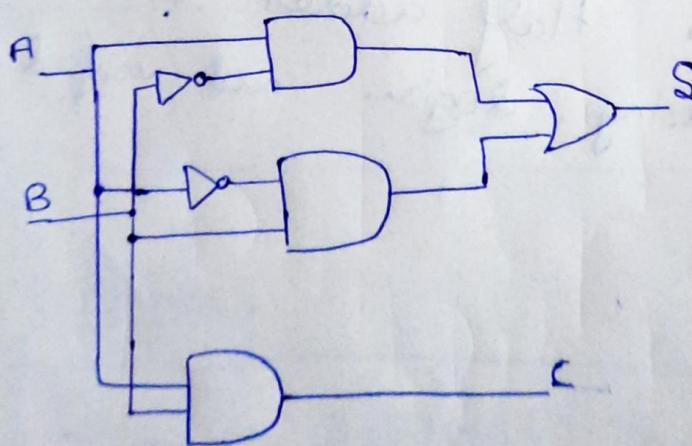
Requirements :- Logism Software

TRUTH TABLE :-

Inputs		Outputs	
A	B	S	C
0	0	0	0
1	0	1	0
0	1	1	0
1	1	0	1

Boolean Expression :- $S = A \oplus B$
 $C = AB$

Logical Circuits :



CHECKING THE CIRCUIT

Case(i) If $A = "1"$, $B = "0"$, then $S = "1"$, $C = "0"$

Substitute in Eq.

$$S = A\bar{B} + B\bar{A}$$

$$S = 1 \cdot 0 + 0 \cdot 1$$

$$S = 0 + 0$$

$$S = 0, \text{ Satisfied}$$

$$C = A\bar{B}$$

$$C = 1 \cdot 0$$

$$= 0, \text{ Satisfied}$$

Case (ii) If $A = "1"$, $B = "1"$, then $S = "0"$, $C = "1"$

Substitute in Eq.

$$S = A\bar{B} + B\bar{A}$$

$$S = 1 \cdot 1 + 1 \cdot 1$$

$$S = 0 + 0$$

$$S = 0, \text{ Satisfied}$$

$$C = A \cdot \bar{B}$$

$$= 1 \cdot 1$$

$$= 1, \text{ Satisfied}$$

Conclusion: The Half adder circuit is simulated using logic and verified with some inputs.

Q-4

AIM: To build Full Adder

Requirements: Logical Software

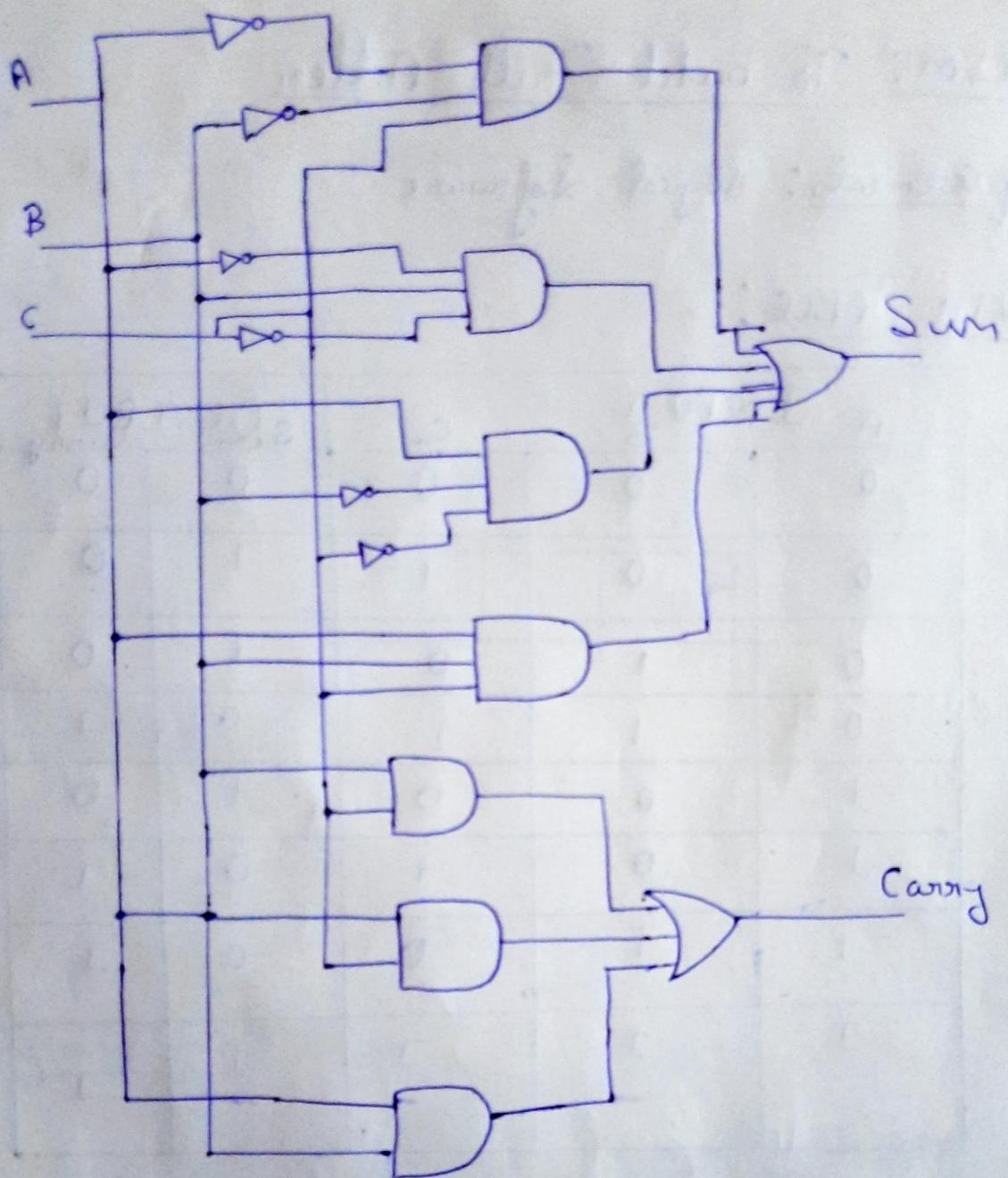
TRUTH TABLE:

A	INPUT B	C _{in}	SUM	CARRY
0	0	0	0	0
0	0	1	1	0
0	1	0	1	0
0	1	1	0	1
1	0	0	1	0
1	0	1	0	1
1	1	0	0	1
1	1	1	1	1

Boolean Expression:

$$\text{Sum} = A \oplus B \oplus C_{in}$$

$$\text{Carry} = AB + (A \oplus B)C_{in}$$



CHECKING THE CI RCUT

Case(i) If $A = "0"$, $B = "0"$, $C_n = "1"$, then
 Sum = "1", Carry = "0"

$$\begin{aligned} \text{Substituting in Eq^*} \\ \text{Sum} &= \bar{A}\bar{B}C_n + \bar{A}B\bar{C}_n + A\bar{B}C_n + ABC \\ &= 0 \cdot 0 \cdot 1 + 0 \cdot 0 \cdot \bar{1} + 0 \cdot 0 \cdot 1 + 0 \cdot 0 \cdot 1 \\ &= 1 \cdot 1 \cdot 1 + 0 + 0 + 0 \\ &= 1 \quad , \text{Satisfied} \\ \text{Carry} &= AB + BC_n + C_nA \\ &= 0 \cdot 0 + 0 \cdot 1 + 1 \cdot 0 \\ &= 0 \quad , \text{Satisfied} \end{aligned}$$

Case(ii) If $A = "1"$, $B = "1"$, $C_n = "0"$, then

$$\begin{aligned} \text{Sum: } 0, \text{ Carry: } 1 \\ \text{Sum} &= \bar{A}\bar{B}C_n + A\bar{B}\bar{C}_n + \bar{A}B\bar{C}_n + ABC \\ &= \bar{1}\bar{1}\cdot 0 + 1\cdot \bar{1}\cdot 0 + \bar{1}\cdot 1\cdot 0 + 1\cdot 1\cdot 0 \\ &= 0 + 0 + 0 + 0 \\ &= 0 \quad , \text{Satisfied} \\ \text{Carry} &= AB + BC_n + C_nA \\ &= 1\cdot 1 + 1\cdot 0 + 0\cdot 1 \\ &= 1 \quad , \text{Satisfied} \end{aligned}$$

Conclusion: The full adder circuit is simulated using logic and verified with some inputs.

Q.5

AIM: To build Half Subtractor

Requirements: Logical Software

Truth Table:

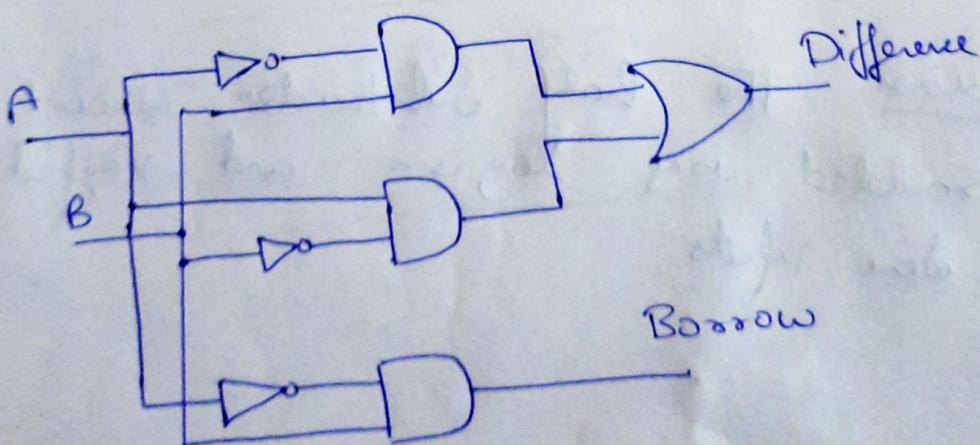
Input		Output	
A	B	Difference	Borrow
0	0	0	0
0	1	1	1
1	0	1	0
1	1	0	0

Boolean Expression:

$$\text{Difference} = A \oplus B$$

$$\text{Borrow} = \bar{A}B$$

Logical Circuit:



CHECKING THE CIRCUIT

Case(i) If $A = "0"$, $B = "1"$, then difference = I^{\bullet} ,
 $\therefore B_{\text{borrow}} = "1"$

Substitute in eq

$$\begin{aligned} \text{Difference} &= A\bar{B} + B\bar{A} & \text{Borrow} &= \bar{A}B \\ &= 0\bar{1} + 1\bar{0} & &= \bar{0}\cdot 1 \\ &= 00 + 11 & &= 1\cdot 1 \\ &= 1 & &= 1 \end{aligned}$$

Case(ii) If $A = "1"$, $B = "1"$, then difference = $"0"$

Substitute in eq

$$\begin{aligned} \text{Difference} &= A\bar{B} + B\bar{A} & \text{Borrow} &= \bar{1}\bar{1} \\ &= 1\bar{1} + 1\bar{1} & &= 0\cdot 1 \\ &= 0+0 & &= 0 \\ &= 0 & & \end{aligned}$$

Conclusion: The half Subtractor circuit is simulated using logicism and verified with some inputs.

D-6

AIM : To build full Subtractor

Requirements : Logical Software

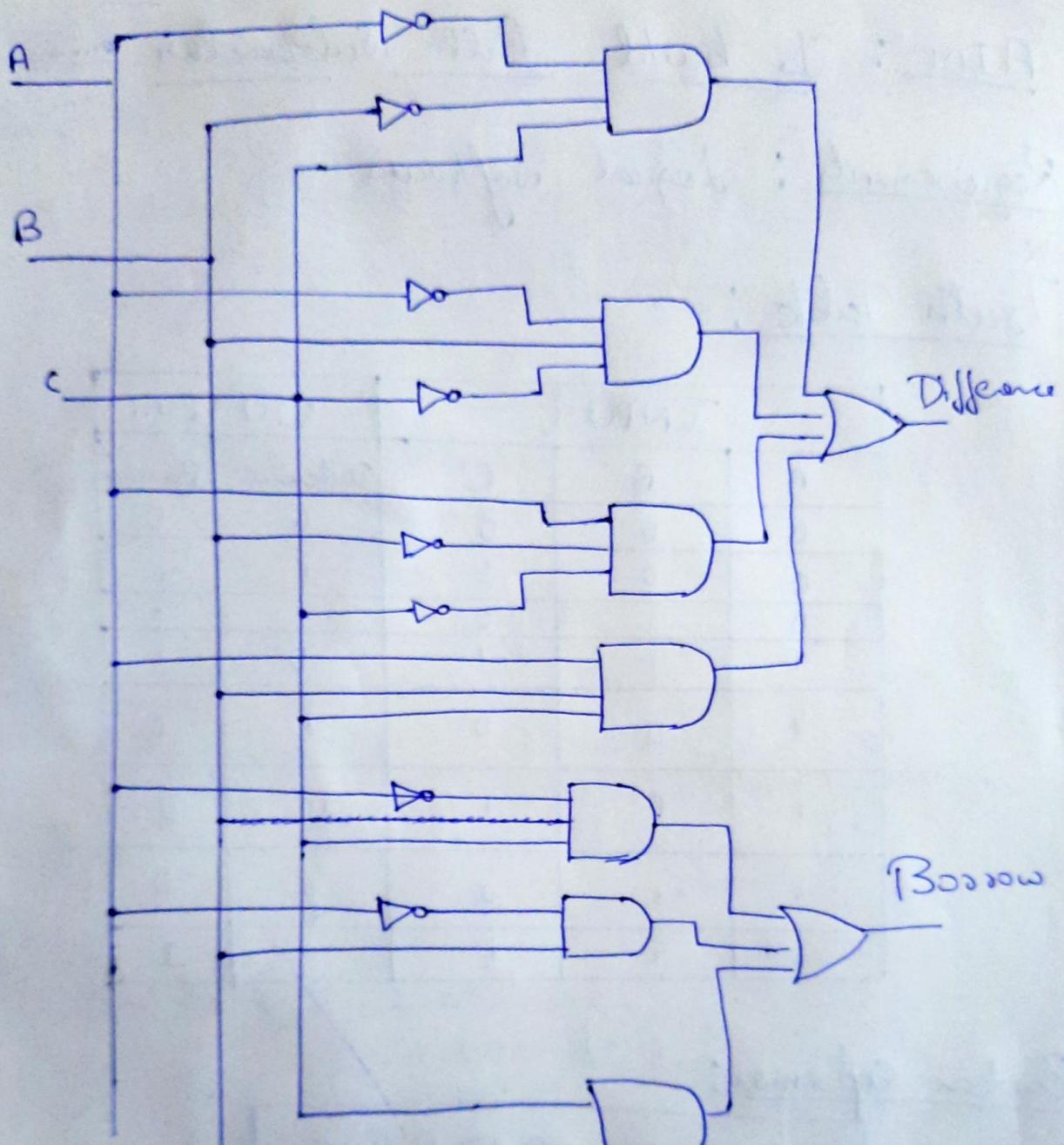
Truth Table :

INPUT.			OUTPUT	
A	B	C	Difference	Borrow
0	0	0	0	0
0	0	1	1	1
0	1	0	1	1
0	1	1	0	1
1	0	0	1	0
1	0	1	0	0
1	1	0	0	0
1	1	1	1	1

Boolean Expression:

$$\text{Difference} = A \oplus B \oplus C$$

$$\text{Borrow} = \bar{A}C + \bar{A}B + BC$$



CHECKING THE CIRCUIT

Case(i) If $A = "0"$, $B = "0"$, $C = 1$, then
 Difference = 1, Borrow = 1

Substituting in Eq'

$$\begin{aligned}\text{Difference} &= A\bar{B}\bar{C} + B\bar{A}\bar{C} + C\bar{A}\bar{B} + ABC \\ &= 0 \cdot 0 \cdot 1 + 0 \cdot 0 \cdot 1 + 1 \cdot 0 \cdot 0 + 0 \cdot 0 \cdot 1 \\ &= 0 + 0 + 1 + 0 \\ &= 1, \text{ Satisfied}\end{aligned}$$

$$\begin{aligned}\text{Borrow} &= \bar{A}C + \bar{A}B + BC \\ &= \bar{0}1 + \bar{0}0 + 0 \cdot 1 \\ &= 1 + 0 + 0 \\ &= 1, \text{ Satisfied}\end{aligned}$$

Case(ii) If $A = "1"$, $B = "1"$, $C = "0"$, then $\ell = 0$

Difference = 0, Borrow = 0

$$\begin{aligned}\text{Difference} &= A\bar{B}\bar{C} + B\bar{A}\bar{C} + C\bar{A}\bar{B} + ABC \\ &= 1 \cdot 1 \cdot 0 + 1 \cdot 1 \cdot 0 + 0 \cdot 1 \cdot 1 + 1 \cdot 1 \cdot 0 \\ &= 0 + 0 + 0 + 0 \\ &= 0\end{aligned}$$

$$\begin{aligned}\text{Borrow} &= \bar{A}C + \bar{A}B + BC \\ &= \bar{1} \cdot 0 + \bar{1} \cdot 1 + 1 \cdot 0 \\ &= 0 + 0 + 0 \\ &= 0\end{aligned}$$

Conclusion: The full subtractor circuit is simulated using logicism and verified with some inputs.