Yash Pote

Ph.D. Candidate School of Computing National University of Singapore

EDUCATION

Ph.D. School of Computing, National University of Singapore, 2019-B.Tech. Computer Science and Engineering, IIT - Guwahati, 2014-2018

RESEARCH AREAS

Solving problems beyond NP.

Verification of Probabilistic Reasoning Systems.

PUBLICATIONS

Conference Proceedings/Compendia

2019	Yash Pote, Saurabh Joshi and Kuldeep S. Meel "Phase Transition Behavior of Cardinality and
	XOR Constraints", Proc. of IJCAI, 2019 (URL, Slides)

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Web: https://yashpote.com

2020 Kuldeep S. Meel, Yash Pote and Sourav Chakraborty "On Testing of Samplers", Proc. of NeurIPS, 2020 (URL)

Manuscripts in Preparation

2021	Yash Pote and Kuldee	ง S. Meel. "	Testing Probabi	llistic Circuits". '	Target: <i>UAI</i> , 2021.

- Cheng-Kai Lim, Dehui Lin, Yash Pote, Yasamin Tabatabaee, Djordje Jevdjic, "Mind the Position: Reliability Bias in DNA Data Storage", Target: *ISCA*, 2021.
- Durgesh Agarwal, Yash Pote and Kuldeep S. Meel, "Partition Function Estimation: A Quantitative Study", Target: *IJCAI-Survey Track*, 2021.

TEACHING EXPERIENCE

National University of Singapore

CS 4244: Knowledge Representation and Reasoning (Teaching Assistant-Spring 2019,2020)

CS 4269/CS 5469: Fundamentals of Logic in Computer Science (Teaching Assistant-Winter 2019)

CS 4218: Software Testing (Lab Tutor-Spring 2021)

PROFESSIONAL EXPERIENCE

2017 Goldman Sachs, Summer Intern in the Global Securities Team Bangalore, India;