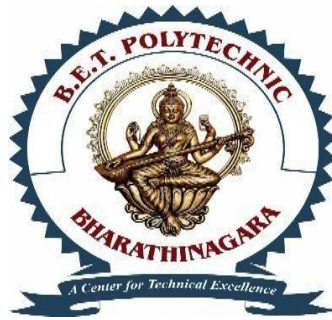


B.E.T POLYTECHNIC

Bharthinagara (K.M Doddi), Manday-571422

Department of Collegiate and Technical Education

[Recognized by the Govt of Karnataka approved by AICTE, New Delhi]



MINI PROJECT REPORT

ON

Bank DataBase

SUBMITTED BY

YASHUNANDAN V(410CS23058)

BHYREGOWDA A.P (410CS23009)

POOJA URS S(410CS24701)

Under the Guidance of

Mrs. Ramya M.P BE., M. tech.,

Lecturer

Computer Science & Engg

B.E.T POLYTECHNIC

[Recognized by the Govt of Karnataka approved by AICTE, New Delhi]

Bharathinagara (K.M Doddi), Manday-571422



Department of Computer Science & Engg

CERTIFICATE

This is to certify that the activity entitled “**Bank DataBase**” is a bona fide work carried out by **Mr. Yashunandan v, Bhyregowda .A.P, Pooja urs S** bearing **REG NO:410CS23058,410CS23009,410CS24701** a student of **B.E.T.Polytechnic** in partial fulfillment for the award of **Diploma in Computer science & Engineering** of the B.E.T.Polytechnic during the year 2024-25 It is certified that all corrections/suggestions indicated for Activity Points have been executed under the directions of **Mrs. Ramya M.P.** The project report has been approved as it satisfies the academic requirements in respect of AICTE Activity Points prescribed for the said Diploma

Mrs.Ramya M.P

Lecturer
Dept. Computer science & Engineering
B.E.T POLYTECHNIC
Bharathinagara

Mr.B. Mahadeva

Head of the Department
Dept. Computer science & Engineering
B.E.T POLYTECHNIC
Bharathinagara

G.Krishna

Principal
B.E.T POLYTECHNIC
Bharathinagara

DECLARATION

We, **Yashunandan V, Bhyregwoda .A.P, Pooja urs S** bearing the USN **410CS23058,410CS23009,410CS24701** studentS of 3rd semester Diploma Department of Computer science & Engineering, **B.E.T.Polytechnic Bharathinagara** declare that the mini Project work entitled “**Bank DataBase**”, has been duly executed by me under the guidance of miss.Ramya M.P lecture, Dept Computer science &Engineering,B.E.T polytechnic, Bharathinagara, The mini Project report of the same is submitted in partial fulfillment of the requirement for the award Diploma in Department of Computer science & Engineering by B.E.T.Polytechnic Bharthinagara during the year 2024-2025.

Date:17/12/2024

Place: Bharathinagara

Mr. Yashunandan V

410CS23058

Mr. Bhyregowda A.P

410CS23009

Miss. Pooja urs S

410CS24701

ACKNOWLEDGEMENT

The satisfaction that accompanies the successful completion of the mini project report which would be complete only with the mention of the almighty God and the people who made it possible, whose report rewarded the effort with success of mini project presentation

We are grateful to **B.E.T.Polytechnic** for providing us an opportunity to enhance our knowledge through the project.

We express our sincere thanks to **G Krishna**, Principal, and B.E.T. Polytechnic Bharathinagara for providing us an opportunity and means to present the mini project.

We express our heart full thanks to **B. Mahadeva**, Head of the Department, Department of Computer Science Engineering, B.E.T. Polytechnic Bharathinagara for encouragement in our mini project work, whose cooperation and guidance helped in nurturing this mini project report.

We would like to express profound thanks to the mini project guide Miss. **Ramya M.P**, lecturer, Department of Computer science & Engineering for the keen interest and encouragement in our mini project presentation.

Finally, we would like to thank our family members and friends for standing with us through all times.

Table of Contents

1. INTRODUCTION

- 1.1. Overview
- 1.2. About the Project
- 1.3. Bank Profile
- 1.4. Our Culture
- 1.5. Purpose
- 1.6. Scope
- 1.7. Functional Components of the Project
- 1.8. Functionality to Enhance the Project

2. SYSTEM ANALYSIS

- 2.1. Present System
- 2.2. Proposed System
- 2.3. Benefits of the System
- 2.4. System can be used Extensively

3. FEASIBILITY REPORT

- 3.1. Understanding Feasibility
- 3.2. Tech NIC ASIA al Feasibility
- 3.3. Operational Feasibility
- 3.4. Economic Feasibility

4. SOFTWARE REQUIREMENT SPECIFICATIONS

5. SYSTEM DESIGN

- 5.1. Introduction
- 5.2. Normalization
- 5.3. ER-Diagrams
- 5.4. DFDs
- 5.5. Database Table

CODING

6. CODING AND OUTPUT SCREENS

7. SYSTEM TESTING AND IMPLEMENTATION

- 7.1. Introduction
- 7.2. Strategic Approach OF Software Testing
- 7.3. Unit Testing
- 7.4. Test

8. SYSTEM SECURITY

- 8.1. Introduction
- 8.2. Security In Software

9. CONCLUSION & FUTURE IMPROVEMEN