

Lab No. 4: Implementation of Error Detection and Error Correction Techniques.

Objectives

- Design and Implementation of Error detection in transmitted data at the receiver side using Parity check and Cyclic Redundancy Check.
- Design and Implementation of error detection and error correction in transmitted data at the receiver side using Hamming code.

Error Control Mechanism

Error control mechanism plays an important role in the transmission of data from one source to another. The noise also gets added into the data when it transmits from one system to another, which causes errors in the received binary data at other systems. The bits of the data may change (either 0 to 1 or 1 to 0) during transmission.

Error control mechanism may involve two possible ways:

- Error detection
- Error correction

It is impossible to avoid the interference of noise, but it is possible to get back the original data. For this purpose, we first need to detect either an error is present or not using error detection codes. If the error is present in the code, then we will correct it with the help of error correction codes. The error detection codes are the code used for detecting the error in the received data bitstream. In these codes, some bits are included appended to the original bitstream.

Types of Errors

There may be three types of errors:

- **Single bit error**



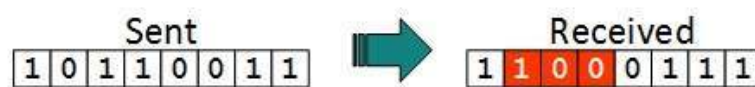
In a frame, there is only one bit, anywhere though, which is corrupt.

- **Multiple bits error**



Frame is received with more than one bits in corrupted state.

- **Burst error**



Frame contains more than 1 consecutive bits corrupted.

To detect errors, a common technique is to introduce redundancy bits that provide additional information. Various techniques for error detection include: Parity check, Checksum, and CRC

Error-detecting codes encode the message before sending it over the noisy channels. The encoding scheme is performed in such a way that the decoder at the receiving can find the errors easily in the receiving data with a higher chance of success.

- These codes are used when we use message backward error correction techniques for reliable data transmission. A feedback message is sent by the receiver to inform the sender whether the message is received without any error or not at the receiver side. If the message contains errors, the sender retransmits the message.
- In error detection codes, in fixed-size blocks of bits, the message is contained. In this, the redundant bits are added for correcting and detecting errors.
- These codes involve checking of the error. No matter how many error bits are there and the type of error.
- Parity check, Checksum, and CRC are the error detection techniques.

Algorithm

Parity check:

One extra bit is sent along with the original bits to make number of 1s either even in case of even parity, or odd in case of odd parity.

The sender while creating a frame counts the number of 1s in it. For example, if even parity is used and number of 1s is even then one bit with value 0 is added. This way number of 1s remains even. If the number of 1s is odd, to make it even a bit with value 1 is added.



The receiver simply counts the number of 1s in a frame. If the count of 1s is even and even parity is used, the frame is considered to be not-corrupted and is accepted. If the count of 1s is odd and odd parity is used, the frame is still not corrupted.

If a single bit flips in transit, the receiver can detect it by counting the number of 1s. But when more than one bits are erroneous, then it is very hard for the receiver to detect the error.

At sender side:

1. Start the program
2. Total number of 1's in the data unit to be transmitted is counted.
3. The total number of 1's in the data unit is made even in case of even parity.
The total number of 1's in the data unit is made odd in case of odd parity.
4. This is done by adding an extra bit called as parity bit.
5. The newly formed code word (Original data + parity bit) is transmitted to the receiver.
6. Stop the program

At receiver side :

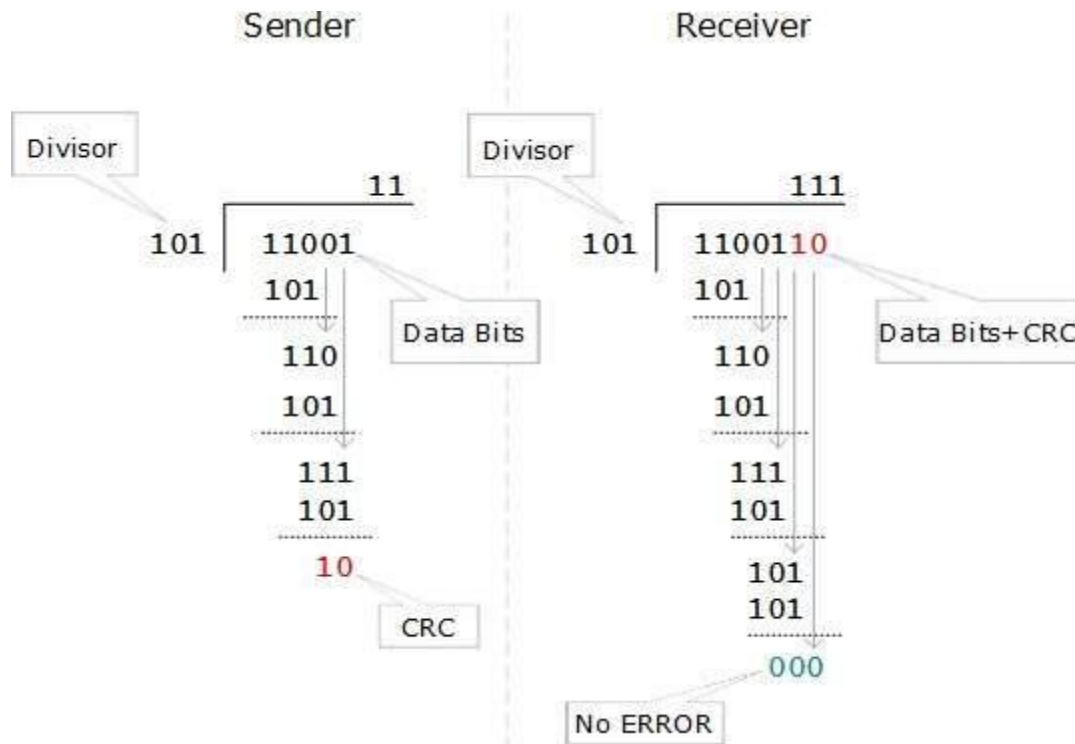
1. Start the program
2. Receiver receives the transmitted code word.
3. The total number of 1's in the received code word is counted.

Then, following cases are possible-
4. If total number of 1's is even and even parity is used, then receiver assumes that no error occurred.
5. If total number of 1's is even and odd parity is used, then receiver assumes that error occurred.
6. If total number of 1's is odd and odd parity is used, then receiver assumes that no error occurred.
7. If total number of 1's is odd and even parity is used, then receiver assumes that error occurred.
8. Stop

Cyclic Redundancy Check:

CRC is a different approach to detect if the received frame contains valid data. This technique

involves binary division of the data bits being sent. The divisor is generated using polynomials. The sender performs a division operation on the bits being sent and calculates the remainder. Before sending the actual bits, the sender adds the remainder at the end of the actual bits. Actual data bits plus the remainder is called a codeword. The sender transmits data bits as codewords.



At the other end, the receiver performs division operation on codewords using the same CRC divisor. If the remainder contains all zeros the data bits are accepted, otherwise it is considered as there some data corruption occurred in transit.

At sender side:

1. Start the program
2. A string of n 0's is appended to the data unit to be transmitted. Here, n is one less than the number of bits in CRC generator.
3. Binary division is performed of the resultant string with the CRC generator.
4. After division, the remainder so obtained is called as CRC. It may be noted that CRC also consists of n bits.
5. The CRC is obtained after the binary division. The string of n 0's appended to the data unit earlier is replaced by the CRC remainder.
6. The newly formed code word (Original data + CRC) is transmitted to the receiver.
7. Stop

At receiver side:

1. Start the program
2. The transmitted code word is received.
3. The received code word is divided with the same CRC generator.
4. On division, the remainder so obtained is checked.
 - a. If the remainder is zero, Receiver assumes that no error occurred in the data during the transmission. Receiver accepts the data.
 - b. If the remainder is non-zero, Receiver assumes that some error occurred in the data during the transmission. Receiver rejects the data and asks the sender for retransmission.
5. Stop

Error Correction:

In the digital world, error correction can be done in two ways:

- **Backward Error Correction:** When the receiver detects an error in the data received, it requests back the sender to retransmit the data unit.
- **Forward Error Correction:** When the receiver detects some error in the data received, it executes error-correcting code, which helps it to auto-recover and to correct some kinds of errors.

The first one, Backward Error Correction, is simple and can only be efficiently used where retransmitting is not expensive. For example, fiber optics. But in case of wireless transmission retransmitting may cost too much. In the latter case, Forward Error Correction is used.

A single additional bit can detect the error, but cannot correct it. For correcting the errors, one has to know the exact position of the error. For example, If we want to calculate a single-bit error, the error correction code will determine which one of seven bits is in error. To achieve this, we have to add some additional redundant bits. Suppose r is the number of redundant bits and d is the total number of the data bits. The number of redundant bits r can be calculated by using the formula: $2^r \geq d + r + 1$. The value of r is calculated by using the above formula. For example, if the value of d is 4, then the possible smallest value that satisfies the above relation would be 3. To determine the position of the bit which is in error, a technique developed by R.W Hamming is Hamming code which can be applied to any length of the data unit and uses the relationship between data units and redundant units.

There are several types of block codes, including:

- **Hamming Codes:** Hamming codes are a type of block code that can detect and correct single-bit errors. They are commonly used in digital systems to ensure the accuracy of transmitted data.

- **Reed-Solomon Codes:** Reed-Solomon codes are a type of block code that can correct multiple-bit errors. They are commonly used in storage systems, such as CD-ROMs and DVDs, to ensure the integrity of stored data.
- **BCH Codes:** BCH (Bose–Chaudhuri–Hocquenghem) codes are a type of block code that can correct a specific number of errors. They are commonly used in digital communication systems to ensure the accuracy of transmitted data.

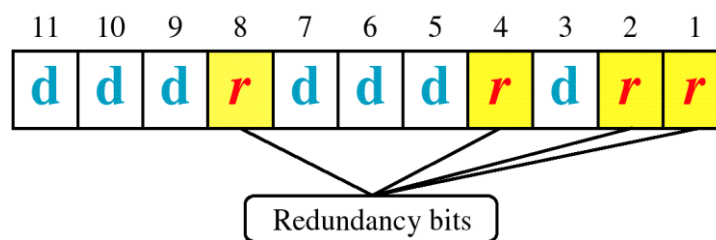
Hamming Code

It is a technique developed by R.W. Hamming for error correction. The number of redundant bits can be calculated using the following formula: $2^r \geq m + r + 1$ where, r = redundant bit, m = data bit. Suppose the number of data bits is 7, then the number of redundant bits can be calculated using: $2^4 = 16 \geq 7 + 4 + 1$ Thus, the number of redundant bits = 4 Parity bits.

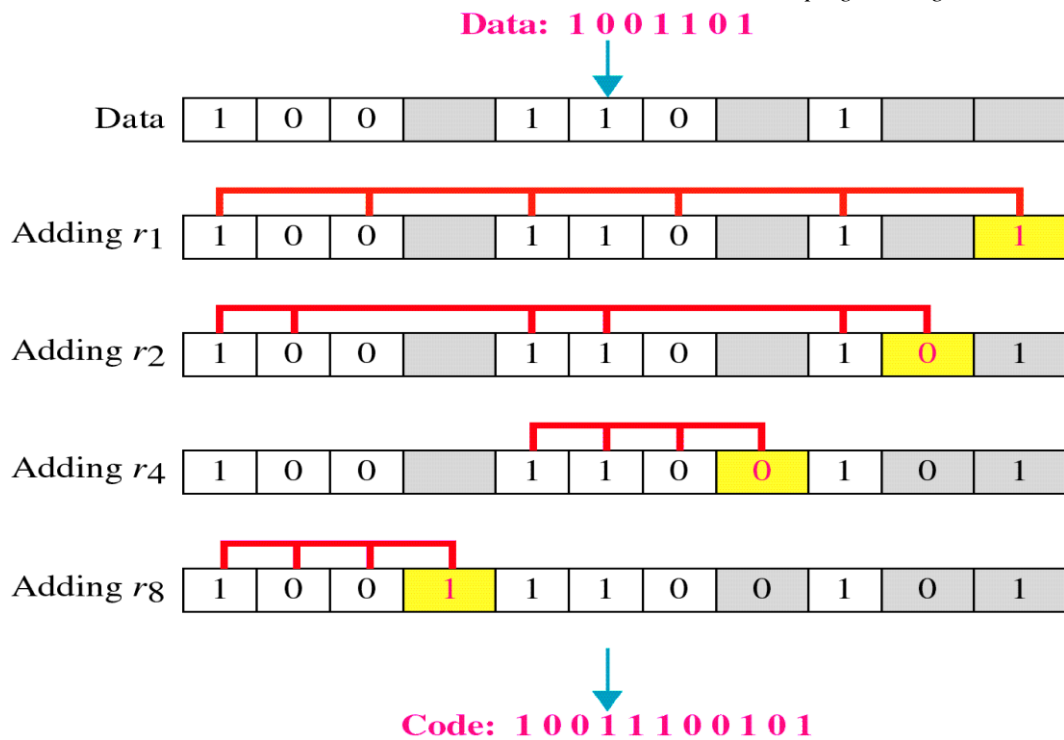
Hamming Code is simply the use of extra parity bits to allow the identification of an error.

At sender side:

1. Start the program
2. Write the bit positions starting from 1 in binary form (1, 10, 11, 100, etc).
3. All the bit positions that are a power of 2 are marked as parity bits (1, 2, 4, 8, etc). All the other bit positions are marked as data bits.



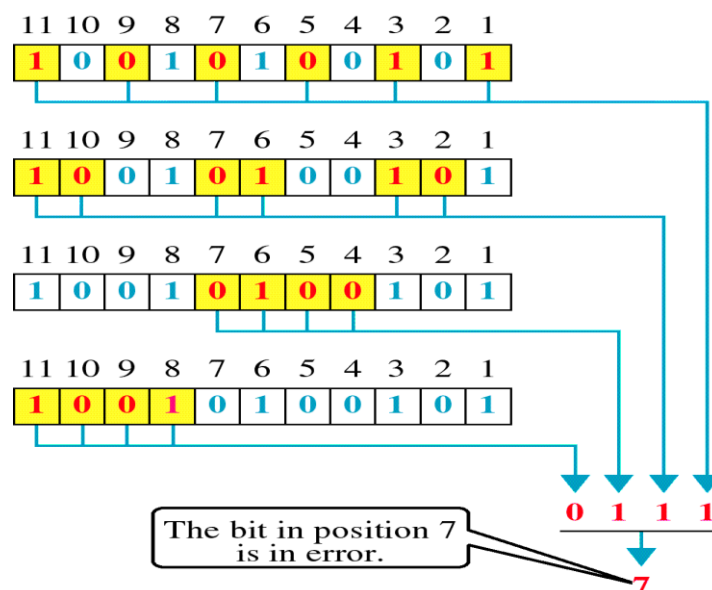
4. Each data bit is included in a unique set of parity bits, as determined its bit position in binary form.
 - a. Parity bit 1 covers all the bits positions whose binary representation includes a 1 in the least significant position (1, 3, 5, 7, 9, 11, etc).
 - b. Parity bit 2 covers all the bits positions whose binary representation includes a 1 in the second position from the least significant bit (2, 3, 6, 7, 10, 11, etc).
 - c. Parity bit 4 covers all the bits positions whose binary representation includes a 1 in the third position from the least significant bit (4–7, 12–15, 20–23, etc).
 - d. Parity bit 8 covers all the bits positions whose binary representation includes a 1 in the fourth position from the least significant bit bits (8–15, 24–31, 40–47, etc).
 - e. In general, each parity bit covers all bits where the bitwise AND of the parity position and the bit position is non-zero.
5. Check for even parity set a parity bit to 1 if the total number of ones in the positions it checks is odd. Set a parity bit to 0 if the total number of ones in the positions it checks is even.



6. Stop

At receiver side:

1. Start the program
2. For all the parity bits we will check the number of 1's in their respective bit positions.
 - a. For R_1 : bits 1, 3, 5, 7, 9, 11. We can see that the number of 1's in these bit positions are 3 and that's even so we get a 1 for this.
 - b. For R_2 : bits 2,3,6,7,10,11 . We can see that the number of 1's in these bit positions are 3 and that's odd so we get a 1 for this.
 - c. For R_4 : bits 4, 5, 6, 7 . We can see that the number of 1's in these bit positions are 1 and that's odd so we get a 1 for this.
 - d. For R_8 : bit 8,9,10,11 . We can see that the number of 1's in these bit positions are 2 and that's even so we get a 0 for this.



3. The bits give the binary number 0111 whose decimal representation is 7. Thus, bit 7 contains an error. To correct the error the 7th bit is changed from 0 to 1.
4. Stop

Lab Exercises

1. Write a C program to transmit the data (binary values) from the sender side to receiver. The receiver side has to check whether the transmit data is corrupt or not using the parity technique – odd or even parity.
2. Write a C program to transmit the data (set of characters) from the sender side to receiver. The receiver side must check whether the transmit data is corrupted or not corrupted using the CRC polynomial techniques – CRC 12, CRC 16, CRC CCIP.
3. Implement the Hamming code method to detect the error in transmitted data and correct the error bit in the transmitted data.

