

YASHVI PATEL

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CAREER OBJECTIVE:

To obtain an entry-level position in embedded systems where I can apply my knowledge of C programming, microcontrollers, and hardware-software integration, while continuously learning and contributing to the development of reliable and efficient embedded solutions.

ACADEMIC DETAILS:

Vishwakarma Government Engineering college, Ahmedabad

Bachelor of Electronics and communication Engineering | In Progress |

Year of

Completion: 2026 | Score: 7.50 CGPA

Higher secondary board

N. M. Nootan Sarva Vidhyalaya, Visnagar | GSEB Board | Year-2022 | Score: 69.50%

Secondary board

N. M. Nootan Sarva Vidhyalaya, Visnagar | GSEB Board | Year-2020 | Score: 86.16%

SKILLS:

Programming & HDL Languages: C, Verilog

Tools and IDEs: Visual Studio Code, Arduino IDE, Atmel studio, Vivado, Proteus

Core concepts: Digital Electronics, Communication Protocols, Understanding of RTOS and Embedded System, RTL Design & Verification (Verilog)

OS: Windows10/11, Linux - basic command-line usage

Personal Competencies: Attention to detail, Analytical thinking, Willingness to learn new tools

PROJECTS & WORK:

Solar Tracking System using Arduino UNO

Role: Idea Development, Writing Code, Integrating Hardware Problem

Tools used: Arduino IDE, Proteus

Outcome: The project built a solar tracking system using Arduino Uno that moves the solar panel to follow the sun during the day. It uses light sensors to find the sun and servo motors to turn the panel. This helps the panel get more sunlight and produce more energy than a fixed panel.

Temperature Monitoring System using ATmega32

Role: Writing Code, Worked in a team to assemble and integrate hardware components

Tools Used: ATmega32 microcontroller, LM35 temperature sensor, 16x2 LCD, Embedded C (AVR-GCC), AVR Studio/Proteus for simulation, hardware prototyping kit.

Outcome: Successfully designed a working prototype that accurately sensed and displayed temperature, provided threshold-based alerts, and demonstrated proficiency in microcontroller programming and hardware interfacing.