Tutorial-8 Winter 2024 Basic Electronics (ECE113)

Q1: In Figure-1, find the output voltage (V_o).

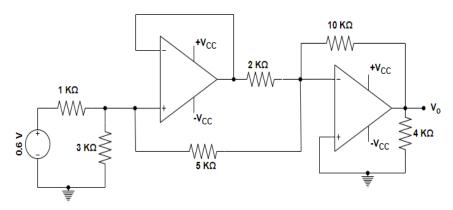


Figure 1

Q2: Find output voltage v_o in the given following circuit (Figure-2).

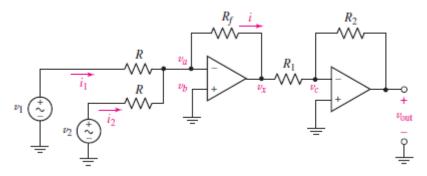
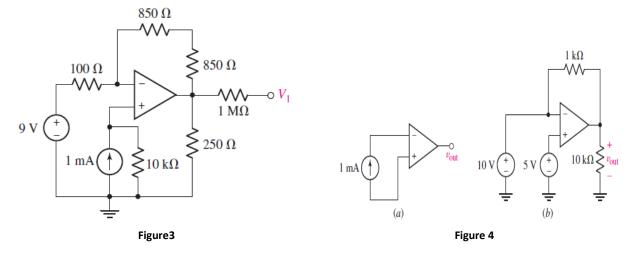


Figure 2

 $\underline{\mathbf{Q3}}$: Find output voltage V_1 in the given following circuit (Figure-3).



Q4: Explain what is wrong with each diagram in Figure-4 if the two op amps are known to be perfectly ideal.