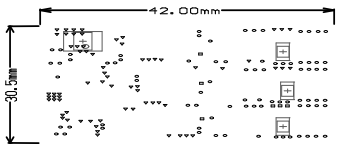
PCB Dimentions: 42mm x 30.5mm



LAYER STACK TABLE

Layer	Name	Material	Thickness
	Top Overlay		
	Top Solder	Solder Resist	0.010mm
1	Top S.Layer	Copper	0.035mm
	Dielectric 2	7628H	0.200mm
2	GND	Copper	0.018mm
	Dielectric 1	FR-4	1.065mm
3	VCC	Copper	0.018mm
	Dielectric 3	7628H	0.200mm
4	Bottom S.Layer	Copper	0.035mm
	Bottom Solder	Solder Resist	0.010mm
	Bottom Overlay		
Total board thickness:			1.691mm

DRILL TABLE

Symbol	Count	Hole Size	Plated	Hole Type	Drill Layer Pair	Via/Pad
□	6	0.200mm (7.87mil)	PTH	Round	Top S.Layer - Bottom S.Layer	Via
◁	68	0.300mm (11.81mil)	PTH	Round	Top S.Layer - Bottom S.Layer	Via
○	81	0.400mm (15.75mil)	PTH	Round	Top S.Layer - Bottom S.Layer	Via
	155 Total					

FABRICATION NOTES

1. FABRICATE PER ANSI/IPC-A-600,IPC-QE-605, IPC-4101,IPC-4552 AND IPC-SM-840 SPECIFICATIONS.
2. MATERIAL: BASE MATERIAL LAMINATED EPOXY GLASS NEMA GRADE FR-4, NOM .062 , COLOR NATURAL. NOM 1 OZ COPPER WEIGHT. THICKNESS .070 MAX AFTER PLATING AND FINISHING. PREFER ROHS COMPLIANT PER IPC-4101 SLASH SHEETS #26 OR #83 OR #98 WITH MINIMUM TG 135 DEGREE C OR HIGHER, TD 300 DEGREE C OR HIGHER AND FLAME RATED UL 94V-0.
3. ALL DIMENTIONS ARE IN INCHES UNLESS OTHERWISE SPECIFIED
4. DIMENSIONAL TOLERANCES ARE: .XX=+/- .01; .XXX=+/-.005;
5. TOTAL THICHNESS OF PCB AFTER PLATING SHALL BE 0.0618 +/- 0.005
6. SOLDERMASK BOTH SIDES OF BOARD OVER BARE COPPER WITH MATERIAL PER ANSI/IPC-SM-840, COLOR SHALL BE GREEN AND SOLVENT FREE
7. APPLY SOLDER PLATING WITH HOT-AIR LEVELING TO EXPOSED COPPER BOTH SIDES. LEAD-FREE SOLDER PREFERRED; LEADED SOLDER ACCEPTABLE.
8. APPLY SILKSCREEN TO TOP SIDE OF BOARD USING NON-CONDUCTIVE WHITE EPOXY INK. NO INK SHALL BE ON EXPOSED PADS
9. SEE SEPARATE DRILL FILE FOR HOLE LOCATIONS.
10. HOLE LOCATIONS SPECIFIED IN SEPARATE DRILL FILE TAKE PRECEDENCE OVER THIS DWG AND ARTWORK. CONTACT IIT BHUBANESWAR TO RESOLVE DIMENSION CONFLICTS BETWEEN DWG AND ARTWORK. NO ALTERATIONS TO GERBER FLES WITHOUT PRIOR CONSENT FROM IIT BHUBANESWAR
11. HOLE SIZES ARE SPECIFIED AS FINAL DIMENSIONS AFTER PLATING AND FINISHING. UNLESS OTHERWISE SPECIFIED ALL HOLES TO BE PLATED.
12. VENDOR TO PLATE HOLES AND EXPOSED PADS. VENDOR TO SPECIFY TYPE OF PLATING MATERIAL TO BE USED WHEN ACCEPTING ORDER.
13. IT IS VENDOR'S RESPONSIBILITY TO SELECT THE BASE MATERIAL TO YIELD THE SPECIFIED IMPEDENCE CHARACTERISTICS TO WITHIN +/-10%
14. ALL HOLES SHALL BE LOCATED WITHIN 0.003 DIAMETER OF TRUE POSITION OR OTHER TRUE POSITION
15. WARP OR TWIST OF BOARD SHALL NOT EXCEED 0.01 INCH PER INCH
16. REMOVE ALL BURRS AND BREAK SHARP EDGES 0.015 MAX
17. PLATED THRU HOLES & EXPOSED PADS SHALL BE TIN/LEAD PLATED 0.0003 TO 0.0005 THICK
18. ONLY VENDOR IDENTIFICATION MARK & ORDER NUMBER CAN BE ADDED ONLY AT THE SPECIFIED LOCATION (UNDER THE IC). AFTER ASSEMBLY IT SHOULD NOT APPEAR OUTSIDE
19. MANUFACTURER SHOULD NOT ADD ANY OTHER DATA EXCEPT ABOVE MENTIONED LIKE NAME, LOGO, DATE CODE, UL LISTING OR ANY OTHER MARKING TO ANY VISIBLE LAYER
20. AFTER ASSEMBLY TRIM SOLDER FILLETS AND COMPONENT LEADS TO 0.06 (MAX) BEYOND BOARD SURFACE ON BOTTOM SIDE.
21. BOARDS MUST PASS VISUAL INSPECTION PER IPC-A-600 CLASS 2
22. FINISHED BOARD MUST MEET UL94V-0 RATING AND ROHS COMPLIANCE
23. DOCUMENTATION THAT MUST BE DELIVERED WITH BOARDS:
    - CROSS SECTION REPORT (SPACING BETWEEN COPPER LAYERS AND COPPER THICHNESS)
    - ELECTRICAL TEST CERTIFICATION OF COMPILANCE (ACC. TO IPC-ET-652 CLASS 2)
    - CERTIFICATION OF COMPILANCE (BOARD HAS BEEN MANUFACTURED TO DRAWING REQUIREMENTS)
    - ROHS CERTIFICATION OF COMPILANCE